User's Manual



UTAdvanced Series Communication Interface (Open Network) User's Manual

IM 05P07A01-02EN

vigilantplant.



Introduction

Thank you for purchasing the UTAdvanced Series digital indicating controller (hereinafter referred to as "UTAdvanced").

This manual describes how to use the communication functions (PROFIBUS-DP, DeviceNet and CC-Link communication) of the UTAdvanced. Read this manual thoroughly beforehand to ensure correct use of the UTAdvanced.

Note that the manuals for the UTAdvanced comprise the following documents.

To use the UTAdvanced, you must have a sufficient knowledge of the communication specifications of the host computer that the UTAdvanced is connected to, communication hardware, the program language used for communication, and other communication-related information.

Printed manual

Manual Name	Manual Number	Description
UT75A Operation Guide	IM 05P01B41-11EN	This manual describes the basic operation method.
UT55A/UT52A Operation Guide (for Standard model)	IM 05P01C31-11EN	This manual describes the basic operation method.
UT55A/UT52A Operation Guide (for Detailed model)	IM 05P01C31-15EN	This manual describes the basic operation method.
UT35A/UT32A Operation Guide (for Standard model)	IM 05P01D31-11EN	This manual describes the basic operation method.
UT35A/UT32A Operation Guide (for Detailed model)	IM 05P01D31-15EN	This manual describes the basic operation method.
UP55A Operation Guide (for Standard model)	IM 05P02C41-11EN	This manual describes the basic operation method.
UP55A Operation Guide (for Detailed model)	IM 05P02C41-15EN	This manual describes the basic operation method.
UP35A/UP32A Operation Guide (for Standard model)	IM 05P02D41-11EN	This manual describes the basic operation method.
UP35A Operation Guide (for Detailed model)	IM 05P02D41-15EN	This manual describes the basic operation method.
UM33A Operation Guide	IM 05P03D21-11EN	This manual describes the basic operation method.

• Electronic manuals

Manual Name	Manual Number	Description
UT75A Operation Guide	IM 05P01B41-11EN	This is identical to the printed manual.
UT55A/UT52A Operation Guide (for Standard model)	IM 05P01C31-11EN	This is identical to the printed manual.
UT55A/UT52A Operation Guide (for Detailed model)	IM 05P01C31-15EN	This is identical to the printed manual.
UT35A/UT32A Operation Guide (for Standard model)	IM 05P01D31-11EN	This is identical to the printed manual.
UT35A/UT32A Operation Guide (for Detailed model)	IM 05P01D31-15EN	This is identical to the printed manual.
UP55A Operation Guide (for Standard model)	IM 05P02C41-11EN	This is identical to the printed manual.
UP55A Operation Guide (for Detailed model)	IM 05P02C41-15EN	This is identical to the printed manual.
UP35A/UP32A Operation Guide (for Standard model)	IM 05P02D41-11EN	This is identical to the printed manual.
UP35A Operation Guide (for Detailed model)	IM 05P02D41-15EN	This is identical to the printed manual.
UM33A Operation Guide	IM 05P03D21-11EN	This is identical to the printed manual.
UT75A User's Manual	IM 05P01B41-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.
UT55A/UT52A User's Manual	IM 05P01C31-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.
UT35A/UT32A User's Manual	IM 05P01D31-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.

9th Edition : April 2015 (YK)

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IM 05P07A01-02EN

Manual Name	Manual Number	Description
UP55A User's Manual	IM 05P02C41-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.
UP35A/UP32A User's Manual	IM 05P02D41-01EN	This manual describes the usage of all functions except the ladder sequence and communication functions.
UM33A User's Manual	IM 05P03D21-01EN	This manual describes the usage of all functions except the communication functions.
UTAdvanced Series Communication Interface (RS-485, Ethernet) User's Manual	IM 05P07A01-01EN	This manual describes how to use the UTAdvanced in Ethernet and serial communications. For communication wiring, see the Operation Guide or User's Manual.
UTAdvanced Series Communication Interface (Open Network) User's Manual	IM 05P07A01-02EN	This manual. It describes how to use the UTAdvanced in PROFIBUS-DP/DeviceNet/CC-Link communication. For communication wiring, see the Operation Guide or User's Manual.
LL50A Parameter Setting Software Installation Manual	IM 05P05A01-01EN	This manual describes how to install and uninstall the LL50A.
LL50A Parameter Setting Software User's Manual	IM 05P05A01-02EN	This manual describes how to use the LL50A, ladder sequence function, peer-to-peer communication, and network profile creating function.

^{*} User's Manual can be downloaded from a website.



Intended Readers

This manual is intended for people familiar with the functions of the UTAdvanced such as control engineers and personnel in charge of the maintenance of instrumentation and control equipment.

Notice

- The contents of this manual are subject to change without notice as a result of continuing improvements to the instrument's performance and functions.
- Every effort has been made to ensure accuracy in the preparation of this manual. Should any errors
 or omissions come to your attention, however, please inform Yokogawa Electric's sales office or sales
 representative.
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Symbols Used in This Manual



This symbol is used on the instrument. It indicates the possibility of injury to the user or damage to the instrument, and signifies that the user must refer to the user's manual for special instructions. The same symbol is used in the user's manual on pages that the user needs to refer to, together with the term "WARNING" or "CAUTION."

WARNING

Calls attention to actions or conditions that could cause serious or fatal injury to the user, and indicates precautions that should be taken to prevent such occurrences.

CAUTION

Calls attention to actions or conditions that could cause injury to the user or damage to the instrument or property and indicates precautions that should be taken to prevent such occurrences.

Note

Identifies important information required to operate the instrument.



Indicates related operations or explanations for the user's reference.

Indicates a character string displayed on the display.

Setting Display

Indicates a setting display and describes the keystrokes required to display the relevant setting display.

Setting Details

Provides the descriptions of settings.

Description

Describes restrictions, etc. regarding a relevant operation.

Procedure

Describes procedures.

How to Use This Maunual

Usage

First read through the Operation Guide to understand the basic operation and then read this manual.

This user's manual is organized into Chapters 1 to 4 as shown below.

Chapter	Title and Description
4	Overview
1	Describes types of communication and communication specifications.
2	Setting Communication Functions
2	Describes communication parameter setting items.
	Description of PROFIBUS-DP/DeviceNet Communication (for UTAdvanced with PROFIBUS-DP/DeviceNet
3	Communication)
	Describes how to use PROFIBUS-DP/DeviceNet communication.
	Description of CC-Link Communication (for UTAdvanced with CC-Link Communication)
	Describes how to use CC-Link communication.

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Revision Information

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1.1 Open Network

In UTAdvanced, PROFIBUS-DP, DeviceNet and CC-Link are collectively called Open Network.

UTAdvanced with Open Network communication runs as a slave controller for Open Network communication. Mainly PLC is used as a master controller for Open Network communication.

Furthermore, UTAdvanced with Open Network communication is equipped with a Open Network communication terminal and RS-485 terminal. The RS-485 terminal allows it to run also as a master controller for Modbus communication. UTAdvanced with RS-485 communication is used as a slave controller for Modbus communication.

UTAdvanced with Open Network communication is able to handle its own parameters, and the parameters of another UTAdvanced with RS-485 communication connected via the RS-485 terminal.

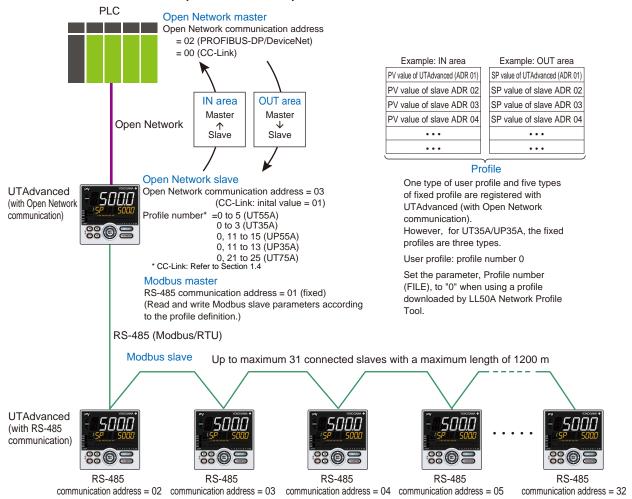
Communication function	Protocol	Connectable device	Terminal position	Suffix code
PROFIBUS-DP communication	PROFIBUS-DP			PROFIBUS-DP: Type 3 = 4
DeviceNet communication	DeviceNet	' ' /	Rear Open Network	DeviceNet:
CC-Link communication	CC-Link		(E3-terminal area)	Type 3 = 5 CC-Link:
RS-485 communication	Modbus/RTU	UTAdvanced		Type 3 = 4

► Terminal position: UTAdvanced Operation Guide or User's Manual

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1.1.1 Explanation of Terms

Example: Overview of Open Network Communication Connection



Open network master

(PROFIBUS-DP master, DeviceNet master, CC-Link master)

An open network master is a controller which exchanges information with slaves periodically and which is a PLC or PC (which is called a class 1 master in PROFIBUS-DP).

An engineering or configuration device is also an open network master which is a PC on which configuration software is installed or software (which is called a class 2 master in PROFIBUS-DP.)

Open Network slave/Modbus master

(PROFIBUS-DP slave, DeviceNet slave, CC-Link slave)

This is an input and output device that is accessed by the master. UTAdvanced (with Open Network communication) runs as a Open Network slave.

Furthermore, UTAdvanced (with Open Network communication), which runs as a Open Network slave, also runs as a Modbus master using the RS-485 terminal.

It reads and writes the parameters of Modbus slaves, which are connected via RS-485 communication, according to the profile definition.

Modbus slave

This is UTAdvanced (with RS-485 terminal). Up to 31 slaves can be connected.

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IN area (UTAdvanced \rightarrow PLC)

This is an area for a PLC to refer to the slave data. Parameters of the Open Network and Modbus slaves that are defined in the profile are always updated.

OUT area (PLC \rightarrow UTAdvanced)

This is an area for a PLC to rewrite the slave parameters. When the OUT area is rewritten, the corresponding parameters of the Open Network slave and Modbus slaves are also rewritten.

Profile

This defines how to assign the slave parameters to the IN and OUT areas.

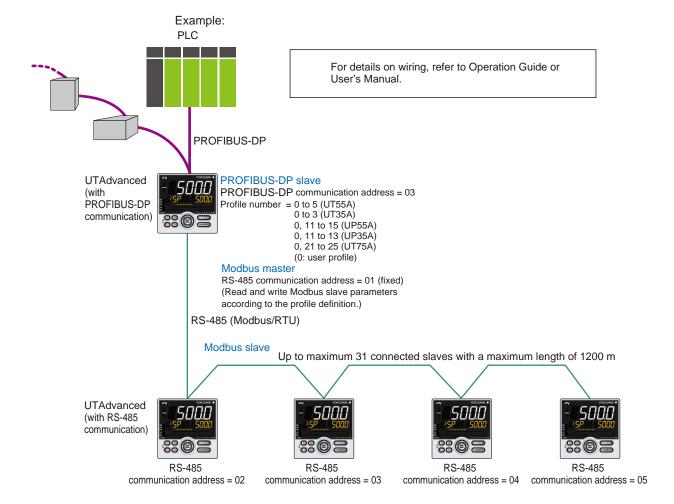
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1.2 PROFIBUS-DP Communication

1.2.1 Communication Specifications of UTAdvanced (with PROFIBUS-DP Communication)

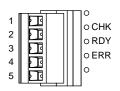
	Communication specifications
Туре	Slave
Supported on version	DP V0
Baud rate	9.6k, 19.2k, 93.75k, 187.5k, 0.5M, 1.5M, 3M, 6M, 12M, 45.45k bps, AUTO*
Transmission distance	1200m (9.6k, 19.2k, 45.45k, 93.75k bps), 1000m (187.5k bps), 400m (0.5M bps), 200m (1.5M bps), 100m (3M, 6M, 12M bps)
Communication address	0 to 125 It is recommended to use 3 to 125 for general use of PROFIBUS-DP.
Max. size of IN area	244 byte
Max. size of OUT area	244 byte
Terminal	5-pin terminal block
LED	CHK: Hardwea (red)
	RDY: Network Status (green)
	ERR: Communication failure (red)

^{*:} AUTO automatically sets the baud rate to that of the host controller (PROFIBUS-DP master).



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1.2.2 LEDs (on Rear Panel)



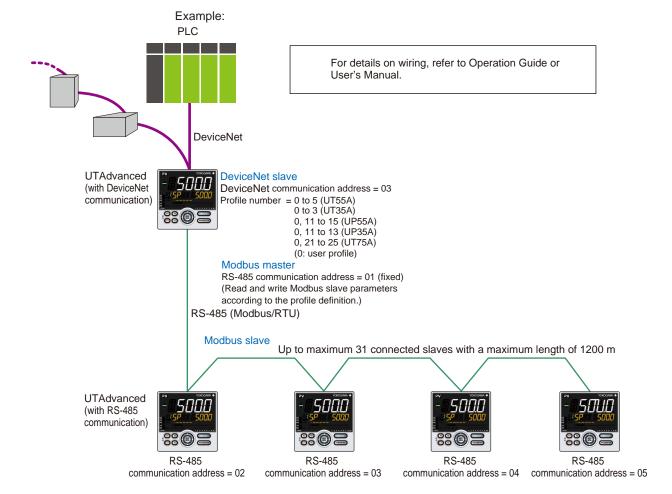
LE	:D	Description	Remedy	Modbus communication
	Unlit	Normal	_	Normal
CHK (red)	Red, lit	User profile error	Download the user profile again.	Communication interruption
PDV (groon)	Unlit	No power, or Communication failure	Check the power supply and connection condition.	
RDY (green)	Green, lit	Normal Communicating successfully	_	
	Unlit	Normal	_	
ERR (red)	Red, lit	Not connected	Check the wiring to the PROFIBUS-DP master and configuration.	_
·	Red, flashing	Communication failure	Check the condition of the connection to the PROFIBUS-DP master.	

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1.3 DeviceNet Communication

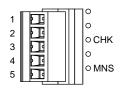
1.3.1 Communication Specifications of UTAdvanced (with DeviceNet Communication)

	Communication specifications
Туре	Slave
Supported on version	-
Baud rate	125k, 250k, 500k bps
Transmission distance	500m (125k bps), 250m (250k bps), 100m (500k bps)
Communication address	0 to 63
Max. size of IN area	254 byte
Max. size of OUT area	254 byte
Terminal	5-pin terminal block
LED	CHK: Hardwea (red)
	MNS: Module Status, Network Status (red/green)



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1.3.2 LEDs (on Rear Panel)



LE	D	Description	Remedy	Modbus communication
	Unlit	Normal	-	Normal
CHK (red)	Red, lit	User profile error	Download the user profile again.	Communication interruption
	Unlit	No electricity	Check the power supply and	
	Offilit	Not on-line	connection condition.]
	Green, lit	Normal. Communicating successfully	-	_
	Green, flashing	Not connected	Check the connection condition.	
MNS (green/red)	Red, lit	Critical link failure	When not communication, it is communication address duplication or bus-off error. If the node address is duplicated or a bus-off error has occurred, communication becomes impossible and the module stops operating. Remove the node address duplication or improve the network environment and restart the module. A bus-off error occurs when the error rate in the network is very high. If a hardware malfunction occurs, it may be necessary to repair or replace the unit.	_
	Red, flashing	Communication timeout	Check the power supply and connection condition.	
	Green/red, flashing	At power-on	_	Communication interruption
	liasilily	Communication faulted	Check the connection condition.	_

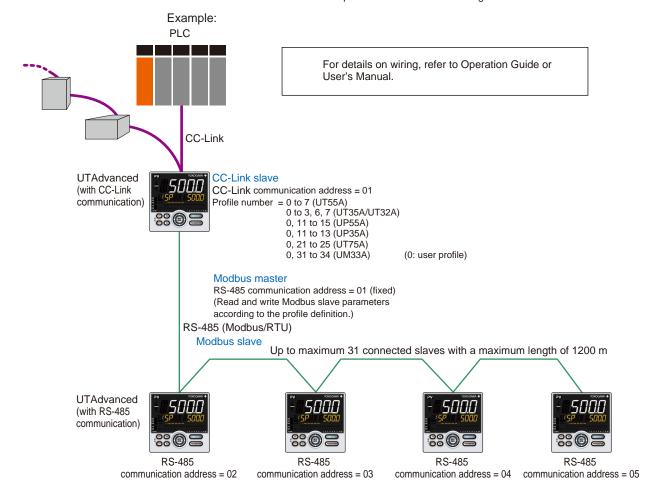
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1.4 CC-Link Communication

1.4.1 Communication Specifications of UTAdvanced (with CC-Link Communication)

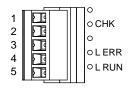
	Communication specifications
Туре	Slave
Supported on version	Ver.1.10 and Ver.2.00
Baud rate	156k, 625k, 2.5M, 5M, 10M bps
Transmission distance	1.2km (156k bps), 600m (625k bps), 200m (2.5M bps), 150m (5M bps), 100m (10M bps) When used optical repeater: 7.6 km (156k) to 4.3 km (10M)
Communication address	0 to 64 *
Max. size of IN area	4-station occupied x8 setting: 368 byte (128 word + 896 bit)
Max. size of OUT area	4-stations occupied x8 setting: 368 byte (128 word + 896 bit)
Terminal	5-pin terminal block
LED	CHK: Hardwea (red) L ERR: Communication failure (red) L RDY: Network Status (green)

^{*:} If the total number of addresses and occupied stations defined in the profile exceeds 65, communication becomes not possible and the CHK LED lights red.



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1.4.2 LEDs (on Rear Panel)



LE	ED .	Description	Remedy	Modbus communication	
	Unlit	Normal	_	Normal	
		User profile error	Download the user profile again.		
CHK (red)	l :t (no al)		Change the setting so that	Communication	
, ,	Lit (red)	Address error	the total number of addresses	interruption	
		Address error	and occupied stations do not		
			exceed 65.		
	Unlit	No carrier detected/	Check the power supply and		
L RUN (green)		Communication timeout	connection condition.		
L KON (green)	Lit (green)	Normal			
	Lit (green)	Communicating successfully	_		
	Unlit	Normal	_		
L ERR (red)	Lit (red)	Communication failure (CRC error)	Check that there is no problem with the network cable, repeater, etc.	_	

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1.5 RS-485 Communication (Modbus Master/Slave)

1.5.1 Communication Specifications

	Communication specifications
Communication interface	RS-485
Communication method	2-wire type
Connection method	1:n multi-drop method (n = max. 31)
Baud rate	9600, 19200, 38400 bps
Communication distance	Max. 1200 m
Protocol	Fixed at Modbus/RTU
Data length	Fixed at 1 bits
Parity bit	Fixed at EVEN
Stop bit	Fixed at 1 bit

Note.

The RS-485 communication address as a Modbus master is 1 (fixed).

1.5.2 Connected Controller

UTAdvanced with RS-485 communication can be connected as a Modbus slave.

1.5.3 Conditions of Connected Controller

The RS-485 communication conditions of a Modbus slave are the same as those of the Modbus master.

The RS-485 communication address of a Modbus slave is set from 2 to 32 without duplication.

Note

The update cycle of the data that is periodically read from each controller becomes longer as the number of connected controllers increases. If controllers that are not connected are registered in the profile, data updating in the first cycle will take a long time. The update cycle is optimized in and after the second cycle, because data is collected by skipping the controllers that are not connected.

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2.1 Setting Parameters

2.1.1 Setting PROFIBUS-DP Communication (for PROFIBUS-DP Slave/Modbus Master)

Setting Display

Parameter Setting Display



Operation Display > PARAMETER and Left arrow keys simultaneously for 3 seconds (to the [CTL] Menu Display) > Right arrow key (to the [PROF] Menu Display in E3) > SET/ENTER key (The setting parameter is displayed.)

(E3 indicating the terminal area is displayed on Group display.)

Setting Details

Parameter symbol	Group display	Name	Setting range		Menu symbol	Initial value	
- Cymae:	a.op.uy		9.6k bps	9.6K		7 41 41	
			19.2k bps	19.2K			
			93.75k bps	93.75K			
			187.5k bps	187.5K			
			0.5M bps	0.5M			
BR		Baud rate	1.5M bps	1.5M	PROF	AUTO	
			3M bps	3M			
			6M bps	6M			
			12M bps	12M			
			AUTO	AUTO			
			45.45k bps	45.45K			
ADR		Address	0 to 125		PROF	3	
			9600 bps	9600			
BPS		Baud rate	19200 bps	19200		38400	
			38400 bps	38400	PROF		
			User profile *1	0			
			Simple PID control, 3 connected slaves (for UT)	1			
	E3		Simple PID control, 5 connected slaves (for UT)	2			
			Simple PID control, 8 connected slaves (for UT)	3	PROF		
			Cascade control, 3 connected slaves (for UT) *3	4	1		
			Cascade control, 5 connected slaves (for UT) *3	5	1		
			Simple PID control, 2 connected slaves (for UP)	11			
		Profile	Simple PID control, 4 connected slaves (for UP)	12			
FILE		number	Simple PID control, 1 connected slave, with proguram pattern setting (for UP)	13		0	
		Humber	Cascade control, 2 connected slaves (for UP) *4	14	PROF		
			Cascade control, 1 connected slavee, with proguram pattern setting (for UP) *4	15			
			Simple PID control, 3 connected slaves (for UT75A) *5	21			
			Simple PID control, 5 connected slaves (for UT75A) *5	22			
			Simple PID control, 8 connected slaves (for UT75A) *5	23			
			Cascade control / dual-loop control, 3 connected slaves (for UT75A) *5	24			
			Simple PID control / cascade control / dual-loop control, 1 connected	25			
			slave, with program pattern setting (for UT75A) *5				
			OFF	OFF			
SCAN		Automatic	1 minute	1M			
*2		rescan	10 minutes	10M		OFF	
-		time	30 minutes	30M			
SCAN *2				60 minutes	60M		

^{*1:} The initial value (Simple PID Control, 2 connected slaves) of the user profile is set.

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^{▶ &}quot;3.4.2 Types of Profile" in this manual

^{*2:} This parameter may not be displayed depending on the parameter display level (LEVL) setting.

[►] UTAdvanced Operation Guide or User's Manual

*3: The profile number 4 (Cascade control, 3 connected slaves) and the profile number 5 (Cascade control, 5 connected slaves) can be set for UT55Aonly.

These numbers cannot be set for UT35A.

*4: The profile number 14 (Cascade control, 2 connected slaves) and the profile number 15 (Cascade control, 1 connected slave) can be set for UP55A only.

These numbers cannot be set for UP35A.

*5: The profile number 21 to 25 can be set for UT75A only.

Description

• Baud rate (BR for PROFIBUS-DP)

Set the same baud rate as that for the PROFIBUS-DP master controller to be connected. The unit is bps (bits per second). Selecting AUTO automatically sets the same baud rate as that for the master controller.

Address

This is the address of PROFIBUS-DP slave.

An arbitrary number from 0 to 125 can be set. (A duplicate number cannot be set on the same network.)

It is recommended to use 3 to 125 for general use of PROFIBUS.

Baud rate (BPS for RS458 (Modbus/RTU) communication)
 Set the same baud rate as that of the Modbus slave controllers to be connected. The unit is bps (bits per second).

Profile number

Set the profile number to be used.

For UT55A: 0 to 5 For UT35A: 0 to 3 For UP55A: 0, 11 to 15 For UP35A: 0, 11 to 13 For UT75A: 0, 21 to 25 (0: user profile)

Automatic rescan time

Set the time interval (cycle) at which a retry is automatically attempted to establish communication with unconnected slave controllers that are assigned in the profile.

The RS-485 communication address as a Modbus master is 1 (fixed).

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2.1.2 Setting DeviceNet Communication (for DeviceNet Slave/Modbus Master)

Setting Display

Parameter Setting Display



Operation Display > PARAMETER and Left arrow keys simultaneously for 3 seconds (to the [CTL] Menu Display) > Right arrow key (to the [DNET] Menu Display in E3) > SET/ENTER key (The setting parameter is displayed.)

(E3 indicating the terminal area is displayed on Group display.)

Setting Details

Parameter symbol	Group display	Name	Setting range		Menu symbol	Initial value	
			125k bps	125K		AUTO	
BR		Baud rate	250k bps	250K			
			500k bps	500K			
ADR		Address	0 to 63			63	
			9600 bps	9600			
BPS		Baud rate	19200 bps	19200		38400	
			38400 bps	38400			
			User profile *1	0			
			Simple PID control, 3 connected slaves (for UT)	1]		
			Simple PID control, 5 connected slaves (for UT)	2]		
			Simple PID control, 8 connected slaves (for UT)	3	DNET 0		
			Cascade control, 3 connected slaves (for UT) *3	4			
			Cascade control, 5 connected slaves (for UT) *3	5			
			Simple PID control, 2 connected slaves (for UP)	11			
	E3	Profile	Simple PID control, 4 connected slaves (for UP)	12			
FILE		number	Simple PID control, 1 connected slave, with proguram pattern setting (for UP)	13		0	
		Hamber	Cascade control, 2 connected slaves (for UP) *4	14			
			Cascade control, 1 connected slavee, with proguram pattern setting (for UP) *4	15			
			Simple PID control, 3 connected slaves (for UT75A) *5	21			
			Simple PID control, 5 connected slaves (for UT75A) *5	22			
			Simple PID control, 8 connected slaves (for UT75A) *5	23			
			Cascade control / dual-loop control, 3 connected slaves (for UT75A) *5	24			
			Simple PID control / cascade control / dual-loop control, 1 connected slave, with program pattern setting (for UT75A) *5	25			
			OFF	OFF			
CCAN		Automatic	1 minute	1M			
		rescan	10 minutes	10M		OFF	
2		time	30 minutes	30M			
SCAN *2				60 minutes	60M]	

- *1: The initial value (Simple PID Control, 2 connected slaves) of the user profile is set.
 - ▶ "3.4.2 Types of Profile" in this manual
- *2: This parameter may not be displayed depending on the parameter display level (LEVL) setting.
 - ► UTAdvanced Operation Guide or User's Manual
- *3: The profile number 4 (Cascade control, 3 connected slaves) and the profile number 5 (Cascade control, 5 connected slaves) can be set for UT55Aonly. These numbers cannot be set for UT35A.
- *4: The profile number 14 (Cascade control, 2 connected slaves) and the profile number 15 (Cascade control, 1 connected slave) can be set for UP55A only.

 These numbers cannot be set for UP35A.
- *5: The profile number 21 to 25 can be set for UT75A only.

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Description

• Baud rate (BR for DeviceNet)

Set the same baud rate as that for the DeviceNet master controller to be connected. The unit is bps (bits per second).

Address

This is the address of DeviceNet slave.

An arbitrary number from 0 to 63 can be set. (A duplicate number cannot be set on the same network.)

Baud rate (BPS for RS458 (Modbus/RTU) communication)
 Set the same baud rate as that of the Modbus slave controllers to be connected. The unit is bps (bits per second).

Profile number

Set the profile number to be used.

For UT55A: 0 to 5 For UT35A: 0 to 3 For UP55A: 0, 11 to 15 For UP35A: 0, 11 to 13 For UT75A: 0, 21 to 25 (0: user profile)

Automatic rescan time

Set the time interval (cycle) at which a retry is automatically attempted to establish communication with unconnected slave controllers that are assigned in the profile.

Note:

The RS-485 communication address as a Modbus master is 1 (fixed).

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2.1.3 Setting CC-Link Communication (for CC-Link Slave/Modbus Master)

Setting Display

Parameter Setting Display



Operation Display > PARAMETER and Left arrow keys simultaneously for 3 seconds (to the [CTL] Menu Display) > Right arrow key (to the [CC-L] Menu Display in E3) > SET/ENTER key (The setting parameter is displayed.)

(E3 indicating the terminal area is displayed on Group display.)

UT52A/UT32A/UP32A/UM33A:

Operation Display > PARA and Left arrow keys simultaneously for 3 seconds (to the [CTL] Menu Display) > Right arrow key (to the [CC-L] Menu Display in E1) > SET/ENTER key (The setting parameter is displayed.) (E1 indicating the terminal area is displayed on Group display.)

Setting Details

Parameter symbol	Group display	Name	Setting range			Initial value
BR		Baud rate	156k bps	156K		AUTO
			625k bps	625K	1	
			2.5M bps	2.5M	1	
			5M bps	5M]	
			10M bps	10M	1	
ADR		Address 0 to 64 *1			7 !	1
			9600 bps	9600] [38400
BPS		Baud rate	19200 bps	19200	1	
			38400 bps	38400	1 2 CC-L	
			User profile *2	0		0
			Simple PID control, 3 connected slaves (for UT)	1		
			Simple PID control, 5 connected slaves (for UT)	2		
			Simple PID control, 8 connected slaves (for UT)	3		
			Cascade control, 3 connected slaves (for UT) *3	4		
	E3/E1	Profile number	Cascade control, 5 connected slaves (for UT) *3	5		
			Simple PID control, 1 connected slave (for UT)	6		
			Cascade control, 1 connected slave (for UT)	7		
			Simple PID control, 2 connected slaves (for UP)	11		
			Simple PID control, 4 connected slaves (for UP)	12		
			Simple PID control, 1 connected slave, with proguram pattern setting (for UP)	13		
FILE			Cascade control, 2 connected slaves (for UP) *5	14	1	
			Cascade control, 1 connected slave, with proguram pattern setting (for UP) *5	15		
			Simple PID control, 3 connected slaves (for UT75A) *6	21		
			Simple PID control, 5 connected slaves (for UT75A) *6	22		
			Simple PID control, 8 connected slaves (for UT75A) *6	23		
			Cascade control / dual-loop control, 3 connected slaves (for UT75A) *6	24		
			Simple PID control / cascade control / dual-loop control, 1 connected	25		
			slave, with program pattern setting (for UT75A) *6	25		
			UM33A, 3 connected slaves (for UM33A) *7	31		
			UM33A, 5 connected slaves (for UM33A) *7	32		
			UM33A, 8 connected slaves (for UM33A) *7	33		
			UM33A, 1 connected slave (for UM33A) *7	34		
			OFF	OFF		OFF
SCAN			1 minute	1M		
*3		rescan	10 minutes	10M		
ا		time	30 minutes	30M		
			60 minutes	60M		

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- *1: If the total number of addresses and occupied stations defined in the profile exceeds 65, communication becomes not possible and the CHK LED lights red.
- *2: The initial value (Simple PID Control, 2 connected slaves) of the user profile is set.
 - "4.4.2 Types of Profile" in this manual
- *3: This parameter may not be displayed depending on the parameter display level (LEVL) setting.
 - ► UTAdvanced Operation Guide or User's Manual
- *4: The profile number 4 (Cascade control, 3 connected slaves) and the profile number 5 (Cascade control, 5 connected slaves) can be set for UT55Aonly.

These numbers cannot be set for UT35A.

- *5: The profile number 14 (Cascade control, 2 connected slaves) and the profile number 15 (Cascade control, 1 connected slave) can be set for UP55A only.
- These numbers cannot be set for UP35A.

 *6: The profile number 21 to 25 can be set for UT75A only.
- *7: The profile number 31 to 34 can be set for UM33A only.

Description

Baud rate (BR for CC-Link)

Set the same baud rate as that for the CC-Link master controller to be connected. The unit is bps (bits per second).

Address

This is the address of CC-Link slave.

An arbitrary number from 0 to 64 can be set. (A duplicate number cannot be set on the same network.)

Note

If the total number of addresses and occupied stations defined in the profile exceeds 65, communication becomes not possible and the CHK LED lights red.

- Baud rate (BPS for RS458 (Modbus/RTU) communication)
 Set the same baud rate as that of the Modbus slave controllers to be connected. The unit is bps (bits per second).
- Profile number

Set the profile number to be used.

For UT55A: 0 to 7

For UT52A: 0 to 3, 6

For UT35A: 0 to 3, 6

For UT32A: 0 to 3, 6

For UP55A: 0, 11 to 15

For UP35A: 0, 11 to 13

For UP32A: 0, 11 to 13 For UT75A: 0, 21 to 25

For UM33A: 0, 31 to 34

(0: user profile)

• Automatic rescan time

Set the time interval (cycle) at which a retry is automatically attempted to establish communication with unconnected slave controllers that are assigned in the profile.

Note

The RS-485 communication address as a Modbus master is 1 (fixed).

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2.1.4 Setting RS-485 Communication (Modbus Slave)

Setting Display

Parameter Setting Display



UT75A/UT55A/UT35A/UP55A/UP35A:

Operation Display > PARAMETER and Left arrow keys simultaneously for 3 seconds (to the [CTL] Menu Display) > Right arrow key (to the [R485] Menu Display in E3 or E4) > SET/ENTER key (The setting parameter is displayed.) (E3 or E4 indicating the terminal area is displayed on Group display.)

UT52A/UT32A/UP32A/UM33A:

Operation Display > PARA and Left arrow keys simultaneously for 3 seconds (to the [CTL] Menu Display) > Right arrow key (to the [R485] Menu Display in E1) > SET/ENTER key (The setting parameter is displayed.) (E1 indicating the terminal area is displayed on Group display.)

Setting Details

Parameter symbol	Group display	Name	Setting range		Menu symbol	Initial value
PSL	UT75A/ UT55A/	Protocol selection	Modbus (RTU)	MBRTU		MBRTU
BPS	UP55A:	Baud rate	9600 bps	9600		
	E3 or E4		19200 bps	19200		19200
	UT35A/		38400 bps *1	38400		
PRI	UP35A: E3 UT52A/ UT32A/ UP32A/ UM33A: E1	Parity	Even	EVEN	D 405	EVEN
STP		Stop bit	1 bit	1	R485	1
DLN		Data length	8 bit	8		8
ADR		Address	2 to 32	2 to 32		1

^{*1: 38400} bps can be specified when the UT75A/UT55A/UT35A/UP55A/UP35A suffix code Type 3 = 1 or UT52A/UT32A/Up32A/UM33A suffix code Type 2 = 1.

Description

- Protocol selection
 Set the protocol to be MBRTU.
- Baud rate

Set the same RS-485 communication baud rate as the UTAdvanced (Open Network slave/Modbus master) that the controller is to be connected to. The baud rate unit is bps (bits per second).

- Parity
 Set the EVEN.
- Stop bit
- Set the 1 bit.
- Data length
 Set the 8 bit.
- Address
 Set the 2 to 32.

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2.2 Setting Write Enable for UTAdvanced

Writing to registers via all communication protocols can be permitted and prohibited. However, communication using the light-loader (on the front panel) is possible. Writing to registers via communication is possible only when the parameter COM.W (communication write enable/disable) in the KLOC menu is set to OFF (enable).

Setting Display

Parameter Setting Display



UT75A/UT55A/UT35A/UP55A/UP35A:

Operation Display > PARAMETER and Left arrow keys simultaneously for 3 seconds (to the [CTL] Menu Display) > Right arrow key (to the [KLOC] Menu Display) > SET/ENTER key (The COM.W parameter is displayed.)

UT52A/UT32A/UP32A/UM33A:

Operation Display > PARA and Left arrow keys simultaneously for 3 seconds (to the [CTL] Menu Display) > Right arrow key (to the [KLOC] Menu Display) > SET/ENTER key (The COM.W parameter is displayed.)

Setting Details

Parameter symbol	Name	Setting range	Menu symbol	Initial value
COM.W	Communication write enable/ disable	OFF: Enable (0)	KLOC	OFF (0)
COWLVV		ON: Disable (1)	KLOC	

Note: Figures in parentheses "()" are values to be set when performing communication.

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3.1 Overview

PROFIBUS/DeviceNet is an open field bus standard used in various applications for factory automation and process automation.

PROFIBUS-DP: IEC61158 DeviceNet: IEC62026

PROFIBUS-DP/DeviceNet (Decentralized Periphery) is used for communication between PLCs and remote I/O, enabling high-speed data transmission.

Note

For details of PROFIBUS specifications and information, see the documents published from the PROFIBUS Organization in respective regions.

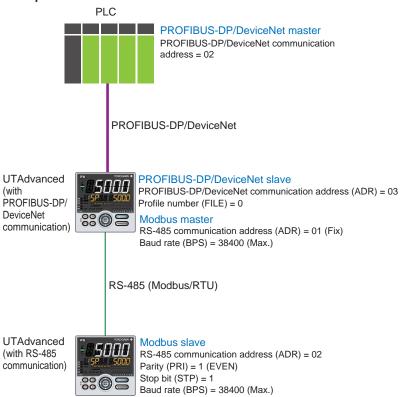
PROFIBUS International: http://www.profibus.com/

Note:

For details of DeviceNet specifications and information, see the documents published from the ODVA $\,\mathrm{Inc.}\,$

ODVA Inc.: http://www.odva.org/

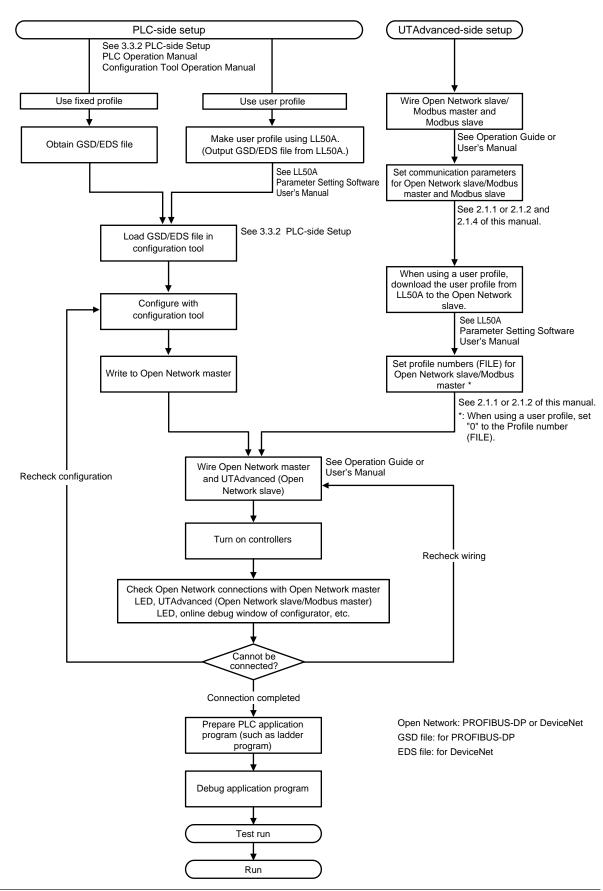
Example: PROFIBUS-DP/DeviceNet Communication Connection



Note: The maximum baud rate may be 19200 bps by the model.

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3.2 Workflow



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3.3 Setting Up Connection with Master

3.3.1 UTAdvanced-side Setup

Wiring

For wiring, see UTAdvanced Operation Guide or User's Manual.

Setting communication parameters

For setting parameters, see 2.1.1 or 2.1.2 and 2.1.4 of this manual.

Downloading User Profile

When using a user profile, download the user profile via LL50A.

For the procedure of download, see LL50A Parameter Setting Software User's Manual.

3.3.2 PLC-side Setup

GSD File (PROFIBUS-DP), EDS file (DeviceNet)

To connect UTAdvanced to a network as a PROFIBUS-DP/DeviceNet slave, first of all, the Electronic Device Data Sheet of UTAdvanced (PROFIBUS-DP/DeviceNet slave) needs to be installed in the configuration tool.

Electronic Device Data Sheet: GSD file (PROFIBUS-DP)

EDS file (DeviceNet)

The GSD/EDS file contains the device information on UTAdvanced.

PLC communicates with UTAdvanced (PROFIBUS-DP/DeviceNet slave) based on the information of the GSD/EDS file.

For how to obtain the configuration tool, contact the PROFIBUS-DP/DeviceNet master vendor.

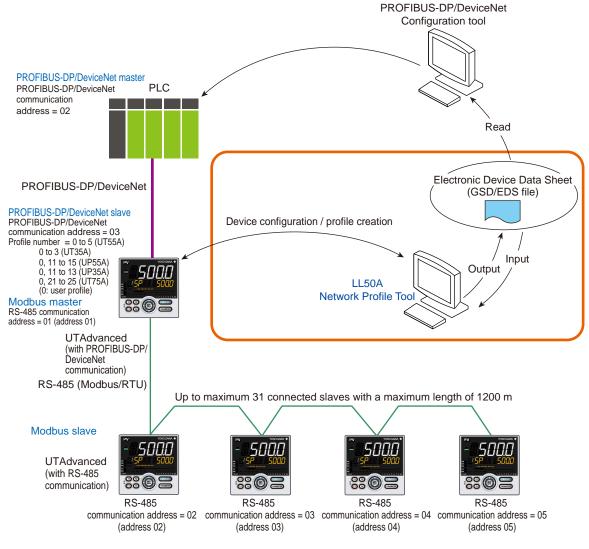
For how to use the configuration tool, see the operation manual for the configuration tool.

Note:

GSD/EDS file for UTAdvanced which can be installed in the configuration tool is one file only. When multiple user profiles are created using LL50A Network Profile Tool, set the IN/OUT area size to the maximum size.

If the IN/OUT area sizes are different between each user profile, UTAdvanced cannot be connected to PLC.

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How to Obtain the GSD/EDS File

- \cdot Output the GSD/EDS file using the Network Profile Tool of LL50A.
 - ▶ LL50A Parameter Setting Software User's Manual

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 Obtain the GSD/EDS file from the Yokogawa Web site when using a fixed profile: URL: www.yokogawa.com/ns/utadv/

	File name	Explanation
PROFUBUS-DP	YEC45F2.GSD	
DeviceNet	UTAdvanced_Profile0.eds	Profile number: 0 (for UT55A/UT35A/UP55A/UP35A)
	UTAdvanced_Profile1.eds	Profile number: 1 (for UT55A/UT35A)
	UTAdvanced_Profile2.eds	Profile number: 2 (for UT55A/UT35A)
	UTAdvanced_Profile3.eds	Profile number: 3 (for UT55A/UT35A)
	UTAdvanced_Profile4.eds	Profile number: 4 (for UT55A)
	UTAdvanced_Profile5.eds	Profile number: 5 (for UT55A)
	UTAdvanced_Profile11.eds	Profile number: 11 (for UP55A/UP35A)
	UTAdvanced_Profile12.eds	Profile number: 12 (for UP55A/UP35A)
	UTAdvanced_Profile13.eds	Profile number: 13 (for UP55A/UP35A)
	UTAdvanced_Profile14.eds	Profile number: 14 (for UP55A)
	UTAdvanced_Profile15.eds	Profile number: 15 (for UP55A)
	UTAdvanced_Profile21.eds	Profile number: 21 (for UT75A)
	UTAdvanced_Profile22.eds	Profile number: 22 (for UT75A)
	UTAdvanced_Profile23.eds	Profile number: 23 (for UT75A)
	UTAdvanced_Profile24.eds	Profile number: 24 (for UT75A)
	UTAdvanced_Profile25.eds	Profile number: 25 (for UT75A)

Contents of the GSD/EDS File

UTAdvanced is displayed in the following way in the configurator window.

	Item	Description	Explanation
PROFIBUS-DP	File Name	YEC45F2.GSD	-
	Slave Family	5 (Controllers)	-
	Vendor	Yokogawa Electric Corp.[250]	-
	Туре	Communication Adapter[12]	-
	Product	UTAdvanced Profile0[100]	Profile number: 0 (for UT55A/UT35A/UP55A/UP35A)
		UTAdvanced Profile1[101]	Profile number: 1 (for UT55A/UT35A)
		UTAdvanced Profile2[102]	Profile number: 2 (for UT55A/UT35A)
		UTAdvanced Profile3[103]	Profile number: 3 (for UT55A/UT35A)
		UTAdvanced Profile4[104]	Profile number: 4 (for UT55A)
		UTAdvanced Profile5[105]	Profile number: 5 (for UT55A)
DeviceNet		UTAdvanced Profile11[111]	Profile number: 11 (for UP55A/UP35A)
Devicemet		UTAdvanced Profile12[112]	Profile number: 12 (for UP55A/UP35A)
		UTAdvanced Profile13[113]	Profile number: 13 (for UP55A/UP35A)
		UTAdvanced Profile14[114]	Profile number: 14 (for UP55A)
		UTAdvanced Profile15[115]	Profile number: 15 (for UP55A)
		UTAdvanced Profile21[121]	Profile number: 21 (for UT75A)
		UTAdvanced Profile22[122]	Profile number: 22 (for UT75A)
		UTAdvanced Profile23[123]	Profile number: 23 (for UT75A)
		UTAdvanced Profile24[124]	Profile number: 24 (for UT75A)
		UTAdvanced Profile25[125]	Profile number: 25 (for UT75A)

Note -

Precaution in using SIEMENS PROFIBUS-DP master in transmission speed of 6M bps or 12M bps In the configuration for PROFIBUS-DP for the following cases, select the "Constant bus cycle time" check box and set the DP constant bus cycle time to 2 ms or more: When using a fixed profile No. 3 or No.5, or when using a user profile with 50 words or more for IN or OUT area.

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3.4 Profile

3.4.1 Contents of Profile

A profile consists of a predefined fixed-part and a data-part to which parameters are assigned.

Flags to switch pages of the data-part and flags to indicate the connection status of controllers are assigned to the fixed-part.

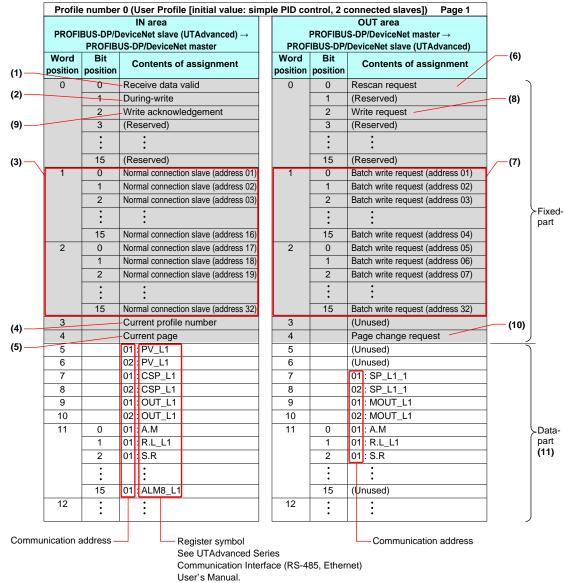
The data-part can be used by switching pages. The number of pages of a profile is 4 (1 to 4).

Note:

Parameters are classified into each page of a profile, i.e. the profile is classified into pages of the more frequently used parameters for routine operation and the less frequently used parameters for startup and batch-start.

Classification for each page allows reducing the memory space occupied in the master. Furthermore, it allows optimizing the updating of the data (in the IN area) read from UTAdvanced.

Example: Profile number 0 (Page 1)



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(1) Receive data valid flag (1: valid)

This flag allows checking whether the data in the IN area is valid.

If the flag is set to 1, the data in the IN area is valid. (However, this only applies to the slave data where the normal connection slave flag (address 01 to 32) is also set to 1.)

When the power is turned on or a rescan is requested, the flag is set to 0. When checking whether all slaves registered in the profile are ready for communication is finished, the flag is set to 1.

(2) During-write flag (0: write enable)

This flag allows checking whether writing to the OUT area is enabled.

If the flag is set to 0, writing to the OUT area is enabled.

When the power is turned on, the flag is set to 0. When the write communication is performed, the flag is set to 1. When a response is returned from the slave, or when the time is up, the flag returns to 0.

Note:

When the flag is set to 1, a write request is not accepted and is ignored (not held).

(3) Normal connection slave flag (Address 01 to 32) (1: connected)

This flag allows checking whether each slave is connected.

The normal connection of slave flags have 32 bits in the fixed-part of the IN area. Slave (address 01) in word position 1, and bit position 0 is UTAdvanced that runs as a PROFIBUS-DP/DeviceNet slave.

Note

If normal connection slave (address 01) is set to 0 in a profile in which the PROFIBUS-DP/ DeviceNet slave (address 01) is registered, and rescan does not cause the flag to return to 1, it is a failure.

Word position 1 and bit positions 1 to 15, and word position 2 and bit positions 0 to 15 correspond to the Modbus slaves with communication addresses 2 to 32. When slaves (address 01 to 32) are connected, each flag is set to 1. When the power is turned on, the flag is set to 0, and when communication becomes enabled, the flag is set to 1. When communication is disabled, the flag is set to 0. When communication becomes enabled upon a rescan request, the flag is set to 1.

(4) Current profile number

The currently used profile number is displayed.

For UT55A: 0 to 5 For UT35A: 0 to 3 For UP55A: 0, 11 to 15 For UP35A: 0, 11 to 13 For UT75A: 0, 21 to 25 (0: user profile)

(5) Current pager

The currently used profile page number (1 to 4) is displayed.

(6) Rescan request flag

A rescan request is made to attempt a retry to establish communication with unconnected slaves. (When connection is normally established with all slave controllers registered in the profile, a rescan is not performed.)

Change the flag in word position 0 and bit position 0 of the OUT area from 0 to 1. Thereafter, return it to 0 when the receive data valid flag has been set to 1.

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(7) Write request flag (address 01 to 32)

This flag allows writing all parameters that are assigned to the OUT area together to the slave for each communication address.

Write request flags have 32 bits in the fixed-part of the OUT area. Slave (address 01) in word position 1 and bit position 0 is UTAdvanced that runs as a PROFIBUS-DP/DeviceNet slave.

Word position 1 and bit positions 1 to 15, and word position 2 and bit positions 0 to 15 correspond to the Modbus slaves with communication addresses 2 to 32. Change the write request flags from 0 to 1 for the slaves (address 01 to 32) to be written, while the during-write flag is set to 0. Thereafter, return them to 0 when the writing process is completed.

- (8) Write request flag and (9) Write acknowledgment flag When a write request is made, regardless of whether the writing is performed individually or all together, both the write acknowledgement flag and write request flag need to be used to reliably recognize that the writing is completed. When the write request flag is set to 1, while the during-write flag and write acknowledgement flag are set to 0, the write acknowledgment flag is set to 1. Set the data-part of the OUT area when the write acknowledgment flag is set to 1. Thereafter, returning the write request flag to 0 prompts the writing to be performed. When the writing is completed, the write acknowledgment flag is set to 0.
- ▶ "3.6 Reading and Writing UTAdvanced Data" in this manual

(10) Page change request

This request switches the currently used profile page.

Set the value in OUT area word position 4 to a value (any of 1 to 4) that is different from the current page (in IN area word position 4).

The receive data valid flag remains set to 0 until the page is switched upon the page change request and the data acquisition is completed.

▶ "3.7 Switching Pages" in this manual

(11) Data-part

The data format is the same as that of the displayed value of UTAdvanced. Users can assign the data-part of the user profile with the Network Profile Tool of LL50A.

► LL50A Parameter Setting Software User's Manual)

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3.4.2 Types of Profile

UT75A/UT55A/UP55A provides one user profile and 5 fixed profiles.

UT35A/UP35A provides one user profile and 3 fixed profiles.

Set each profile numbers according to the configurations.

Profile numbers can be set with the FILE parameter in the PROFIBUS-DP Communication Settings menu (PROF) or DeviceNet Communication Settings menu (DNET).

▶ Setting FILE parameters: "2.1.1 Setting PROFIBUS-DP Communication (PROFIBUS-DP Slave/Modbus Master)" or "2.1.2 Setting DeviceNet Communication (DeviceNet Slave/Modbus Master)" in this manual

Example: UT55A/UT35A

For a simple PID control with 6 connected controllers, use "Profile number 3: 8 simple PID controllers".

For a simple PID control with 10 connected controllers, set the connection devices using "Profile number 0: User profile" with Network Profile Tool of LL50A.

UT55A/UT35A

Profile	Name	Page	Item	Data length	Applicable control mode and control type		
number	Name	number	item	(byte)	Control mode	Control type	
	User profile	1	Process value, operation mode, alarm status				
	(Initial value: Simple PID	2	PID parameter	26			
0	Control,	3	Heating/cooling PID parameter				
	2 connected slaves)	4	Alarm setpoint				
	Simple PID	1	Process value, operation mode, alarm status				
	Control,	2	PID parameter	40	All mades		
1	3 connected	3	Heating/cooling PID parameter		All modes except for Cascade Control (4: CAS)		
	slaves	4	Alarm setpoint]			
	Simple PID	1	Process value, operation mode, alarm status				
2	Control, 5 connected	2	PID parameter	70			
2		3	Heating/cooling PID parameter	10			
	slaves	4	Alarm setpoint			All type	
	Simple PID	1	Process value, operation mode, alarm status				
3	Control,	2	PID parameter	106			
3	8 connected	3	Heating/cooling PID parameter				
	slaves	4	Alarm setpoint				
	Cascade	1	Process value, operation mode, alarm status				
4	Control,	2	PID parameter	70			
4	3 connected	3	Heating/cooling PID parameter	10			
	slaves	4	Alarm setpoint		Cascade Control		
	Cascade	1	Process value, operation mode, alarm status		(4: CAS)		
5	Control,	2	PID parameter	110	,		
	5 connected	3	Heating/cooling PID parameter] 110			
	slaves	4	Alarm setpoint				

UP55A/UP35A

						le control
Profile	Name	Page number	Item	Data length		
number	number numi			(byte)	Control mode	Control type
	User profile (Initial value:	1	Process value, operation mode, alarm status		mode	турс
0	Simple PID	2	PID parameter (for address 1)	26		
	Control, 2 connected	3	PID parameter (for address 2)	20		
	slaves)	4	Local event-1 to -2 setpoint (for address 1, 2)			
		1	Process value, operation mode, alarm status			
11	Simple PID	2	PID parameter, Alarm setpoint	48		
''	Control, 2 connected slaves	3	Local event-1 to -7 setpoint (for address 1)	40	All modes	
		4	Local event-1 to -7 setpoint (for address 2)		except for Cascade	All type
		1	Process value, operation mode, alarm status		Control (4: CAS)	
12	Simple PID Control, 4 connected slaves	2	PID parameter, Alarm setpoint	88		
12		3	Local event-1 to -7 setpoint (for address 1, 2)			
		4	Local event-1 to -7 setpoint (for address 3, 4)			
	Simple PID	1	Process value, operation mode, alarm status	162		
13	Control, 1 connected slave	2	PID parameter, Local event-1 to -7 setpoint, Program pattern clearance			
	(with program	3	Pattern setting	1		
	pattern setting)	4	Segment setting	1		
		1	Process value, operation mode, alarm status			
14	Cascade Control,	2	PID parameter, Alarm setpoint	88		
14	2 connected slaves	3	Local event-1 to -7 setpoint (for address 1, 2)	00		
		4	Local event-1 to -7 setpoint (for address 3, 4)	1	Cascade	
		1	Process value, operation mode, alarm status		Control	
15	Cascade Control, 1 connected slave	2	PID parameter, Local event-1 to -7 setpoint, Program pattern clearance	162	(4: CAS)	
	(with program pattern setting)	3	Pattern setting			
	F3111 001g/	4	Segment setting			

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UT75A

Profile	Name	Page	ltem	Data length	Applicab mode and of	le control control type
number	Ivaille	number	item	(byte)	Control mode	Control type
	User profile	1	Process value, operation mode, alarm status		mode	type
	(Initial value: Simple PID	2	PID parameter			
0	Control, 2 connected slaves)	3	Heating/cooling PID parameter	13		
		4	Alarm setpoint			
	Simple PID	1	Process value, operation mode, alarm status		All modes	
21	Control,	2	PID parameter	23	except for	
21	3 connected	3	Heating/cooling PID parameter	23	Cascade	
	slaves	4	Alarm setpoint		Control (4: CAS) and	
	Simple PID	1	Process value, operation mode, alarm status		Dual-loop	
00	Control, 5 connected slaves	2	PID parameter	35	Control (11: 2LP)	
22		3	Heating/cooling PID parameter	35		
		4	Alarm setpoint			
	Simple PID	1	Process value, operation mode, alarm status	53		All type
20	Control,	2	PID parameter			
23	8 connected	3	Heating/cooling PID parameter			
	slaves	4	Alarm setpoint			
	Cascade	1	Process value, operation mode, alarm status			
24	Control / Dual-	2	PID parameter	25		
24	loop Control, 3 connected	3		35		
	slaves	4	Alarm setpoint		Cascade Control (4:	
	Simple PID	1	Process value, operation mode, alarm status		CAS) and	
	Control /	2	PID parameter		Dual-loop	
0.5	Cascade	3	Program pattern parameter	0.5	Control (11:	
25	Control / Dual- loop Control, 1 connected slaves	4	Program pattern parameter	65	2LP)	

User profile

As the default, a parameter for a simple PID control with 2 connected controllers is set.

Users can assign the data-part of the user profile with the Network Profile Tool of LL50A.

LL50A Parameter Setting Software User's Manual

Fixed profile

UT75A:

Parameters for a simple PID control /cascade control / dual-loop control (with program pattern setting) with 1 connected controllers are set.

UT55A/UT35A:

Parameters for a simple PID control with 3, 5, and 8 connected controllers and for a cascade control with 3 and 5 connected controllers are set.

UP55A/UP35A:

Parameters for a simple PID control with 2, 4, and 1 (with program setting function) connected controllers and for a cascade control with 2 and 1 (with program setting function) connected controllers are set.

However, the parameters for a cascade control (Profile numbers: 4 and 5) cannot be used for UT35A, and the parameters for a cascade control (Profile numbers: 14 and 15) cannot be used for UP35A.

3.5 Operation at the Time of Power-On

The following shows how the IN area of UTAdvanced looks like from the perspective of a PLC when UTAdvanced is turned on while the PLC power is already on.

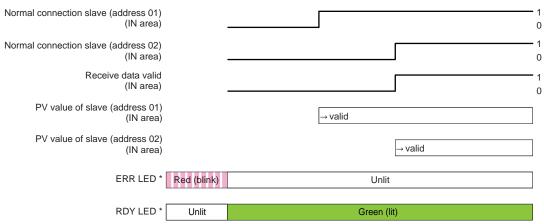
Note

The UTAdvanced data and write request in the IN area become valid when the normal connection flag for each slave is set to 1 ("→valid" in the figure below). However, it is recommended to handle them after the receive data valid flag is set to 1.

- ▶ 3.1 Overview: "Example: PROFIBUS-DP/DeviceNet Communication Connection" in this manual
- ▶ "3.9 Profile List" in this manual

3.5.1 Example at the Time of Power-On

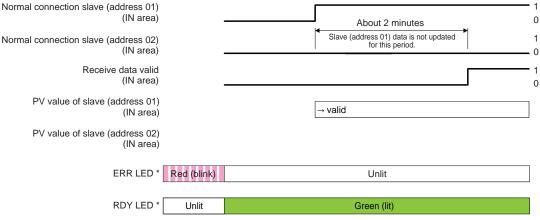
Example of connecting 2 slaves (address 01 and 02):



^{*:} For DeviceNet, one MNS LED turns on (green) or blinks (red).

3.5.2 Example at the Time of Power-On (When Slave (address 02) is not Connected)

Example where slave (address 01) is connected, but slave (address 02) is not connected:



^{*:} For DeviceNet, one MNS LED turns on (green) or blinks (red).

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3.6 Reading and Writing UTAdvanced Data

- ▶ 3.1 Overview: "Example: PROFIBUS-DP/DeviceNet Communication Connection" in this manual
- ▶ "3.9 Profile List" in this manual

3.6.1 Reading

Data in the IN area that is always updated can be read.

Procedure

- 1. Check that the receive data valid flag is set to 1.
- 2. Check that the normal connection slave flag for a slave to be handled (address 01 to 32) is set to 1.
- Data for the corresponding slave (address 01 to 32) in the IN area can be handled.

Note -

If both the receive data valid flag and normal connection slave flag are set to 1, the data in the IN area is valid.

Example of ladder program

ı	Receive data valid	slave (address 02)		
			Process of handling slave (address 02):	
			UTAdvanced value (e.g. PV) in the IN area	

3.6.2 Writing Individual Parameters

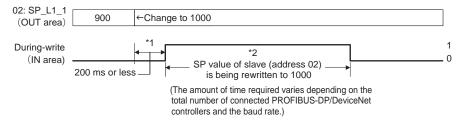
Only the parameter values to be changed in the OUT area can be written.

■ Simple procedure of writing individual parameters used when the write interval is long

Procedure

- 1. Check that the during-write flag is set to 0.
- Change the value in the OUT area to which the parameter to be written is assigned.

Example of rewriting the SP value for slave (address 02):



Note

- *1 in the figure above
 - If the write value is changed multiple times during the period*1, the last write value is valid.

*2 in the figure above

- If the during-write flag is set to 1, a changed value in the OUT area is invalid. The change of the value is ignored (not held).
- Changing a value in the OUT area results in a request for writing the individual parameter.
- UTAdvanced holds the previous values in the OUT area in order to detect changes in the values in the OUT area. The previous values in the OUT area are set to 0 when the power is turned on, or when PROFIBUS-DP/DeviceNet is disconnected. If a value other than 0 is written in the OUT area of a PLC when the disconnected PROFIBUS-DP/ DeviceNet is connected, UTAdvanced handles it as a request for writing the individual parameter.

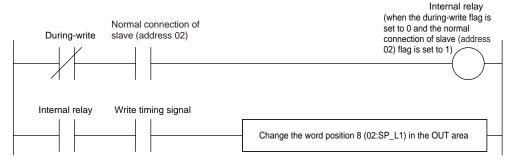
If you want to write 0 first after the disconnected PROFIBUS-DP/DeviceNet is connected, you need to use batch writing. Furthermore, the first writing after the power is turned on needs to use batch writing.

- ▶ Batch writing: "3.6.3 Batch writing for Each Communication Address" of this manual
- If the PROFIBUS-DP/DeviceNet baud rate is slow or the PLC scan cycle is long, the PLC program may be unable to detect that the during-write flag in the IN area has been set to 1.
 In order to reliably detect that the writing is completed, individual parameters need to be written using both the write request flag in the OUT area and the write acknowledgement flag in the OUT area (Procedure of reliably detecting that the writing is completed).

Note

For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.

Example of ladder program



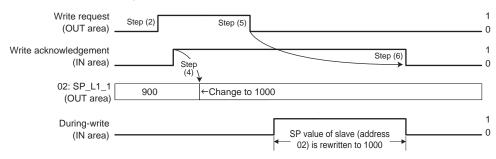
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Writing individual parameters (Procedure to reliably detect that the writing is completed)

Procedure

- 1. Check that the during-write flag is set to 0.
- 2. Change the write request flag from 0 to 1.
- 3. Check that the write acknowledgment flag has been set to 1.
- **4.** Change the value in the OUT area to which the parameter to be written is assigned.
- 5. Return the write request flag from 1 to 0 (which is equivalent to the write start command). The timing of returning the flag to 0 may be the same as that of step 4.
- 6. When the write acknowledgment flag has been set to 0, the writing is completed.

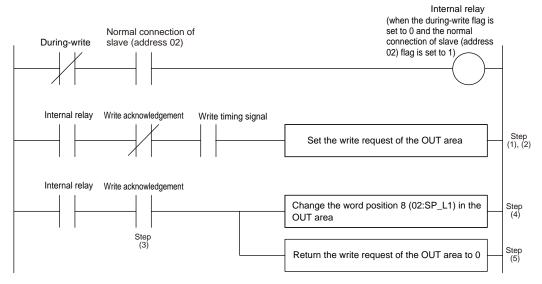
Example of rewriting the SP value of slave (address 02):



Note.

- Changing the value in the OUT area results in a request for writing the individual parameter. If you want to write the current values in the OUT area, use batch writing.
- For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.

Example of ladder program



3.6.3 Batch Writing for Each Communication Address

Batch writing can be performed on parameter values assigned to the OUT area for each communication address.

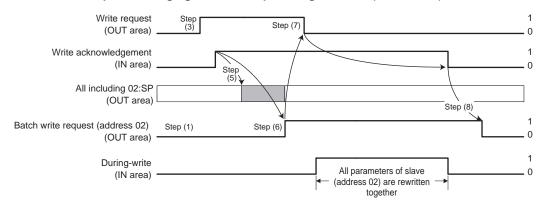
Procedure

- 1. Set the write request flag for the slaves to be written (address 01 to 32) to 0.
- Check that the during-write flag is set to 0.
- 3. Change the write request flag from 0 to 1.
- 4. Check that the write acknowledgment flag has been set to 1.
- **5.** Set the values in the OUT area to which the parameters to be written are assigned. (The same values as the previously written values can also be written.)
- 6. Change the write request flag for the slaves to be written (address 01 to 32) from 0 to 1.
- 7. Return the write request flag from 1 to 0 (which is equivalent to the write start command). The timing of returning the flag to 0 may be the same as that in steps 5 and 6
- When the write acknowledgment flag has been set to 0, the writing is completed. Return the write request flag from 1 to 0.

Note .

- Batch writing writes the values in the OUT area at the point of step (7).
- Changing the write request flag from 0 to 1 (step 6) needs to be performed when the duringwrite flag is set to 0 and the write acknowledgment flag is set to 1. If these conditions are not met, the write request is invalid.

Example of changing the write request flag for slave (address 02) from 0 to 1:

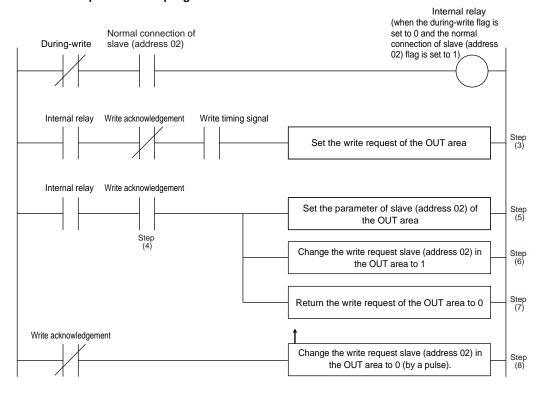


Note

- For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.
- When the PROFIBUS-DP/DeviceNet baud rate is slow, or the scan cycle of a PLC is long, the PLC program may be unable to detect that the during-write flag has been set to 0.

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Example of ladder program



3.6.4 Reading Program Pattern (for UP55A/UP35A)

Procedure

- Check that the receive data valid flag and the normal connection slave flag are set to 1
- Check that the during-write flag is set to 0.
- 3. Change the write request flag from 0 to 1.
- 4. Check that the write acknowledgment flag has been set to 1.
- **5.** Write "0" to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
- 6. Return the write request flag from 1 to 0.
- 7. Check that the write acknowledgment flag has been set to 0.
- 8. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C), and then confirm that is "0".
- 9. Check that the during-write flag is set to 0.
- 10. Change the write request flag from 0 to 1.
- 11. Check that the write acknowledgment flag has been set to 1.
- 12. Write the required pattern number and the segment number to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
- 13. Return the write request flag from 1 to 0.
- 14. Check that the write acknowledgment flag has been set to 0.
- 15. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C), the Segment number designation (SEGNO._C), and the Read/write error information (PTN.ERR).
 - Confirm that the required pattern number and the segment number are set to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C), and then the Read/write error information (PTN.ERR) has been set to "0".
- **16.** Read the data corresponding to the segment parameters in the IN area; from the Final target setpoint (TSP_L1) to the Off time of time event 16 (T.OF16).

Note

The following operations cannot be executed concurrently, otherwise the program pattern cannot be read/written normally.

- Access to the program pattern via Open Network.
- Upload/download of the program pattern using the LL50A Parameter Setting Tool.

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3.6.5 Writing Program Pattern (for UP55A/UP35A)

Procedur<u>e</u>

- Check that the receive data valid flag and the normal connection slave flag are set to 1.
- 2. Check that the during-write flag is set to 0.
- 3. Change the write request flag from 0 to 1.
- 4. Check that the write acknowledgment flag has been set to 1.
- **5.** Write "0" to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
- 6. Return the write request flag from 1 to 0.
- 7. Check that the write acknowledgment flag has been set to 0.
- Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C), and confirm that is "0".
- 9. Check that the during-write flag is set to 0.
- 10. Change the write request flag from 0 to 1.
- 11. Check that the write acknowledgment flag has been set to 1.
- 12. Write the required pattern number and pattern data to the OUT area corresponding to the Program pattern number selection (PTNO._C) and Starting target setpoint (SSP_L1) to Program pattern name (P.NAME).
- 13. Return the write request flag from 0 to 1.
- 14. Return the write request flag from 1 to 0.
- 15. Check that the write acknowledgment flag has been set to 0.
- 16. Return the write request flag from 1 to 0.
- 17. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C) and the Read/write error information (PTN.ERR).

Confirm that the required pattern number is set to the Program pattern number selection (PTNO._C) and the Read/write error information (PTN.ERR) has been set to "0".

Note

The following operations cannot be executed concurrently, otherwise the program pattern cannot be read/written normally.

- Access to the program pattern via Open Network.
- Upload/download of the program pattern using the LL50A Parameter Setting Tool.

3.7 Switching Pages

Pages can be switched by changing the value for the page change request (in word position 4 of the OUT area fixed-part).

- ▶ 3.1 Overview: "Example: PROFIBUS-DP/DeviceNet Communication Connection" in this manual
- ▶ "3.9 Profile List" in this manual

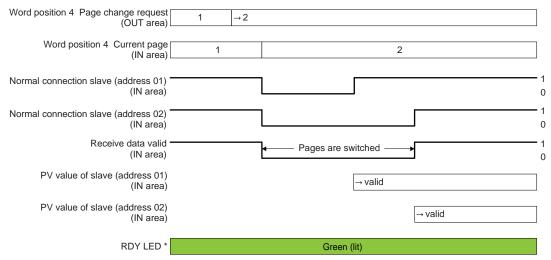
Procedure

- 1. Check that the during-write flag is set to 0.
- 2. Change the value for the page change request (in word position 4 of the OUT area fixed-part) to a value (any of 1 to 4) that is different from the current page (in word position 4 of the IN area). The pages will be switched.

Note

- The page change request is accepted even when the during-write flag is set to 1. However, the page is actually changed when the writing is completed.
- PROFIBUS-DP/DeviceNet communication remains connected during the period when the page is being changed.
- The data and write request in the IN area become valid when the normal connection flag for each slave is set to 1 ("—valid" in the figure below). However, it is recommended to handle them after the receive data valid flag has been set to 1.
- The value for the page change request needs to be held without change for 200 ms or longer. It is recommended that the next page change request is made after the receive data valid flag has been changed from 0 to 1.

Change of the flag when the page is switched from 1 to 2 when 2 slaves (address 01 and 02) are connected:



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

Checking page

The current page can be checked with the word position 4 of the IN area fixed-part.

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3.8 Request for Rescanning

UTAdvanced that runs as a Modbus master attempts to establish communication with Modbus slaves registered in the profile, and if it cannot connect to a slave because of a wiring error or inconsistency in the communication conditions, it gives up the attempt to establish communication with that slave from the next time. It reduces the update cycle of the read data by reducing the time of communication with slaves that are disabled for communication.

A request for rescanning is made to attempt to start communication with slaves that were disabled for communication after errors with the wiring and communication conditions are fixed.

There are two types of request for rescanning: one type of request is made as needed, and the other is made at a constant frequency (automatic rescan time in SCAN). This section describes the type of rescan request that is made as needed.

- 3.1 Overview: "Example: PROFIBUS-DP/DeviceNet Communication Connection" in this manual
- "3.9 Profile List" in this manual

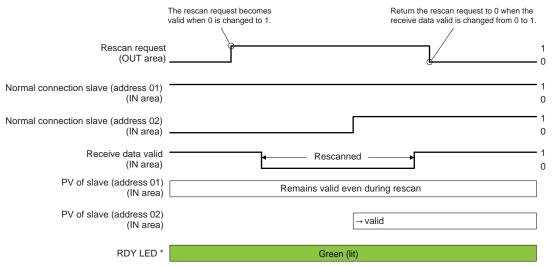
Procedure

- Change the rescan request flag (in work position 0 and bit position 0 in the OUT area) from 0 to 1. Rescanning starts.
- 2. Return the rescan request flag from 1 to 0.

Note.

- A request for rescanning is accepted even when the during-write flag is set to 0. However, the rescan request process is actually performed after the writing is completed.
- The data and write request in the IN area become valid when the normal connection flag for each salve is set to 1 ("-valid" in the figure below). However, it is recommended to handle them after the receive data valid flag is set to 1. This is why if there are slaves to which connection cannot be established, the updating of the data of the salves to which connection can be established will be delayed by a time equaling the number of unconnected slaves multiplied by about 2 seconds. If there are many slaves that cannot be connected, it is recommended for the same reason to use the automatic rescan function.
- 0 of the rescan request flag needs to be held for 200 ms or longer before it is set to 1.
 Furthermore, after it is set to 1, 1 needs to be held for 200 ms or longer before it is set to 0.
 It is recommended to return the rescan request flag to 0 after the receive data valid flag is changed from 0 to 1.
- The rescan operation is performed on slaves that are not connected. If connection is normally established with all slaves registered in the profile, the receive data valid flag remains set to 1 even when a rescan request is made.
- ▶ "3.10 Changing Automatic Rescan Time (SCAN in PROF/DNET Menu)" in this manual

The operation of each flag when slave (address 01) is connected and slave (address 02) is not connected, and the rescan request flag is changed from 0 to 1 in order to establish a connection with the slave (address 02):



^{*:} For DeviceNet, one MNS LED turns on (green) or blinks (red).

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Intentionally blank

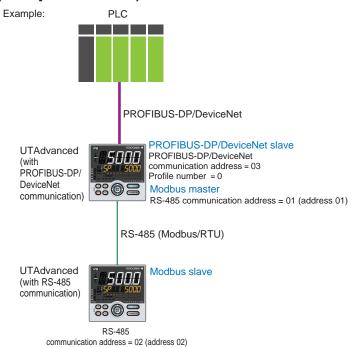
3.9 Profile List

For how to read the profile, see "3.4 Profile."

3.9.1 Profile List for UT55A/UT35A

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers])





Page 1

Profi	Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page						
PF	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit Contents of assignment			Word position	Bit position	Contents of assignment	
0	0	Receive data valid		0	0	Rescan request	
	1	During-write			1	(Reserved)	
	2	Write acknowledgement			2	Write request	
	3	(Reserved) (Reserved) (Reserved)			3	(Reserved)	
	4				4	(Reserved)	
	5				5	(Reserved)	
	6	(Reserved)			6	(Reserved)	
	7	(Reserved)			7	(Reserved)	
	8	(Reserved)			8	(Reserved)	
	9	(Reserved)			9	(Reserved)	
	10	(Reserved)			10	(Reserved)	
	11	(Reserved)			11	(Reserved)	
	12	(Reserved)			12	(Reserved)	
	13	(Reserved)			13	(Reserved)	
	14	(Reserved)			14	(Reserved)	
	15	(Reserved)			15	(Reserved)	

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Profi	le numl	per 0 (User profile [initial value: simple	e PID cont	rol with			
PR		IN area -DP/DeviceNet slave (UTAdvanced) →	PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/			
Word	PR Bit	OFIBUS-DP/DeviceNet master	Word	De Bit	eviceNet slave (UTAdvanced)		
position		Contents of assignment		position	Contents of assignment		
1	0	Normal connection slave (address 01)	1	0	Batch write request (address 01)		
	1	Normal connection slave (address 02)		1	Batch write request (address 02)		
	3	Normal connection slave (address 03) Normal connection slave (address 04)		3	Batch write request (address 03) Batch write request (address 04)		
	4	Normal connection slave (address 04)		4	Batch write request (address 05)		
	5	Normal connection slave (address 06)		5	Batch write request (address 06)		
	6	Normal connection slave (address 07)	1	6	Batch write request (address 07)		
	7	Normal connection slave (address 08)		7	Batch write request (address 08)		
	8	Normal connection slave (address 09)		8	Batch write request (address 09)		
	9	Normal connection slave (address 10)		9	Batch write request (address 10)		
	10 11	Normal connection slave (address 11) Normal connection slave (address 12)		10	Batch write request (address 11) Batch write request (address 12)		
	12	Normal connection slave (address 12)		12	Batch write request (address 12)		
	13	Normal connection slave (address 14)		13	Batch write request (address 14)		
	14	Normal connection slave (address 15)		14	Batch write request (address 15)		
	15	Normal connection slave (address 16)		15	Batch write request (address 16)		
2	0	Normal connection slave (address 17)	2	0	Batch write request (address 17)		
	1	Normal connection slave (address 18)		1	Batch write request (address 18)		
	3	Normal connection slave (address 19) Normal connection slave (address 20)		3	Batch write request (address 19) Batch write request (address 20)		
	4	Normal connection slave (address 20)		4	Batch write request (address 20)		
	5	Normal connection slave (address 22)		5	Batch write request (address 22)		
	6	Normal connection slave (address 23)		6	Batch write request (address 23)		
	7	Normal connection slave (address 24)		7	Batch write request (address 24)		
	8	Normal connection slave (address 25)		8	Batch write request (address 25)		
	9	Normal connection slave (address 26)		9	Batch write request (address 26)		
	10	Normal connection slave (address 27)		10	Batch write request (address 27)		
	11 12	Normal connection slave (address 28) Normal connection slave (address 29)		11 12	Batch write request (address 28) Batch write request (address 29)		
	13	Normal connection slave (address 29)		13	Batch write request (address 30)		
	14	Normal connection slave (address 31)		14	Batch write request (address 31)		
	15	Normal connection slave (address 32)		15	Batch write request (address 32)		
3		Current profile number	3		(Unused)		
4		Current page	4		Page change request		
5		01: PV_L1	5		(Unused)		
6		02: PV_L1	6		(Unused)		
7		01: CSP_L1	7		01: SP_L1_1		
8		02: CSP_L1	8		02: SP_L1_1		
9		01: OUT_L1	9		01: MOUT_L1		
10		02: OUT_L1	10		02: MOUT_L1		
11	0	01: A.M	11	0	01: A.M		
	2	01: R.L_L1 01: S.R		2	01: R.L_L1 01: S.R		
	3	(Unused)		3	(Unused)		
	4	(Unused)		4	(Unused)		
	5	(Unused)		5	(Unused)		
	6	(Unused)		6	(Unused)		
	7	(Unused)		7	(Unused)		
	8	01: ALM1_L1		8	(Unused)		
	9	01: ALM2_L1 01: ALM3_L1		9 10	(Unused)		
	11	01: ALM3_L1 01: ALM4_L1		11	(Unused)		
	12	01: ALM5_L1		12	(Unused)		
	13	01: ALM6 L1		13	(Unused)		
	14	01: ALM7_L1 \ \ \ UT35A: unused		14	(Unused)		
	15	01: ALM8_L1		15	(Unused)		

Profi	le numl	per 0 (User profile [initial value: simp	ole F	PID control with 2 connected controllers]) on page 1				
		IN area			OUT area			
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		OP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word	Word Bit			Word	Word Bit			
position	position	Contents of assignment		position	position	Contents of assignment		
12	0	02: A.M		12	0	02: A.M		
	1	02: R.L_L1			1	02: R.L_L1		
	2	02: S.R			2	02: S.R		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)]		7	(Unused)		
	8	02: ALM1_L1			8	(Unused)		
	9	02: ALM2_L1			9	(Unused)		
	10	02: ALM3_L1]		10	(Unused)		
	11	02: ALM4_L1			11	(Unused)		
	12	02: ALM5_L1			12	(Unused)		
	13	02: ALM6_L1 >UT35A: unused			13	(Unused)		
	14	02: ALM7_L1			14	(Unused)		
	15	02: ALM8_L1			15	(Unused)		

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IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
0	0	Receive data valid	0	0	Rescan request	
	1	During-write		1	(Reserved)	
	2	Write acknowledgement		2	Write request	
	3	(Reserved)		3	(Reserved)	
	4	(Reserved)		4	(Reserved)	
	5	(Reserved)		5	(Reserved)	
	6	(Reserved)		6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)	
4		Current page	4		Page change request	
5		01: P_L1_1	5		01: P_L1_1	
6		02: P_L1_1	6		02: P_L1_1	
7		01: I_L1_1	7		01: I_L1_1	
8		02: I_L1_1	8		02: I_L1_1	
9		01: D_L1_1	9		01: D_L1_1	
10		02: D_L1_1	10		02: D_L1_1	
11		01: SPNO.	11		01: SPNO.	
12		02: SPNO.	12		02: SPNO.	

Page 3

Profi	ile numl	ber 0 (User profile [initial value: simple	e PID cont	rol with	2 connected controllers]) on page 3			
		IN area		OUT area				
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →				PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/			
	PROFIBUS-DP/DeviceNet master				eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Word Bit Contents of assign				
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: Pc_L1_1	5		01: Pc_L1_1			
6		02: Pc_L1_1	6		02: Pc_L1_1			
7		01: lc_L1_1	7		01: lc_L1_1			
8		02: lc_L1_1	8		02: lc_L1_1			
9		01: Dc_L1_1	9		01: Dc_L1_1			
10		02: Dc_L1_1	10		02: Dc_L1_1			
11		01: SPNO.	11		01: SPNO.			
12		02: SPNO.	12		02: SPNO.			

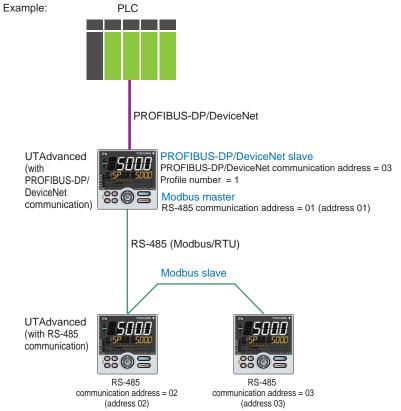
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Prof	ile numl	per 0 (User profile [initial value: simpl	e PID cont	rol with	2 connected controllers]) on page 4			
		IN area		OUT area				
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →				PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/			
		OFIBUS-DP/DeviceNet master		DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: A1_L1_1	5		01: A1_L1_1			
6		02: A1_L1_1	6		02: A1_L1_1			
7		01: A2_L1_1	7		01: A2_L1_1			
8		02: A2_L1_1	8		02: A2_L1_1			
9		01: A3_L1_1	9		01: A3_L1_1			
10		02: A3_L1_1	10		02: A3_L1_1			
11		01: A4_L1_1	11		01: A4_L1_1			
12		02: A4_L1_1	12		02: A4_L1_1			

Profile number 1 (Simple PID control with 3 connected controllers)





Page 1

Pro	ofile nu	mber 1 (Simple PID control with 3	3 connec	ted con	trollers) on page 1			
PF	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: PV_L1	5		(Unused)			
6		02: PV_L1	6		(Unused)			
7		03: PV_L1	7		(Unused)			
8		01: CSP_L1	8		01: SP_L1_1			
9		02: CSP_L1	9		02: SP_L1_1			
10		03: CSP_L1	10		03: SP_L1_1			
11		01: OUT_L1	11		01: MOUT_L1			
12		02: OUT_L1	12		02: MOUT_L1			

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Profile number 1 (Simple PID control with 3 connected controllers) on page 1									
		IN area			OUT area				
PR	ROFIBUS	-DP/DeviceNet slav	re (UTAdvanced) →	PR	PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PR	OFIBUS-DP/Device	Net master		De	eviceNet slave (UTAdvanced)			
Word	Bit	Contents	of assignment	Word	Bit	Contents of assignment			
•	position		or assignment	position	position	-			
13		03: OUT_L1		13		03: MOUT_L1			
14		01: H.OUT_L1		14		01: MOUT_L1			
15		02: H.OUT_L1		15		02: MOUT_L1			
						_			
16		03: H.OUT_L1		16		03: MOUT_L1			
17		01: C.OUT_L1		17		01: MOUTc_L1			
18		02: C.OUT_L1		18		02: MOUTc_L1			
19		03: C.OUT_L1		19		03: MOUTc_L1			
20	0	01: A.M		20	0	01: A.M			
	1	01: R.L_L1			1	01: R.L_L1			
	2	01: S.R			2	01: S.R			
	3	(Unused)			3	(Unused)			
	4	(Unused)			4	(Unused)			
	5	(Unused)			5	(Unused)			
	6	(Unused)			6	(Unused)			
	7	(Unused)			7	(Unused)			
	8	01: ALM1_L1			8	(Unused)			
	9	01: ALM2_L1			9	(Unused)			
	10	01: ALM3_L1		10	(Unused)				
	11	01: ALM4_L1		11	(Unused)				
	12	01: ALM5_L1	 1		12	(Unused)			
	13	01: ALM6_L1			13	(Unused)			
	14	01: ALM7_L1	UT35A: unused		14	(Unused)			
	15	01: ALM7_L1			15	(Unused)			
21	0	02: A.M)	21	0	02: A.M			
21	1	02: R.L_L1		21	1	02: R.L_L1			
	2	02: K.L_L1			2	02: N.L_L1			
	3	(Unused)			3	(Unused)			
	4	(Unused)			4	(Unused)			
	5	(Unused)			5	(Unused)			
	6	(Unused)			6	(Unused)			
	7	(Unused)			7	(Unused)			
	8	02: ALM1_L1			8	(Unused)			
	9	02: ALM1_L1			9	(Unused)			
	10				10	,			
	11	02: ALM3_L1			-	(Unused)			
		02: ALM4_L1	1		11	(Unused)			
	12	02: ALM5_L1			12	(Unused)			
	13	02: ALM6_L1	UT35A: unused		13	(Unused)			
	14	02: ALM7_L1			14	(Unused)			
- 00	15	02: ALM8_L1	1		15	(Unused)			
22	0	03: A.M		22	0	03: A.M			
	1	03: R.L_L1			1	03: R.L_L1			
	2	03: S.R			2	03: S.R			
	3	(Unused)			3	(Unused)			
	4	(Unused)			4	(Unused)			
	5	(Unused)			5	(Unused)			
	6	(Unused)			6	(Unused)			
	7	(Unused)			7	(Unused)			
	8	03: ALM1_L1			8	(Unused)			
	9	03: ALM2_L1			9	(Unused)			
	10	03: ALM3_L1			10	(Unused)			
	11	03: ALM4_L1			11	(Unused)			
	12	03: ALM5_L1			12	(Unused)			
	13	03: ALM6_L1	UT35A: unused		13	(Unused)			
	14	03: ALM7_L1			14	(Unused)			
	15	03: ALM8_L1	J		15	(Unused)			

Page 2

Pro	ofile nu	mber 1 (Simple PID control with	3 connect	ed con	trollers) on page 2			
		IN area			OUT area			
		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
		The fixed-part is omitted			The fixed-part is omitted			
		(See profile number 0 on page 1)		:	(See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: P_L1_1	5		01: P_L1_1			
6		02: P_L1_1	6		02: P_L1_1			
7		03: P_L1_1	7		03: P_L1_1			
8		01: I_L1_1	8		01: I_L1_1			
9		02: I_L1_1	9		02: I_L1_1			
10		03: I_L1_1	10		03: I_L1_1			
11		01: D_L1_1	11		01: D_L1_1			
12		02: D_L1_1	12		02: D_L1_1			
13		03: D_L1_1	13		03: D_L1_1			
14		01: SPNO.	14		01: SPNO.			
15		02: SPNO.	15		02: SPNO.			
16		03: SPNO.	16		03: SPNO.			
17		(Unused)	17		(Unused)			
18		(Unused)	18		(Unused)			
19		(Unused)	19		(Unused)			
20		(Unused)	20		(Unused)			
21		(Unused)	21		(Unused)			
22		(Unused)	22		(Unused)			

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Pro	ofile nu	mber 1 (Simple PID control with 3	connect	ed con	trollers) on page 3		
		IN area			OUT area		
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted		
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)		
•	•	(See prome number of on page 1)	•	•	(Gee prome number of on page 1)		
4		Current page	4		Page change request		
5		01: Pc_L1_1	5		01: Pc_L1_1		
6		02: Pc_L1_1	6		02: Pc_L1_1		
7		03: Pc_L1_1	7		03: Pc_L1_1		
8		01: lc_L1_1	8		01: lc_L1_1		
9		02: lc_L1_1	9		02: lc_L1_1		
10		03: lc_L1_1	10		03: lc_L1_1		
11		01: Dc_L1_1	11		01: Dc_L1_1		
12		02: Dc_L1_1	12		02: Dc_L1_1		
13		03: Dc_L1_1	13		03: Dc_L1_1		
14		01: SPNO.	14		01: SPNO.		
15		02: SPNO.	15		02: SPNO.		
16		03: SPNO.	16		03: SPNO.		
17		(Unused)	17		(Unused)		
18		(Unused)	18		(Unused)		
19		(Unused)	19		(Unused)		
20		(Unused)	20		(Unused)		
21		(Unused)	21		(Unused)		
22		(Unused)	22		(Unused)		

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Pro	ofile nu	mber 1 (Simple PID control with	3 connect	ed con	trollers) on page 4		
	ROFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit	Contents of assignment	Word position	Bit	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: A1_L1_1	5		01: A1_L1_1		
6		02: A1_L1_1	6		02: A1_L1_1		
7		03: A1_L1_1	7		03: A1_L1_1		
8		01: A2_L1_1	8		01: A2_L1_1		
9		02: A2_L1_1	9		02: A2_L1_1		
10		03: A2_L1_1	10		03: A2_L1_1		
11		01: A3_L1_1	11		01: A3_L1_1		
12		02: A3_L1_1	12		02: A3_L1_1		
13		03: A3_L1_1	13		03: A3_L1_1		
14		01: A4_L1_1	14		01: A4_L1_1		
15		02: A4_L1_1	15		02: A4_L1_1		
16		03: A4_L1_1	16		03: A4_L1_1		
17		01: A5_L1_1	17		01: A5_L1_1		
18		02: A5_L1_1 UT35A: unused	18		02: A5_L1_1 UT35A: unused		
19		03: A5_L1_1	19		03: A5_L1_1		
20		(Unused)	20		(Unused)		
21		(Unused)	21		(Unused)		
22		(Unused)	22		(Unused)		

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Intentionally blank

Profile number 2 (Simple PID control with 5 connected controllers) UT55A UT35A Example: PLC PROFIBUS-DP/DeviceNet UTAdvanced PROFIBUS-DP/DeviceNet slave (with PROFIBUS-DP/ PROFIBUS-DP/DeviceNet communication address = 03 Profile number = 2 DeviceNet Modbus master communication) RS-485 communication address = 01 (address 01) RS-485 (Modbus/RTU) Modbus slave UTAdvanced *500.0 500.*6 500.C *500.*0 (with RS-485 communication) RS-485 RS-485 RS-485 RS-485 communication address = 02 communication address = 03 communication address = 04 communication address = 05 (address 02) (address 03) (address 04) (address 05)

Page 1

		IN area			trollers) on page 1 OUT area	
PROFIBUS-DP/DeviceNet slave (UTAdvanced) →			PRO	OFIBUS-I	OP/DeviceNet master → PROFIBUS-DP/	
		OFIBUS-DP/DeviceNet master			eviceNet slave (UTAdvanced)	
Word osition	Nord Bit Contents of assignment		Word position	Bit position	Contents of assignment	
0	0	Receive data valid	0	0	Rescan request	
	1	During-write		1	(Reserved)	
	2	Write acknowledgement		2	Write request	
	3	(Reserved)		3	(Reserved)	
	4	(Reserved)		4	(Reserved)	
	5	(Reserved)		5	(Reserved)	
	6	(Reserved)		6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)	
4		Current page	4		Page change request	
5		01: PV_L1	5		(Unused)	
6		02: PV_L1	6		(Unused)	
7		03: PV_L1	7		(Unused)	
8		04: PV_L1	8		(Unused)	
9		05: PV_L1	9		(Unused)	
10		01: CSP_L1	10		01: SP_L1_1	
11		02: CSP_L1	11		02: SP_L1_1	

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IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master Word Rit			PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS- DeviceNet slave (UTAdvanced)			
Word	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
12	pooliion	03: CSP_L1	12	poomon	03: SP_L1_1		
13		04: CSP_L1	13		04: SP_L1_1		
14		05: CSP_L1	14		05: SP_L1_1		
15		01: OUT_L1	15		01: MOUT_L1		
16		02: OUT_L1	16		02: MOUT_L1		
17		03: OUT_L1	17		03: MOUT_L1		
18		04: OUT_L1	18		04: MOUT_L1		
19		05: OUT_L1	19		05: MOUT_L1		
20		01: H.OUT_L1	20		01: MOUT_L1		
21		02: H.OUT_L1	21		02: MOUT_L1		
					_		
22		03: H.OUT_L1	22		03: MOUT_L1		
23		04: H.OUT_L1	23		04: MOUT_L1		
24		05: H.OUT_L1	24		05: MOUT_L1		
25		01: C.OUT_L1	25		01: MOUTc_L1		
26		02: C.OUT_L1	26		02: MOUTc_L1		
27		03: C.OUT_L1	27		03: MOUTc_L1		
28		04: C.OUT_L1	28		04: MOUTc_L1		
29		05: C.OUT_L1	29		05: MOUTc_L1		
30	0	01: A.M	30	0	01: A.M		
	1	01: R.L_L1		1	01: R.L_L1		
	3	01: S.R (Unused)		3	01: S.R (Unused)		
	4	(Unused)		4	(Unused)		
	5	(Unused)		5	(Unused)		
	6	(Unused)		6	(Unused)		
	7	(Unused)		7	(Unused)		
	8	01: ALM1_L1		8	(Unused)		
	9	01: ALM1_L1		9	(Unused)		
	10	01: ALM2_L1 01: ALM3_L1		10	(Unused)		
	11	01: ALM3_L1		11	(Unused)		
	12			12	(Unused)		
	13	01: ALM5_L1 01: ALM6_L1		13	(Unused)		
	14	01: ALM7_L1 UT35A: unused		14	(Unused)		
	15	01: ALM7_L1		15	(Unused)		
31	0	02: A.M	31	0	02: A.M		
01	1	02: R.L_L1	31	1	02: A.M 02: R.L L1		
	2	02: S.R 02: S.R		2	02: S.R		
	3	(Unused)		3	(Unused)		
	4	(Unused)		4	(Unused)		
					'		
	5	(Unused)		5	(Unused)		
	6	(Unused)		6	(Unused)		
	7	(Unused)		7	(Unused)		
	8	02: ALM1_L1		8	(Unused)		
	9	02: ALM2_L1		9	(Unused)		
	10	02: ALM3_L1		10	(Unused)		
	11	02: ALM4_L1		11	(Unused)		
	12	02: ALM5_L1_		12	(Unused)		
					La L		
	13	02: ALM6_L1 >UT35A: unused		13	(Unused)		
		02: ALM6_L1 02: ALM7_L1 02: ALM8_L1 UT35A: unused		13 14	(Unused)		

Profile number 2 (Simple PID control with 5 connected controllers) on page 1

Pro	ofile nu	mber 2 (Simple PID control with	5 c	onnect	ed con	trollers) on page 1		
		IN area		OUT area				
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		OP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
32	0	03: A.M		32	0	03: A.M		
	1	03: R.L L1			1	03: R.L L1		
	2	03: S.R			2	03: S.R		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	03: ALM1_L1			8	(Unused)		
	9	03: ALM2_L1			9	(Unused)		
	10	03: ALM3_L1			10	(Unused)		
	11	03: ALM4_L1			11	(Unused)		
	12	03: ALM5_L1			12	(Unused)		
	13	03: ALM6_L1 >UT35A: unused			13	(Unused)		
	14	03: ALM7_L1			14	(Unused)		
	15	03: ALM8_L1			15	(Unused)		
33	0	04: A.M		33	0	04: A.M		
	1	04: R.L_L1			1	04: R.L_L1		
	2	04: S.R			2	04: S.R		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	04: ALM1_L1			8	(Unused)		
	9	04: ALM2_L1			9	(Unused)		
	10	04: ALM3_L1			10	(Unused)		
	11	04: ALM4_L1			11	(Unused)		
	12	04: ALM5_L1			12	(Unused)		
	13	04: ALM6_L1 VT35A: unused			13	(Unused)		
	14	04: ALM7_L1			14	(Unused)		
34	15 0	04: ALM8_L1		34	15 0	(Unused) 05: A.M		
34	1	05: R.L L1		34	1	05: R.L L1		
	2	05: S.R 05: S.R			2	05: S.R 05: S.R		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	05: ALM1_L1			8	(Unused)		
	9	05: ALM2_L1			9	(Unused)		
	10	05: ALM3_L1			10	(Unused)		
	11	05: ALM4_L1			11	(Unused)		
	12	05: ALM5_L1			12	(Unused)		
	13	05: ALM6 L1			13	(Unused)		
	14	05: ALM7_L1 UT35A: unused			14	(Unused)		
	15	05: ALM8_L1			15	(Unused)		

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Page 2

Pro	ofile nu	mber 2 (Simple PID control with	5 connect	ed con	trollers) on page 2
		IN area			OUT area
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)
•	•	(cooperation to an page 1)	•	•	(cooprome name of on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		02: P_L1_1	6		02: P_L1_1
7		03: P_L1_1	7		03: P_L1_1
8		04: P_L1_1	8		04: P_L1_1
9		05: P_L1_1	9		05: P_L1_1
10		01: I_L1_1	10		01: I_L1_1
11		02: I_L1_1	11		02: I_L1_1
12		03: I_L1_1	12		03: I_L1_1
13		04: I_L1_1	13		04: I_L1_1
14		05: I_L1_1	14		05: I_L1_1
15		01: D_L1_1	15		01: D_L1_1
16		02: D_L1_1	16		02: D_L1_1
17		03: D_L1_1	17		03: D_L1_1
18		04: D_L1_1 05: D_L1_1	18		04: D_L1_1 05: D_L1_1
20		01: SPNO.	20		01: SPNO.
21		02: SPNO.	21		02: SPNO.
22		03: SPNO.	22		03: SPNO.
23		04: SPNO.	23		04: SPNO.
24		05: SPNO.	24		05: SPNO.
25 26	-	(Unused)	25 26		(Unused)
26	-	(Unused)	26		(Unused)
28		(Unused)	28		(Unused)
29		(Unused)	29	<u> </u>	(Unused)
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32	İ	(Unused)	32		(Unused)
33	ĺ	(Unused)	33		(Unused)
34		(Unused)	34		(Unused)

Page 3

Pro	file nu	mber 2 (Simple PID control with	connect	ed con	
PR		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word position	Bit	Contents of assignment	Word	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
-	3	(Reserved) (Reserved)		3	(Reserved)
-	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
		The fixed-part is omitted			The fixed-part is omitted
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: Pc_L1_1	5		01: Pc_L1_1
6		02: Pc_L1_1	6		02: Pc_L1_1
7		03: Pc_L1_1	7		03: Pc_L1_1
8		04: Pc_L1_1	8		04: Pc_L1_1 05: Pc_L1_1
9		05: Pc_L1_1	9		
10		01: lc_L1_1	10		01: lc_L1_1
12		02: lc_L1_1 03: lc_L1_1	12		02: Ic_L1_1 03: Ic_L1_1
13		04: lc_L1_1	13		04: lc_L1_1
14		05: lc_L1_1	14		05: lc_L1_1
15		01: Dc_L1_1	15		01: Dc_L1_1
16		02: Dc_L1_1	16		02: Dc_L1_1
17		03: Dc_L1_1	17		03: Dc_L1_1
18		04: Dc_L1_1	18		04: Dc_L1_1
19		05: Dc_L1_1	19		05: Dc_L1_1
20		01: SPNO.	20		01: SPNO.
21		02: SPNO.	21		02: SPNO.
22		03: SPNO.	22		03: SPNO.
23		04: SPNO.	23		04: SPNO.
24		05: SPNO.	24		05: SPNO.
25		(Unused)	25		(Unused)
26		(Unused)	26		(Unused)
27 28		(Unused)	27		(Unused)
29		(Unused)	29		(Unused)
30		(Unused)	30		(Unused)
31		(Unused)	31		(Unused)
32		(Unused)	32		(Unused)
33 34		(Unused)	33 34		(Unused)

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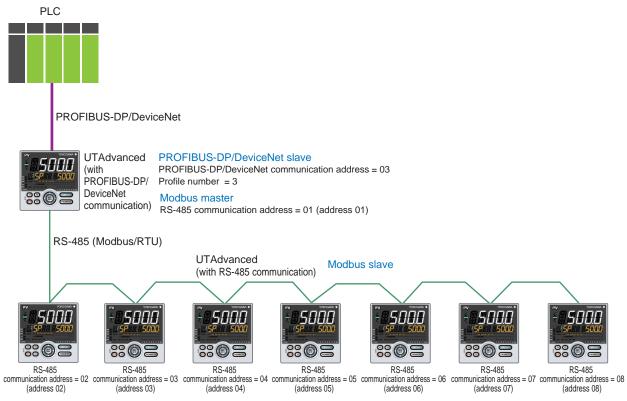
Page 4

Pro	ofile nu	mber 2 (Simple PID control with	5 connect	ed con	trollers) on page 4			
		IN area		OUT area				
		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master			DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position				
0	0	Receive data valid	0	0	Rescan request			
	2	During-write Write acknowledgement		2	(Reserved) Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5 6	(Reserved)		5 6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
					,			
•		The fixed-part is omitted	•	•	The fixed-part is omitted			
•	:	(See profile number 0 on page 1)		:	(See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: A1_L1_1	5		01: A1_L1_1			
6		02: A1_L1_1	6		02: A1_L1_1			
7		03: A1_L1_1	7		03: A1_L1_1			
8		04: A1_L1_1	8		04: A1_L1_1			
9		05: A1_L1_1	9		05: A1_L1_1			
10		01: A2_L1_1	10		01: A2_L1_1			
11		02: A2_L1_1	11		02: A2_L1_1			
12		03: A2_L1_1 04: A2_L1_1	12		03: A2_L1_1 04: A2_L1_1			
13		04: A2_L1_1 05: A2_L1_1	14		04: A2_L1_1 05: A2_L1_1			
15		01: A3_L1_1	15		01: A3_L1_1			
16		02: A3_L1_1	16		02: A3_L1_1			
17		03: A3_L1_1	17		03: A3_L1_1			
18		04: A3_L1_1	18		04: A3_L1_1			
19		05: A3_L1_1	19		05: A3_L1_1			
20		01: A4_L1_1	20		01: A4_L1_1			
21		02: A4_L1_1	21		02: A4_L1_1			
22		03: A4_L1_1	22		03: A4_L1_1			
23		04: A4_L1_1	23		04: A4_L1_1			
24		05: A4_L1_1	24		05: A4_L1_1			
25		01: A5_L1_1	25		01: A5_L1_1			
26		02: A5_L1_1	26		02: A5_L1_1			
27		03: A5_L1_1 UT35A: unused	27		03: A5_L1_1 UT35A: unused			
28		04: A5_L1_1 05: A5_L1_1	28		04: A5_L1_1 05: A5_L1_1			
30		(Unused)			(Unused)			
31		(Unused)	30		(Unused)			
32		(Unused)	32		(Unused)			
33		(Unused)	33		(Unused)			
34		(Unused)	34		(Unused)			

Profile number 3 (Simple PID control with 8 connected controllers)







Page 1

IN area				OUT area			
PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-				OFIBUS-I	OP/DeviceNet master → PROFIBUS-DP/		
DP/DeviceNet master				De	eviceNet slave (UTAdvanced)		
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: PV_L1	5		(Unused)		
6		02: PV_L1	6		(Unused)		
7		03: PV_L1	7		(Unused)		
8		04: PV_L1	8		(Unused)		
9		05: PV_L1	9		(Unused)		
10		06: PV_L1	10		(Unused)		
11		07: PV L1	11		(Unused)		

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PROFIE	BUS-DP/D	IN area DeviceNet slave (UTAdvanœd) → PROFIBUS- DP/DeviceNet master		PRO	OFIBUS- D
Word position	Bit position	Contents of assignment		Word	Bit
12	position	08: PV_L1		position 12	position
13		01: CSP_L1		13	
14		02: CSP_L1		14	
15		03: CSP_L1		15	
16		04: CSP_L1		16	
17		05: CSP_L1		17	
18		06: CSP_L1		18	
19		07: CSP_L1		19	
20		08: CSP_L1		20	
21		01: OUT_L1		21	
22		02: OUT_L1		22	
23		03: OUT_L1		23	
24		04: OUT_L1		24	
25		05: OUT_L1		25	
26		06: OUT_L1		26	
27		07: OUT_L1		27	
28		08: OUT_L1		28	
29		01: H.OUT_L1		29	
30		02: H.OUT_L1		30	
31		03: H.OUT_L1		31	
32		04: H.OUT_L1		32	
33		05: H.OUT_L1		33	
34		06: H.OUT_L1		34	
35		07: H.OUT_L1		35	
36		08: H.OUT_L1		36	
37		01: C.OUT_L1		37	
38		02: C.OUT_L1		38	
39		03: C.OUT_L1		39	
40		04: C.OUT_L1		40	
41		05: C.OUT_L1		41	
42		06: C.OUT_L1		42	
43		07: C.OUT_L1		43	
44		08: C.OUT_L1	1	44	

Profile number 3 (Simple PID control with 8 connected controllers) on page 1

Word	Bit	Contents of assignment
osition	position	
12		(Unused)
13		01: SP_L1_1
14		02: SP_L1_1
15		03: SP_L1_1
16		04: SP_L1_1
17		05: SP_L1_1
18		06: SP_L1_1
19		07: SP_L1_1
20		08: SP_L1_1
21		01: MOUT_L1
22		02: MOUT_L1
23		03: MOUT_L1
24		04: MOUT_L1
25		05: MOUT_L1
26		06: MOUT_L1
27		07: MOUT_L1
28		08: MOUT_L1
29		01: MOUT_L1
30		02: MOUT_L1
31		03: MOUT_L1
32		04: MOUT_L1
33		05: MOUT_L1
34		06: MOUT_L1
35		07: MOUT_L1
36		08: MOUT_L1
37		01: MOUTc_L1
38		02: MOUTc_L1
39		03: MOUTc_L1
40		04: MOUTc_L1
41		05: MOUTc_L1
42		06: MOUTc_L1
43		07: MOUTc_L1
44		08: MOUTc_L1

OUT area

Pro	ofile nu	mber 3 (Simple PID control with	8 connected controllers) on page 1			
		IN area	OUT area			
		DeviceNet slare (UTAdvanœd) → PROFIBUS- DP/DeviceNet master		De	DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)	
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
45	0	01: A.M	45	0	01: A.M	
	1	01: R.L_L1		1	01: R.L_L1	
	3	01: S.R (Unused)		3	01: S.R (Unused)	
	4	(Unused)		4	(Unused)	
	5	(Unused)		5	(Unused)	
	6	(Unused)		6	(Unused)	
	7	(Unused)		7	(Unused)	
	8	01: ALM1_L1 01: ALM2_L1		8	(Unused)	
	10	01: ALM3_L1		10	(Unused)	
	11	01: ALM4_L1		11	(Unused)	
	12	01: ALM5_L1_		12	(Unused)	
	13	01: ALM6_L1 UT35A: unused		13	(Unused)	
	14 15	01: ALM7_L1 01: ALM8_L1		14 15	(Unused)	
46	0	02: A.M	46	0	02: A.M	
	1	02: R.L_L1		1	02: R.L_L1	
	2	02: S.R		2	02: S.R	
	3	(Unused)		3	(Unused)	
	<u>4</u> 5	(Unused)		5	(Unused)	
	6	(Unused)		6	(Unused)	
	7	(Unused)		7	(Unused)	
	8	02: ALM1_L1		8	(Unused)	
	9	02: ALM2_L1		9	(Unused)	
	10 11	02: ALM3_L1 02: ALM4_L1		10	(Unused)	
	12	02: ALM4_L1 02: ALM5_L1		12	(Unused)	
	13	02: ALM6_L1 VT35A: unused		13	(Unused)	
	14	02: ALM7_L1_		14	(Unused)	
47	15	02: ALM8_L1 J	47	15	(Unused)	
47	1	03: A.M 03: R.L_L1	47	1	03: A.M 03: R.L L1	
	2	03: S.R		2	03: S.R	
	3	(Unused)		3	(Unused)	
	4	(Unused)		4	(Unused)	
	5 6	(Unused)		5 6	(Unused)	
	7	(Unused)		7	(Unused)	
	8	03: ALM1_L1		8	(Unused)	
	9	03: ALM2_L1		9	(Unused)	
	10	03: ALM3_L1		10	(Unused)	
	11 12	03: ALM4_L1 03: ALM5_L1		11 12	(Unused)	
	13	03: ALM6 L1		13	(Unused)	
	14	03: ALM7_L1 UT35A: unused		14	(Unused)	
	15	03: ALM8_L1		15	(Unused)	
48	0	04: A.M	48	0	04: A.M	
	2	04: R.L_L1 04: S.R		2	04: R.L_L1 04: S.R	
	3	(Unused)		3	(Unused)	
	4	(Unused)		4	(Unused)	
	5	(Unused)		5	(Unused)	
	6	(Unused)		6	(Unused)	
	7 8	(Unused) 04: ALM1_L1		8	(Unused)	
	9	04: ALM1_L1 04: ALM2_L1		9	(Unused)	
	10	04: ALM3_L1		10	(Unused)	
	11	04: ALM4_L1		11	(Unused)	
	12	04: ALM5_L1		12	(Unused)	
	13	04: ALM6_L1 04: ALM7_L4 UT35A: unused		13	(Unused)	
	14 15	04: ALM7_L1 04: ALM8_L1		14 15	(Unused)	
	10	UT. /\LIVIU_L1 /		13	(Onasca)	

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Pro	ofile nu	mber 3 (Simple PID control with	8 connect	ed con	trollers) on page 1		
		IN area	OUT area				
PROFIE	BUS-DP/D	DeviceNet slave (UTAdvanced) \rightarrow PROFIBUS-	PROFIBUS-DP/DeviceNet master → PROFIBUS-DI				
		DP/DeviceNet master			DeviceNet slave (UTAdvanced)		
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment		
position 49	position		position 49	position			
49	1	05: A.M 05: R.L L1	49	1	05: A.M 05: R.L_L1		
	2	05: S.R 05: S.R		2	05: S.R		
	3	(Unused)		3	(Unused)		
	4	(Unused)		4	(Unused)		
	5	(Unused)		5	(Unused)		
	6	(Unused)		6	(Unused)		
	7	(Unused)		7	(Unused)		
	8	05: ALM1_L1		8	(Unused)		
	9	05: ALM2_L1		9	(Unused)		
	10	05: ALM3_L1		10	(Unused)		
	11	05: ALM4_L1		11	(Unused)		
	12	05: ALM5_L1		12	(Unused)		
	14	05: ALM7_L1 \ UT35A: unused		14	(Unused)		
	15	05: ALM8_L1		15	(Unused)		
50	0	06: A.M	50	0	06: A.M		
	1	06: R.L_L1		1	06: R.L_L1		
	2	06: S.R		2	06: S.R		
	3	(Unused)		3	(Unused)		
	4	(Unused)		4	(Unused)		
	5	(Unused)		5	(Unused)		
	6	(Unused)		6	(Unused)		
	7	(Unused)		7	(Unused)		
	8	06: ALM1_L1 06: ALM2_L1		9	(Unused)		
	10	06: ALM3_L1		10	(Unused)		
	11	06: ALM4_L1		11	(Unused)		
	12	06: ALM5_L1		12	(Unused)		
	13	06: ALM6_L1 VT35A: unused		13	(Unused)		
	14	06: ALM7_L1 0135A. unused		14	(Unused)		
	15	06: ALM8_L1		15	(Unused)		
51	0	07: A.M	51	0	07: A.M		
	1	07: R.L_L1		1	07: R.L_L1		
	3	07: S.R (Unused)		3	07: S.R (Unused)		
	4	(Unused)		4	(Unused)		
	5	(Unused)		5	(Unused)		
	6	(Unused)		6	(Unused)		
	7	(Unused)		7	(Unused)		
	8	07: ALM1_L1		8	(Unused)		
	9	07: ALM2_L1		9	(Unused)		
	10	07: ALM3_L1		10	(Unused)		
	11	07: ALM4_L1		11	(Unused)		
	12	07: ALM5_L1 07: ALM6_L1		12	(Unused)		
	14	07: ALM6_L1 07: ALM7_L1 UT35A: unused		14	(Unused)		
	15	07: ALM7_L1		15	(Unused)		
52	0	08: A.M	52	0	08: A.M		
	1	08: R.L_L1		1	08: R.L_L1		
	2	08: S.R		2	08: S.R		
	3	(Unused)		3	(Unused)		
	4	(Unused)		4	(Unused)		
	5	(Unused)		5	(Unused)		
	7	(Unused)		7	(Unused)		
	8	(Unused) 08: ALM1_L1		8	(Unused)		
	9	08: ALM2_L1		9	(Unused)		
	10	08: ALM3_L1		10	(Unused)		
	11	08: ALM4_L1		11	(Unused)		
	12	08: ALM5_L1		12	(Unused)		
	13	08: ALM6_L1 }UT35A: unused		13	(Unused)		
1	14	08: ALM7_L1		14	(Unused)		
	15		1	15	(Unused)		

Pro	ofile nu	mber 3 (Simple PID control with 8	3 connect				
	ROFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OFIBUS-I	OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word	Bit position	Contents of assignment	Word	Bit	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
		The fixed-part is omitted			The fixed-part is omitted		
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)		
•	•		•	•			
4		Current page	4		Page change request		
5		01: P_L1_1	5		01: P_L1_1		
6		02: P_L1_1	6		02: P_L1_1		
7		03: P_L1_1	7		03: P_L1_1		
8		04: P_L1_1	8		04: P_L1_1		
9		05: P_L1_1	9		05: P_L1_1		
10		06: P_L1_1	10		06: P_L1_1		
11		07: P_L1_1	11		07: P_L1_1		
12		08: P_L1_1	12		08: P_L1_1		
13		01: I_L1_1	13		01: I_L1_1		
14		02: I_L1_1	14		02: I_L1_1		
15		03: I_L1_1	15		03: I_L1_1		
16		04: I_L1_1	16		04: I_L1_1		
17		05: I_L1_1	17		05: I_L1_1		
18		06: I_L1_1	18		06: I_L1_1		
19		07: I_L1_1	19		07: I_L1_1		
20		08: I_L1_1	20		08: I_L1_1		
21		01: D_L1_1	21		01: D_L1_1		
22		02: D_L1_1	22		02: D_L1_1		
23		03: D_L1_1	23		03: D_L1_1		
24		04: D_L1_1	24		04: D_L1_1		
25		05: D_L1_1	25		05: D_L1_1		
26		06: D_L1_1	26		06: D_L1_1		
27		07: D_L1_1	27		07: D_L1_1		
28		08: D_L1_1	28		08: D_L1_1		
29		01: SPNO.	29		01: SPNO.		
30		02: SPNO.	30		02: SPNO.		
31		03: SPNO.	31		03: SPNO.		

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Pro	ofile nu	mber 3 (Simple PID control with 8	C	connected controllers) on page 2				
		IN area		OUT area				
PF	ROFIBUS	-DP/DeviceNet slave (UTAdvanced) →		PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PROFIBUS-DP/DeviceNet master			DeviceNet slave (UTAdvanced)				
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment		
position	position	Contents of assignment		position	position	Contents of assignment		
32		04: SPNO.		32		04: SPNO.		
33		05: SPNO.		33		05: SPNO.		
34		06: SPNO.		34		06: SPNO.		
35		07: SPNO.		35		07: SPNO.		
36		08: SPNO.		36		08: SPNO.		
37		(Unused)		45		(Unused)		
38		(Unused)		46		(Unused)		
39		(Unused)		47		(Unused)		
40		(Unused)		48		(Unused)		
41		(Unused)		49		(Unused)		
42		(Unused)		50		(Unused)		
43		(Unused)		51		(Unused)		
44		(Unused)		52		(Unused)		
45		(Unused)		53		(Unused)		
46		(Unused)		46		(Unused)		
47		(Unused)		47		(Unused)		
48		(Unused)		48		(Unused)		
49		(Unused)		49		(Unused)		
50		(Unused)		50		(Unused)		
51		(Unused)		51		(Unused)		
52		(Unused)		52		(Unused)		

Pro	ofile nu	mber 3 (Simple PID control with 8	connected controllers) on page 3				
	ROFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OFIBUS-	OUT area DP/DeviceNet master → PROFIBUS-DP/		
Word	Bit		Word	Bit	eviceNet slave (UTAdvanced)		
•	position			position	Contents of assignment		
0	0	Receive data valid	0	1	Rescan request		
	2	During-write Write acknowledgement		2	(Reserved) Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5 6	(Reserved)		5	(Reserved)		
	7	(Reserved)		7	(Reserved)		
		The fixed-part is omitted	•		The fixed-part is omitted		
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)		
•	•		•	•			
4		Current page	4		Page change request		
5		01: Pc_L1_1	5		01: Pc_L1_1		
6		02: Pc_L1_1	6		02: Pc_L1_1		
7		03: Pc_L1_1	7		03: Pc_L1_1		
8		04: Pc_L1_1	8		04: Pc_L1_1		
9		05: Pc_L1_1	9		05: Pc_L1_1		
10		06: Pc_L1_1	10		06: Pc_L1_1		
11		07: Pc_L1_1	11		07: Pc_L1_1		
12		08: Pc_L1_1	12		08: Pc_L1_1		
13		01: lc_L1_1	13		01: lc_L1_1		
14		02: lc_L1_1	14		02: lc_L1_1		
15		03: lc_L1_1	15		03: lc_L1_1		
16		04: lc_L1_1	16		04: lc_L1_1		
17		05: lc_L1_1	17		05: lc_L1_1		
18		06: lc_L1_1	18		06: lc_L1_1		
19		07: lc_L1_1 08: lc_L1_1	19		07: lc_L1_1		
20			20		08: lc_L1_1		
21		01: Dc_L1_1 02: Dc_L1_1	21		01: Dc_L1_1 02: Dc_L1_1		
23		03: Dc_L1_1	23		03: Dc_L1_1		
24		04: Dc_L1_1	24		04: Dc_L1_1		
25		05: Dc_L1_1	25		05: Dc_L1_1		
26		06: Dc_L1_1	26		06: Dc_L1_1		
27		07: Dc_L1_1	27		07: Dc_L1_1		
28		08: Dc_L1_1	28		08: Dc_L1_1		
29		01: SPNO.	29		01: SPNO.		
30		02: SPNO.	30		02: SPNO.		
31		03: SPNO.	31		03: SPNO.		

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Pro	ofile nu	mber 3 (Simple PID control with 8	3 C	connected controllers) on page 3			
		IN area		OUT area			
PF		-DP/DeviceNet slave (UTAdvanced) →		PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/			
		OFIBUS-DP/DeviceNet master		DeviceNet slave (UTAdvanced)			
Word	Bit position	Contents of assignment		Word	Bit position	Contents of assignment	
•	position	04: SPNO.			position	04: SPNO.	
32		04: SPNO.		32		104: SPNO.	
33		05: SPNO.		33		05: SPNO.	
34		06: SPNO.		34		06: SPNO.	
35		07: SPNO.		35		07: SPNO.	
36		08: SPNO.		36		08: SPNO.	
37	İ	(Unused)		37		(Unused)	
38		(Unused)		38		(Unused)	
39		(Unused)		39		(Unused)	
40		(Unused)		40		(Unused)	
41		(Unused)		41		(Unused)	
42		(Unused)		42		(Unused)	
43		(Unused)		43		(Unused)	
44		(Unused)		44		(Unused)	
45		(Unused)		45		(Unused)	
46		(Unused)		46		(Unused)	
47		(Unused)		47		(Unused)	
48		(Unused)		48		(Unused)	
49		(Unused)		49		(Unused)	
50		(Unused)		50		(Unused)	
51		(Unused)		51		(Unused)	
52		(Unused)		52		(Unused)	

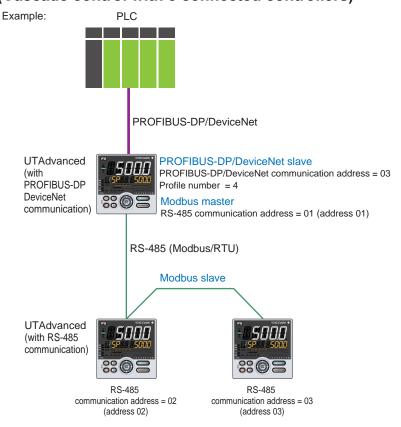
Profile number 3 (Simple PID control with 8 connected controllers) on page 4 IN area OUT area					
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	2	During-write Write acknowledgement		2	(Reserved) Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5 6	(Reserved)		5 6	(Reserved)
	7	(Reserved)		7	(Reserved)
•		The fixed-part is omitted			The fixed-part is omitted
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: A1_L1_1	5		01: A1_L1_1
6		02: A1_L1_1	6		02: A1_L1_1
7		03: A1_L1_1	7		03: A1_L1_1
8		04: A1_L1_1	8		04: A1_L1_1
9		05: A1_L1_1	9		05: A1_L1_1
10		06: A1_L1_1	10		06: A1_L1_1
11		07: A1_L1_1	11		07: A1_L1_1
12		08: A1_L1_1	12		08: A1_L1_1
13		01: A2_L1_1	13		01: A2_L1_1
14		02: A2_L1_1	14		02: A2_L1_1
15		03: A2_L1_1	15		03: A2_L1_1
16		04: A2_L1_1	16		04: A2_L1_1
17		05: A2_L1_1	17		05: A2_L1_1
18		06: A2_L1_1	18		06: A2_L1_1
19		07: A2_L1_1	19		07: A2_L1_1
20		08: A2_L1_1	20		08: A2_L1_1
21		01: A3_L1_1	21		01: A3_L1_1
22		02: A3_L1_1	22		02: A3_L1_1
23		03: A3_L1_1	23		03: A3_L1_1
24		04: A3_L1_1	24		04: A3_L1_1
25		05: A3_L1_1	25		05: A3_L1_1
26		06: A3_L1_1	26		06: A3_L1_1
27		07: A3_L1_1	27		07: A3_L1_1
28		08: A3_L1_1	28		08: A3_L1_1
29		01: A4_L1_1	29		01: A4_L1_1
30		02: A4_L1_1	30		02: A4_L1_1
31		03: A4_L1_1	31		03: A4_L1_1
32		04: A4_L1_1	32		04: A4_L1_1
33		05: A4_L1_1	33		05: A4_L1_1

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Pro	ofile nu	mber 3 (Simp	le PID control with 8	connect	ed con	trollers) on pa	age 4		
		IN area			OUT area				
PF		-DP/DeviceNet slav OFIBUS-DP/Device	ve (UTAdvanced) →	PRO	PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)				
Word	Word Bit			Word	Word Bit				
	position	Contents	s of assignment	position		Content	s of assignment		
34		06: A4_L1_1		34		06: A4_L1_1			
35		07: A4_L1_1		35		07: A4_L1_1			
36		08: A4_L1_1		36		08: A4_L1_1			
37		01: A5_L1_1		37		01: A5_L1_1			
38		02: A5_L1_1		38		02: A5_L1_1			
39		03: A5_L1_1		39		03: A5_L1_1			
40		04: A5_L1_1	LITOFA	40		04: A5_L1_1	LITOFALLIBORE		
41		05: A5_L1_1	UT35A: unused	41		05: A5_L1_1	UT35A: unused		
42		06: A5_L1_1		42		06: A5_L1_1			
43		07: A5_L1_1		43		07: A5_L1_1			
44		08: A5_L1_1		44		08: A5_L1_1			
45		(Unused)		45		(Unused)			
46		(Unused)		46		(Unused)			
47		(Unused)		47		(Unused)			
48		(Unused)		48		(Unused)			
49		(Unused)		49		(Unused)			
50		(Unused)		50		(Unused)			
51		(Unused)		51		(Unused)			
52		(Unused)		52		(Unused)			

Profile number 4 (Cascade control with 3 connected controllers)





Page 1

Dr	OCIDIIO	IN area -DP/DeviceNet slave (UTAdvanced) →	DD.	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/			
Pr	PROFIBUS-DP/DeviceNet slave (or Advanced) → PROFIBUS-DP/DeviceNet master				or/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word osition	Bit position	Contents of assignment	Word	Bit	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: PV_L1	5		(Unused)		
6		02: PV_L1	6		(Unused)		
7		03: PV_L1	7		(Unused)		
8		01: PV_L2	8		(Unused)		
9		02: PV_L2	9		(Unused)		
10		03: PV_L2	10		(Unused)		
11		01: CSP_L1	11		01: SP_L1_1		
12		02: CSP_L1	12		02: SP_L1_1		

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	PROBIT OF THE PR	IN area -DP/DeviceNet slave (UTAdvanced) → DFIBUS-DP/DeviceNet master Contents of assignment 03: CSP_L1 01: CSP_L2 02: CSP_L2 03: CSP_L2 01: C.A.M 02: C.A.M 03: C.A.M 03: C.A.M 01: OUT_L2 02: OUT_L2 03: OUT_L2	PRO Word position 13 14 15 16 17 18 19 20 21	De Bit	OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced) Contents of assignment 03: SP_L1_1 01: SP_L2_1 02: SP_L2_1 03: SP_L2_1 01: C.A.M 02: C.A.M
position po 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28	osition	03: CSP_L1 01: CSP_L2 02: CSP_L2 03: CSP_L2 01: C.A.M 02: C.A.M 03: C.A.M 01: OUT_L2	position 13 14 15 16 17 18 19 20		03: SP_L1_1 01: SP_L2_1 02: SP_L2_1 03: SP_L2_1 01: C.A.M 02: C.A.M
13		01: CSP_L2 02: CSP_L2 03: CSP_L2 01: C.A.M 02: C.A.M 03: C.A.M 01: OUT_L2 02: OUT_L2	13 14 15 16 17 18 19 20		01: SP_L2_1 02: SP_L2_1 03: SP_L2_1 01: C.A.M 02: C.A.M
15 16 17 18 19 20 21 22 23 24 25 26 27 28		02: CSP_L2 03: CSP_L2 01: C.A.M 02: C.A.M 03: C.A.M 01: OUT_L2 02: OUT_L2	15 16 17 18 19 20		02: SP_L2_1 03: SP_L2_1 01: C.A.M 02: C.A.M
16		03: CSP_L2 01: C.A.M 02: C.A.M 03: C.A.M 01: OUT_L2 02: OUT_L2	16 17 18 19 20		03: SP_L2_1 01: C.A.M 02: C.A.M
17 18 19 20 21 22 23 24 25 26 27 28		01: C.A.M 02: C.A.M 03: C.A.M 01: OUT_L2 02: OUT_L2	17 18 19 20		01: C.A.M 02: C.A.M
18		02: C.A.M 03: C.A.M 01: OUT_L2 02: OUT_L2	18 19 20		02: C.A.M
19 20 21 22 23 24 25 26 27 28		03: C.A.M 01: OUT_L2 02: OUT_L2	19		
20 21 22 23 24 25 26 27 28		01: OUT_L2 02: OUT_L2	20		03: C.A.M
21 22 23 24 25 26 27 28		02: OUT_L2			
22 23 24 25 26 27 28		_	21	1	01: MOUT_L2
23 24 25 26 27 28		03: OUT L2			02: MOUT_L2
24 25 26 27 28			22		03: MOUT_L2
25 26 27 28		01: H.OUT_L2	23		01: MOUT_L2
26 27 28		02: H.OUT_L2	24		02: MOUT_L2
27		03: H.OUT_L2	25		03: MOUT_L2
28		01: C.OUT_L2	26		01: MOUTc_L2
		02: C.OUT_L2	27		02: MOUTc_L2
29		03: C.OUT_L2	28		03: MOUTc_L2
	0	(Unused)	29	0	(Unused)
	2	01: R.L_L1 01: S.R	-	2	01: R.L_L1 01: S.R
	3	(Unused)	+	3	(Unused)
_	4	(Unused)	1	4	(Unused)
	5	(Unused)	1	5	(Unused)
	6	(Unused)	1	6	(Unused)
	7	(Unused)	1	7	(Unused)
	8	01: ALM1_L1	7	8	(Unused)
	9	01: ALM2 L1	1	9	(Unused)
	10	01: ALM3_L1	7	10	(Unused)
	11	01: ALM4_L1	7	11	(Unused)
	12	01: ALM5_L1	7	12	(Unused)
	13	01: ALM6_L1]	13	(Unused)
	14	01: ALM7_L1	_	14	(Unused)
	15	01: ALM8_L1		15	(Unused)
30	0	(Unused)	30	0	(Unused)
	1	(Unused)	4	1	(Unused)
<u> </u>	2	(Unused)	4	2	(Unused)
	3	(Unused)	-	3	(Unused)
	4	(Unused)	-	4	(Unused)
	5	(Unused)	-	5	(Unused)
	6 7	(Unused)		7	(Unused)
		01: ALM1_L2	 	8	(Unused)
		01: ALM1_L2 01: ALM2_L2		9	(Unused)
	10	01: ALM2_L2 01: ALM3_L2		10	(Unused)
	11	01: ALM3_L2 01: ALM4_L2	 	11	(Unused)
		01: ALM5_L2	1	12	(Unused)
_	12	01: ALM6_L2	1	13	(Unused)
_		01: ALM7_L2		14	(Unused)
	12 13 14	01: ALM8_L2	⊣	15	

Pro	ofile nu	mber 4 (Cascade control with 3 o	connected	contro	ollers) on page 1			
		IN area		OUT area				
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word	Bit position	Contents of assignment			
31	0	(Unused)	31	0	(Unused)			
	1	02: R.L_L1		1	02: R.L_L1			
	2	02: S.R		2	02: S.R			
	3	(Unused)		3	(Unused)			
	5	(Unused)		5	(Unused)			
	6	(Unused)		6	(Unused)			
	7	(Unused)		7	(Unused)			
	8	02: ALM1_L1		8	(Unused)			
	9	02: ALM2_L1		9	(Unused)			
	10 11	02: ALM3_L1 02: ALM4_L1		10 11	(Unused)			
	12	02: ALM5_L1		12	(Unused)			
	13	02: ALM6_L1		13	(Unused)			
	14	02: ALM7_L1		14	(Unused)			
	15	02: ALM8_L1		15	(Unused)			
32	0	(Unused)	32	0	(Unused)			
	1	(Unused)		1	(Unused)			
	3	(Unused)		3	(Unused)			
	4	(Unused)		4	(Unused)			
	5	(Unused)		5	(Unused)			
	6	(Unused)		6	(Unused)			
	7	(Unused)		7	(Unused)			
	8	02: ALM1_L2		8	(Unused)			
	9	02: ALM2_L2		9	(Unused)			
	10 11	02: ALM3_L2 02: ALM4_L2		10	(Unused)			
	12	02: ALM5 L2		12	(Unused)			
	13	02: ALM6_L2		13	(Unused)			
	14	02: ALM7_L2		14	(Unused)			
	15	02: ALM8_L2		15	(Unused)			
33	1	(Unused)	33	1	(Unused)			
	2	03: R.L_L1 03: S.R		2	03: R.L_L1 03: S.R			
	3	(Unused)		3	(Unused)			
	4	(Unused)		4	(Unused)			
	5	(Unused)		5	(Unused)			
	6	(Unused)		6	(Unused)			
	7	(Unused)		7	(Unused)			
	9	03: ALM1_L1 03: ALM2_L1		9	(Unused)			
	10	03: ALM3_L1		10	(Unused)			
	11	03: ALM4_L1		11	(Unused)			
	12	03: ALM5_L1		12	(Unused)			
	13	03: ALM6_L1		13	(Unused)			
	14	03: ALM7_L1		14	(Unused)			
24	15	03: ALM8_L1	0.4	15	(Unused)			
34	1	(Unused)	34	1	(Unused)			
	2	(Unused)		2	(Unused)			
	3	(Unused)		3	(Unused)			
	4	(Unused)		4	(Unused)			
	5	(Unused)		5	(Unused)			
	6	(Unused)		6	(Unused)			
	7	(Unused)		7	(Unused)			
	8 9	03: ALM1_L2 03: ALM2_L2		9	(Unused)			
	10	03: ALM3_L2		10	(Unused)			
	11	03: ALM4_L2		11	(Unused)			
	12	03: ALM5_L2		12	(Unused)			
	13	03: ALM6_L2]	13	(Unused)			
	14	03: ALM7_L2		14	(Unused)			
	15	03: ALM8_L2		15	(Unused)			

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Page 2

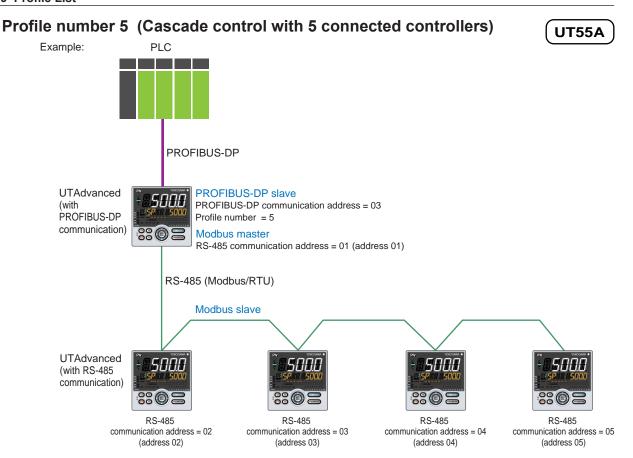
Pro	Profile number 4 (Cascade control with 3 connected controllers) on page 2						
		IN area		OUT area			
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	7	(Reserved)		7	(Reserved)		
	-	(Neserved)			(Neserved)		
		The fixed-part is omitted			The fixed-part is omitted		
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: P_L1_1	5		01: P_L1_1		
6		02: P_L1_1	6		02: P_L1_1		
7		03: P_L1_1	7		03: P_L1_1		
8		01: I_L1_1	8		01: I_L1_1		
9		02: I_L1_1	9		02: I_L1_1		
10		03: I_L1_1	10		03: I_L1_1		
11		01: D_L1_1	11		01: D_L1_1		
12		02: D_L1_1	12		02: D_L1_1		
13		03: D_L1_1	13		03: D_L1_1		
14		01: SPNO.	14		01: SPNO.		
15		02: SPNO.	15		02: SPNO.		
16		03: SPNO.	16		03: SPNO.		
17		01: P_L2_1	17		01: P_L2_1		
18		02: P_L2_1	18		02: P_L2_1		
19		03: P_L2_1	19		03: P_L2_1		
20		01: I_L2_1	20		01: I_L2_1		
21		02: I_L2_1	21		02: I_L2_1		
22		03: I_L2_1 01: D_L2_1	22		03: I_L2_1 01: D_L2_1		
23		01: D_L2_1 	23		01: D_L2_1 		
25		03: D_L2_1	25		03: D_L2_1		
26		(Unused)	26		(Unused)		
27		(Unused)	27		(Unused)		
28		(Unused)	28		(Unused)		
29		(Unused)	29	1	(Unused)		
30	İ	(Unused)	30		(Unused)		
31		(Unused)	31		(Unused)		
32		(Unused)	32		(Unused)		
33		(Unused)	33		(Unused)		
34		(Unused)	34		(Unused)		

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			PRO		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
		((,			(**************************************
		The fixed-part is omitted			The fixed-part is omitted
		(See profile number 0 on page 1)			(See profile number 0 on page 1)
	•				
4		Current page	4		Page change request
5		(Unused)	5		(Unused)
6		(Unused)	6		(Unused)
7		(Unused)	7		(Unused)
8		(Unused)	8		(Unused)
9		(Unused)	9		(Unused)
10		(Unused)	10		(Unused)
11		(Unused)	11		(Unused)
12		(Unused)	12		(Unused)
13		(Unused)	13		(Unused)
14		01: SPNO.	14		01: SPNO.
15		02: SPNO.	15		02: SPNO.
16		03: SPNO.	16		03: SPNO.
17		01: Pc_L2_1	17		01: Pc_L2_1
18		02: Pc_L2_1	18		02: Pc_L2_1
19		03: Pc_L2_1	19		03: Pc_L2_1
20		01: lc_L2_1	20		01: lc_L2_1
21		02: lc_L2_1	21		02: lc_L2_1
22		03: Ic_L2_1	22		03: lc_L2_1
23		01: Dc_L2_1	23		01: Dc_L2_1 02: Dc_L2_1
24		02: Dc_L2_1	24		
25		03: Dc_L2_1	25		03: Dc_L2_1
26	-	(Unused)	26	-	(Unused)
27	-	(Unused)	27	-	
28	-	(Unused)	28		(Unused)
29	-	(Unused)	29		(Unused)
30	-	(Unused)	30	-	(Unused)
31	-	(Unused)	31	-	(Unused)
32	-	(Unused)	32		(Unused)
33		(Unused)	33		(Unused)

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Pro	Profile number 4 (Cascade control with 3 connected controllers) on page 4							
IN area				OUT area				
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	2	During-write Write acknowledgement		2	(Reserved) Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5 6	(Reserved)		5 6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: A1_L1_1	5		01: A1_L1_1			
6		02: A1_L1_1	6		02: A1_L1_1			
7		03: A1_L1_1	7		03: A1_L1_1			
8		01: A2_L1_1	8		01: A2_L1_1			
9		02: A2_L1_1	9		02: A2_L1_1			
10		03: A2_L1_1	10		03: A2_L1_1			
11		01: A3_L1_1	11		01: A3_L1_1			
12		02: A3_L1_1	12		02: A3_L1_1			
13		03: A3_L1_1	13		03: A3_L1_1			
14		01: A4_L1_1	14		01: A4_L1_1			
15		02: A4_L1_1	15		02: A4_L1_1			
16		03: A4_L1_1	16		03: A4_L1_1			
17		01: A5_L1_1	17		01: A5_L1_1			
18		02: A5_L1_1	18		02: A5_L1_1			
19		03: A5_L1_1	19		03: A5_L1_1			
20		01: A1_L2_1	20		01: A1_L2_1			
21		02: A1_L2_1	21		02: A1_L2_1			
22		03: A1_L2_1	22		03: A1_L2_1			
23		01: A2_L2_1	23		01: A2_L2_1			
24		02: A2_L2_1	24		02: A2_L2_1			
25		03: A2_L2_1	25		03: A2_L2_1			
26		01: A3_L2_1	26		01: A3_L2_1			
27		02: A3_L2_1	27		02: A3_L2_1			
28		03: A3_L2_1	28		03: A3_L2_1			
29		01: A4_L2_1	29		01: A4_L2_1			
30		02: A4_L2_1	30		02: A4_L2_1			
31		03: A4_L2_1	31		03: A4_L2_1			
32		01: A5_L2_1	32		01: A5_L2_1			
33		02: A5_L2_1	33		02: A5_L2_1			
34		03: A5_L2_1	34		03: A5_L2_1			



Page 1

		IN area			OUT area			
PROFIBUS-DP/DeviceNet slave (UTAdvanced) →			PRO	PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
PROFIBUS-DP/DeviceNet master				DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: PV_L1	5		(Unused)			
6		02: PV_L1	6		(Unused)			
7		03: PV_L1	7		(Unused)			
8		04: PV_L1	8		(Unused)			
9		05: PV_L1	9		(Unused)			
10		01: PV_L2	10		(Unused)			
11		02: PV_L2	11		(Unused)			
12		03: PV_L2	12		(Unused)			

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Pro	ofile nu	mber 5 (Cascade control with	5 connec	ted
	OFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		R
Word	Bit	Contents of assignment	Word	
osition 13	position	04: PV L2	position 13	
14		05: PV L2	14	
15		01: CSP_L1	15	
16		02: CSP_L1	16	
17		03: CSP_L1	17	
18		04: CSP_L1	18	
19		05: CSP_L1	19	
20		01: CSP_L2	20	
21		02: CSP_L2	21	
22		03: CSP_L2	22	1
23		04: CSP_L2	23	\dagger
24		05: CSP_L2	24	1
25		01: C.A.M	25	1
26		02: C.A.M	26	\dagger
27		03: C.A.M	27	1
28		04: C.A.M	28	1
29		05: C.A.M	29	1
30		01: OUT_L2	30	1
31		02: OUT_L2	31	1
32		03: OUT_L2	32	1
33		04: OUT_L2	33	1
34		05: OUT_L2	34	1
35		01: H.OUT_L2	35	
36		02: H.OUT_L2	36	t
37		03: H.OUT_L2	37	t
38		04: H.OUT_L2	38	1
39		05: H.OUT_L2	39	1
40		01: C.OUT_L2	40	1
41		02: C.OUT_L2	41	1
42		03: C.OUT_L2	42	1
43		04: C.OUT_L2	43	
44		05: C.OUT_L2	44	

PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)							
Word	Bit	Contents of assignment						
position 13	position	(Unused)						
13								
14		(Unused)						
15		01: SP_L1_1						
16		02: SP_L1_1						
17		03: SP_L1_1						
18		04: SP_L1_1						
19		05: SP_L1_1						
20		01: SP_L2_1						
21		02: SP_L2_1						
22		03: SP_L2_1						
23		04: SP_L2_1						
24		05: SP_L2_1						
25		01: C.A.M						
26		02: C.A.M						
27		03: C.A.M						
28		04: C.A.M						
29		05: C.A.M						
30		01: MOUT_L2						
31		02: MOUT_L2						
32		03: MOUT_L2						
33		04: MOUT_L2						
34		05: MOUT_L2						
35		01: MOUT_L2						
36		02: MOUT_L2						
37		03: MOUT_L2						
38		04: MOUT_L2						
39		05: MOUT_L2						
40		01: MOUTc_L2						
41		02: MOUTc_L2						
42		03: MOUTc_L2						
43		04: MOUTc_L2						
44		05: MOUTc_L2						
	1	I						

Profile number 5 (Cascade control with 5 connected controllers) on page 1									
		IN area		OUT area					
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment			
45	0	(Unused)		45	0	(Unused)			
	1	01: R.L_L1			1	01: R.L_L1			
	3	01: S.R (Unused)			3	01: S.R (Unused)			
	4	(Unused)			4	(Unused)			
	5	(Unused)			5	(Unused)			
	6	(Unused)			6	(Unused)			
	7	(Unused)			7	(Unused)			
	8	01: ALM1_L1			8	(Unused)			
	10	01: ALM2_L1 01: ALM3_L1			9 10	(Unused)			
	11	01: ALM4_L1			11	(Unused)			
	12	01: ALM5_L1			12	(Unused)			
	13	01: ALM6_L1			13	(Unused)			
	14	01: ALM7_L1			14	(Unused)			
- 10	15	01: ALM8_L1		- 10	15	(Unused)			
46	1	(Unused)		46	1	(Unused)			
	2	(Unused)			2	(Unused)			
	3	(Unused)			3	(Unused)			
	4	(Unused)			4	(Unused)			
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	10	01: ALM3_L2			10	(Unused)			
	11	01: ALM4_L2			11	(Unused)			
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	13	01: ALM6_L2			13	(Unused)			
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47	0	(Unused)		47	0	(Unused)			
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6 (Unused) 7 (Unused) 8 04: ALM1_L2 9 04: ALM2_L2 10 04: ALM3_L2 11 04: ALM4_L2 12 04: ALM5_L2 13 04: ALM6_L2 6 (Unused) 7 (Unused) 8 (Unused) 9 (Unused) 10 (Unused) 11 (Unused) 12 (Unused) 13 (Unused) 14 (Unused) 15 (Unused) 16 (Unused) 17 (Unused) 18 (Unused) 19 (Unused) 11 (Unused) 11 (Unused) 12 (Unused) 13 (Unused)			1	\dashv \mid			,
7 (Unused) 8 04: ALM1_L2 9 04: ALM2_L2 10 04: ALM3_L2 11 04: ALM4_L2 12 04: ALM5_L2 13 04: ALM6_L2 7 (Unused) 8 (Unused) 9 (Unused) 10 (Unused) 11 (Unused) 12 (Unused) 13 (Unused) 13 (Unused)				\dashv \mid			
8 04: ALM1_L2 8 (Unused) 9 04: ALM2_L2 9 (Unused) 10 04: ALM3_L2 10 (Unused) 11 04: ALM4_L2 11 (Unused) 12 04: ALM5_L2 12 (Unused) 13 04: ALM6_L2 13 (Unused)							,
10 04: ALM3_L2		8					
11 04: ALM4_L2 11 (Unused) 12 04: ALM5_L2 12 (Unused) 13 04: ALM6_L2 13 (Unused)			_	_			,
12 04: ALM5_L2 12 (Unused) 13 04: ALM6_L2 13 (Unused)							,
13 04: ALM6_L2 13 (Unused)			i	\dashv \mid			, ,
			_	+			` '
			_				/
15 04: ALM8_L2 15 (Unused)		15	_			15	, ,

OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/							
DeviceNet slave (UTAdvanced) Word Bit							
		Contents of assignment					
position	position	(1 lave a 4)					
49	0	(Unused)					
	1	03: R.L_L1					
	2	03: S.R					
	3	(Unused)					
	5	(Unused)					
	6	(Unused)					
	7	(Unused)					
	8	(Unused)					
	9	(Unused)					
	10	(Unused)					
	11	(Unused)					
	12	(Unused)					
	13	(Unused)					
	14	(Unused)					
	15	(Unused)					
50	0	(Unused)					
	1	(Unused)					
	2	(Unused)					
	3	(Unused)					
	4	(Unused)					
	5	(Unused)					
	6	(Unused)					
	7	(Unused)					
	8	(Unused)					
	9	(Unused)					
	10	(Unused)					
	11	(Unused)					
	12	(Unused)					
	13	(Unused)					
	14	(Unused)					
	15	(Unused)					
51	0	(Unused)					
	1	04: R.L_L1					
	2	04: S.R					
	3	(Unused)					
	4	(Unused)					
	5	(Unused)					
	6	(Unused)					
	7	(Unused)					
	8	(Unused)					
	9	(Unused)					
	10	(Unused)					
	11	(Unused)					
	12	(Unused)					
	13	(Unused)					
	14	(Unused)					
	15	(Unused)					
52	0	(Unused)					
	1	(Unused)					
	2	(Unused)					
	3	(Unused)					
	4	(Unused)					
	5	(Unused)					
	6	(Unused)					
	7	(Unused)					
	8	(Unused)					
	9	(Unused)					
	10	(Unused)					
	11	(Unused)					
	12	(Unused)					
	13	(Unused)					
	14	(Unused)					
	15	(Unused)					

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Pro	ofile nu	mber 5 (Cascade control with 5 c	nected controllers) on page 1						
		IN area		OUT area					
PF		-DP/DeviceNet slave (UTAdvanced) →		PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/					
		OFIBUS-DP/DeviceNet master			DeviceNet slave (UTAdvanced)				
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment			
	position				position	Contonic or doorgiment			
53	0	(Unused)		53	0	(Unused)			
	1	05: R.L_L1			1	05: R.L_L1			
	2	05: S.R			2	05: S.R			
	3	(Unused)			3	(Unused)			
	4	(Unused)			4	(Unused)			
	5	(Unused)			5	(Unused)			
	6	(Unused)			6	(Unused)			
	7	(Unused)			7	(Unused)			
	8	05: ALM1_L1			8	(Unused)			
	9	05: ALM2_L1			9	(Unused)			
	10	05: ALM3_L1			10	(Unused)			
	11	05: ALM4_L1			11	(Unused)			
	12	05: ALM5_L1			12	(Unused)			
	13	05: ALM6_L1			13	(Unused)			
	14	05: ALM7_L1			14	(Unused)			
	15	05: ALM8_L1			15	(Unused)			
54	0	(Unused)		54	0	(Unused)			
	1	(Unused)			1	(Unused)			
	2	(Unused)			2	(Unused)			
	3	(Unused)			3	(Unused)			
	4	(Unused)			4	(Unused)			
	5	(Unused)			5	(Unused)			
	6	(Unused)			6	(Unused)			
	7	(Unused)			7	(Unused)			
	8	05: ALM1_L2			8	(Unused)			
	9	05: ALM2_L2			9	(Unused)			
	10	05: ALM3_L2			10	(Unused)			
	11	05: ALM4_L2			11	(Unused)			
	12	05: ALM5_L2			12	(Unused)			
	13	05: ALM6_L2			13	(Unused)			
	14	05: ALM7_L2			14	(Unused)			
	15	05: ALM8_L2			15	(Unused)			

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Page 2

Pro	Profile number 5 (Cascade control with 5 connected controllers) on page 2								
		IN area		OUT area					
		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment			
0	0	Receive data valid		0	0	Rescan request			
	1	During-write			1	(Reserved)			
	2	Write acknowledgement	ļ		2	Write request			
	3	(Reserved)			3	(Reserved)			
	5	(Reserved)	1		5	(Reserved)			
	6	(Reserved)			6	(Reserved)			
	7	(Reserved)			7	(Reserved)			
		The fixed-part is omitted				The fixed-part is omitted			
		(See profile number 0 on page 1)		•		(See profile number 0 on page 1)			
4		Current page		4		Page change request			
5		01: P_L1_1		5		01: P_L1_1			
6		02: P_L1_1		6		02: P_L1_1			
7		03: P_L1_1		7		03: P_L1_1			
8		04: P_L1_1		8		04: P_L1_1			
9		05: P_L1_1		9		05: P_L1_1			
10		01: I_L1_1		10		01: I_L1_1			
11		02: I_L1_1		11		02: I_L1_1			
12		03: I_L1_1		12		03: I_L1_1			
13		04: I_L1_1		13		04: I_L1_1			
14		05: I_L1_1		14		05: I_L1_1			
15		01: D_L1_1 02: D_L1_1		15		01: D_L1_1 02: D_L1_1			
17		03: D_L1_1		17		03: D_L1_1			
18		04: D_L1_1		18		04: D_L1_1			
19		05: D_L1_1		19		05: D_L1_1			
20		01: SPNO.		20		01: SPNO.			
21		02: SPNO.		21		02: SPNO.			
22		03: SPNO.		22		03: SPNO.			
23		04: SPNO.		23		04: SPNO.			
24		05: SPNO.		24		05: SPNO.			
25		01: P_L2_1		25		01: P_L2_1			
26		02: P_L2_1		26		02: P_L2_1			
27		03: P_L2_1		27		03: P_L2_1			
28		04: P_L2_1		28		04: P_L2_1			
29		05: P_L2_1		29		05: P_L2_1			
30		01: I_L2_1		30		01: I_L2_1			
31		02: I_L2_1	-	31		02: I_L2_1			
32		03: I_L2_1 04: I_L2_1		32		03: I_L2_1 04: I_L2_1			
		UT. I_LZ_ 				U7. 1_LZ_ 			

Pro	Profile number 5 (Cascade control with 5 connected controllers) on page 2								
		IN area		OUT area					
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →				PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PR	OFIBUS-DP/DeviceNet master		DeviceNet slave (UTAdvanced)					
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment			
position	position	Contents of assignment		position	position	Contents of assignment			
34		05: I_L2_1		34		05: I_L2_1			
35		01: D_L2_1		35		01: D_L2_1			
36		02: D_L2_1		36		02: D_L2_1			
37		03: D_L2_1		37		03: D_L2_1			
38		04: D_L2_1		38		04: D_L2_1			
39		05: D_L2_1		39		05: D_L2_1			
40		(Unused)		40		(Unused)			
41		(Unused)		41		(Unused)			
42		(Unused)		42		(Unused)			
43		(Unused)		43		(Unused)			
44		(Unused)		44		(Unused)			
45		(Unused)		45		(Unused)			
46		(Unused)		46		(Unused)			
47		(Unused)		47		(Unused)			
48		(Unused)		48		(Unused)			
49		(Unused)		49		(Unused)			
50		(Unused)		50		(Unused)			
51		(Unused)		51		(Unused)			
52		(Unused)		52		(Unused)			
53		(Unused)		53		(Unused)			
54		(Unused)		54		(Unused)			

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Pro	ofile nu	mber 5 (Cascade control with 5 c	onnected	contro	ollers) on page 3				
	IN area				OUT area				
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment				
0	0	Receive data valid	0	0	Rescan request				
	1	During-write		1	(Reserved)				
	3	Write acknowledgement (Reserved)		3	Write request (Reserved)				
	4	(Reserved)		4	(Reserved)				
	5	(Reserved)		5	(Reserved)				
	6	(Reserved)		6	(Reserved)				
	7	(Reserved)		7	(Reserved)				
•	•	The fixed-part is omitted	•		The fixed-part is omitted				
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)				
•	•	(cooprome name of on page 1)	•	•	(coopiems mannes o en page 1)				
4		Current page	4		Page change request				
5		(Unused)	5		(Unused)				
6		(Unused)	6		(Unused)				
8		(Unused)	8		(Unused)				
9		(Unused)	9		(Unused)				
10		(Unused)	10		(Unused)				
11		(Unused)	11		(Unused)				
12		(Unused)	12		(Unused)				
13		(Unused)	13	-	(Unused)				
14 15		(Unused)	14		(Unused)				
16		(Unused)	16		(Unused)				
17		(Unused)	17		(Unused)				
18		(Unused)	18		(Unused)				
19		(Unused)	19		(Unused)				
20		01: SPNO. 02: SPNO.	20		01: SPNO. 02: SPNO.				
22		02: SPNO.	21		03: SPNO.				
23		04: SPNO.	23		04: SPNO.				
24		05: SPNO.	24		05: SPNO.				
25		01: Pc_L2_1	25		01: Pc_L2_1				
26		02: Pc_L2_1			02: Pc_L2_1				
27		03: Pc_L2_1	26		03: Pc_L2_1				
28		04: Pc_L2_1	27		04: Pc_L2_1				
29		05: Pc_L2_1	28		05: Pc_L2_1				
30		01: lc_L2_1	30		01: lc_L2_1				
31		02: lc_L2_1	31		02: lc_L2_1				
32		03: lc_L2_1	32		03: lc_L2_1				
33		04: lc_L2_1	33		04: lc_L2_1				
34		05: lc_L2_1	34		05: lc_L2_1				
35		01: Dc_L2_1	35		01: Dc_L2_1				
36		02: Dc_L2_1	36		02: Dc_L2_1				
37		03: Dc_L2_1	37		03: Dc_L2_1				
38		04: Dc_L2_1	38		04: Dc_L2_1				
39		05: Dc_L2_1	39		05: Dc_L2_1				

Pro	Profile number 5 (Cascade control with 5 connected controllers) on page 3							
PF	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word Bit Contents of assignment				
40		(Unused)		40		(Unused)		
41		(Unused)		41		(Unused)		
42		(Unused)		42		(Unused)		
43		(Unused)		43		(Unused)		
44		(Unused)		44		(Unused)		
45		(Unused)		45		(Unused)		
46		(Unused)		46		(Unused)		
47		(Unused)		47		(Unused)		
48		(Unused)		48		(Unused)		
49		(Unused)		49		(Unused)		
50		(Unused)		50		(Unused)		
51		(Unused)		51		(Unused)		
52		(Unused)		52		(Unused)		
53		(Unused)		53		(Unused)		
54		(Unused)		54		(Unused)		

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Pro	Profile number 5 (Cascade control with 5 connected controllers) on page 4						
		IN area			OUT area		
PF	PR	-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	2	During-write Write acknowledgement		2	(Reserved) Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	7	(Reserved)		7	(Reserved)		
	,	(Neserveu)			(Incocived)		
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted		
		(See profile number 0 on page 1)			(See profile number 0 on page 1)		
		Comment of the comment	4		Dana sharrar sanart		
4		Current page	4		Page change request		
5		01: A1_L1_1	5		01: A1_L1_1		
6		02: A1_L1_1	6		02: A1_L1_1		
8		03: A1_L1_1 04: A1_L1_1	8		03: A1_L1_1 04: A1_L1_1		
9		05: A1_L1_1	9		05: A1_L1_1		
10		01: A2 L1 1	10		01: A2_L1_1		
11		02: A2_L1_1	11		02: A2_L1_1		
12		03: A2_L1_1	12		03: A2_L1_1		
13		04: A2_L1_1	13		04: A2_L1_1		
14		05: A2_L1_1	14		05: A2_L1_1		
15		01: A3_L1_1	15		01: A3_L1_1		
16		02: A3_L1_1	16		02: A3_L1_1		
17		03: A3_L1_1	17		03: A3_L1_1		
18		04: A3_L1_1	18		04: A3_L1_1		
19		05: A3_L1_1	19		05: A3_L1_1		
20		01: A4_L1_1	20		01: A4_L1_1		
21		02: A4_L1_1	21		02: A4_L1_1		
22		03: A4_L1_1	22		03: A4_L1_1		
23		04: A4_L1_1	23		04: A4_L1_1		
24		05: A4_L1_1	24		05: A4_L1_1		
25		01: A5_L1_1	25		01: A5_L1_1		
26		02: A5_L1_1	26		02: A5_L1_1		
27		03: A5_L1_1	27		03: A5_L1_1		
28		04: A5_L1_1	28		04: A5_L1_1		
29		05: A5_L1_1	29		05: A5_L1_1		
30		01: A1_L2_1	30		01: A1_L2_1		
31		02: A1_L2_1	31		02: A1_L2_1		
32		03: A1_L2_1	32		03: A1_L2_1		
33		04: A1_L2_1	33		04: A1_L2_1		
34		05: A1_L2_1	34		05: A1_L2_1		

Pro	Profile number 5 (Cascade control with 5 connected controllers) on page 4								
	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)				
Word osition	Bit position	Contents of assignment		1	Word Bit Contents of assig				
35		01: A2_L2_1		35		01: A2_L2_1			
36		02: A2_L2_1		36		02: A2_L2_1			
37		03: A2_L2_1	1	37		03: A2_L2_1			
38		04: A2_L2_1	1	38		04: A2_L2_1			
39		05: A2_L2_1	1	39		05: A2_L2_1			
40		01: A3_L2_1	1	40		01: A3_L2_1			
41		02: A3_L2_1	1	41		02: A3_L2_1			
42		03: A3_L2_1	1	42		03: A3_L2_1			
43		04: A3_L2_1	1	43		04: A3_L2_1			
44		05: A3_L2_1	1	44		05: A3_L2_1			
45		01: A4_L2_1	1	45		01: A4_L2_1			
46		02: A4_L2_1	1	46		02: A4_L2_1			
47		03: A4_L2_1	1	47		03: A4_L2_1			
48		04: A4_L2_1	1	48		04: A4_L2_1			
49		05: A4_L2_1	1	49		05: A4_L2_1			
50		01: A5_L2_1	1	50		01: A5_L2_1			
51		02: A5_L2_1	1	51		02: A5_L2_1			
52		03: A5_L2_1	1	52		03: A5_L2_1			
53		04: A5_L2_1	1	53		04: A5_L2_1			
54		05: A5_L2_1	1	54		05: A5_L2_1			

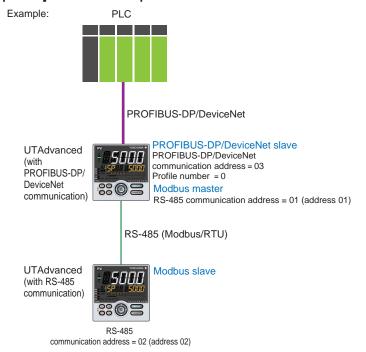
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3.9.2 Profile List for UP55A/UP35A



Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers])



Page 1

Profi	le numi	ber 0 (User profile [initial value: simp	le P	ID cont	rol with	2 connected controllers]) on page 1		
		IN area		OUT area				
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO	PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word Bit Contents of assignment				
0	0	Receive data valid		0	0	Rescan request		
	1	During-write	1		1	(Reserved)		
	2	Write acknowledgement			2	Write request		
	3	(Reserved)	1		3	(Reserved)		
	4	(Reserved)			4	(Reserved)		
	5	(Reserved)			5	(Reserved)		
	6	(Reserved)			6	(Reserved)		
	7	(Reserved)			7	(Reserved)		
	8	(Reserved)			8	(Reserved)		
	9	(Reserved)			9	(Reserved)		
	10	(Reserved)			10	(Reserved)		
	11	(Reserved)			11	(Reserved)		
	12	(Reserved)	1		12	(Reserved)		
	13	(Reserved)]		13	(Reserved)		
	14	(Reserved)			14	(Reserved)		
	15	(Reserved)			15	(Reserved)		
1	0	Normal connection slave (address 01)]	1	0	Batch write request (address 01)		
	1	Normal connection slave (address 02)			1	Batch write request (address 02)		
	2	Normal connection slave (address 03)			2	Batch write request (address 03)		
	3	Normal connection slave (address 04)			3	Batch write request (address 04)		
	4	Normal connection slave (address 05)			4	Batch write request (address 05)		
	5	Normal connection slave (address 06)			5	Batch write request (address 06)		
	6	Normal connection slave (address 07)			6	Batch write request (address 07)		
	7	Normal connection slave (address 08)			7	Batch write request (address 08)		
	8	Normal connection slave (address 09)			8	Batch write request (address 09)		
	9	Normal connection slave (address 10)			9	Batch write request (address 10)		
	10	Normal connection slave (address 11)			10	Batch write request (address 11)		
	11	Normal connection slave (address 12)			11	Batch write request (address 12)		
	12	Normal connection slave (address 13)			12	Batch write request (address 13)		
	13	Normal connection slave (address 14)			13	Batch write request (address 14)		
	14	Normal connection slave (address 15)			14	Batch write request (address 15)		
	15	Normal connection slave (address 16)			15	Batch write request (address 16)		

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		IN area			2 connected controllers]) on page OUT area
	PR	-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		De	DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
2	0	Normal connection slave (address 17)	2	0	Batch write request (address 17)
	1	Normal connection slave (address 18)		1	Batch write request (address 18)
	2	Normal connection slave (address 19)		2	Batch write request (address 19)
	3	Normal connection slave (address 20)		3	Batch write request (address 20)
	4	Normal connection slave (address 21)		4	Batch write request (address 21)
	5	Normal connection slave (address 22)		5	Batch write request (address 22)
	6	Normal connection slave (address 23)		6	Batch write request (address 23)
	7	Normal connection slave (address 24)		7	Batch write request (address 24)
	8	Normal connection slave (address 25)		8	Batch write request (address 25)
	9	Normal connection slave (address 26)		9	Batch write request (address 26)
	10	Normal connection slave (address 27)		10	Batch write request (address 27)
	11	Normal connection slave (address 28)		11	Batch write request (address 28)
	12	Normal connection slave (address 29)		12	Batch write request (address 29)
	13	Normal connection slave (address 30)		13	Batch write request (address 30)
	14	Normal connection slave (address 31)		14	Batch write request (address 31)
	15	Normal connection slave (address 32)		15	Batch write request (address 32)
3		Current profile number	3		(Unused)
4		Current page	4		Page change request
5		01: PV_L1	5		01: H.TSP_L1
6		01: CSP_L1	6		01: H.SP_L1
7		01: SEG_RTIME	7		01: H.TM_L1
8		02: PV_L1	8		02: H.TSP_L1
9		02: CSP_L1	9		02: H.SP_L1
10		02: SEG_RTIME	10		02: H.TM_L1
11	0	01: RST_ON	11	0	01: RST_ON
	1	01: PRG_ON		1	01: PRG_ON
	2	01: LOC_ON		2	01: LOC_ON
	3	01: HOLD]	3	01: HOLD
	4	(Unused)]	4	01: ADV
	5	01: A.M_L1]	5	01: A.M_L1
	6	01: PV_EV1]	6	(Unused)
	7	01: PV_EV2]	7	(Unused)
	8	01: TIME_EV1]	8	(Unused)
	9	01: TIME_EV2]	9	(Unused)
	10	01: TIME_EV3]	10	(Unused)
	11	01: TIME_EV4]	11	(Unused)
	12	01: TIME_EV5		12	(Unused)
	13	01: TIME_EV6		13	(Unused)
	14	01: TIME_EV7		14	(Unused)
	15	01: TIME_EV8		15	(Unused)
12	0	02: RST_ON	12	0	02: RST_ON
	1	02: PRG_ON		1	02: PRG_ON
	2	02: LOC_ON		2	02: LOC_ON
	3	02: HOLD		3	02: HOLD
	4	(Unused)		4	02: ADV
	5	02: A.M_L1]	5	02: A.M_L1
	6	02: PV_EV1		6	(Unused)
	7	02: PV_EV2		7	(Unused)
	8	02: TIME_EV1		8	(Unused)
	9	02: TIME_EV2		9	(Unused)
	10	02: TIME_EV3]	10	(Unused)
	11	02: TIME_EV4		11	(Unused)
	12	02: TIME_EV5		12	(Unused)
	13	02: TIME_EV6 VP35A: unused		13	(Unused)
	14	02: TIME_EV7		14	(Unused)
	15	02: TIME_EV8	1 1	15	(Unused)

Profi	ile numl	ber 0 (User profile [initial value: simpl	e PID conf	PID control with 2 connected controllers]) on page 2				
		IN area		OUT area				
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR	PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Word Bit Contents of assignment				
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: P_L1_1	5		01: P_L1_1			
6		01: I_L1_1	6		01: I_L1_1			
7		01: D_L1_1	7		01: D_L1_1			
8		01: L.PID	8		01: L.PID			
9		01: C.PTNO.	9		01: PTNO.			
10		01: SEG.N	10		01: SST			
11		(Unused)	11		(Unused)			
12		(Unused)	12		(Unused)			

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PR	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Word Bit Contents of assignment			
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		02: P_L1_1	5		02: P_L1_1		
6		02: I_L1_1	6		02: I_L1_1		
7		02: D_L1_1	7		02: D_L1_1		
8		02: L.PID	8		02: L.PID		
9		02: C.PTNO.	9		02: PTNO.		
10		02: SEG.N	10		02: SST		
11		(Unused)	11		(Unused)		
12		(Unused)	12		(Unused)		

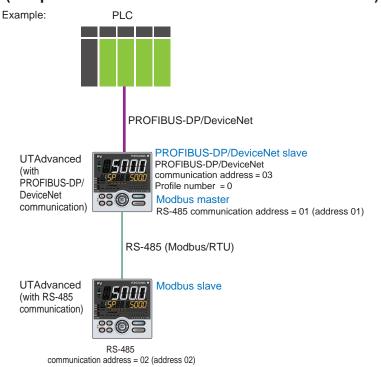
Profi	ile numl	ber 0 (User profile [initial value: simp	le PID cont	rol with	2 connected controllers]) on page 4			
		IN area		OUT area				
		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Word Bit contents of assignment				
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: L.TY1	5		01: L.TY1			
6		01: L.EV1	6		01: L.EV1			
7		01: L.TY2	7		01: L.TY2			
8		01: L.EV2	8		01: L.EV2			
9		02: L.TY1	9		02: L.TY1			
10		02: L.EV1	10		02: L.EV1			
11		02: L.TY2	11		02: L.TY2			
12		02: L.EV2	12		02: L.EV2			

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Profile number 11 (Simple PID control with 2 connected controllers)





Page 1

Pro	ofile nu	mber 11 (Simple PID control with	2 co	nnec	ted co		
		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master				OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word osition	Bit position	Contents of assignment	
0	0	Receive data valid		0	0	Rescan request	
	1	During-write			1	(Reserved)	
	2	Write acknowledgement			2	Write request	
	3	(Reserved)			3	(Reserved)	
	4	(Reserved)			4	(Reserved)	
	5	(Reserved)			5	(Reserved)	
	6	(Reserved)			6	(Reserved)	
	7	(Reserved)			7	(Reserved)	
•	•	The fixed-part is omitted (See profile number 0 on page 1)		•	•	The fixed-part is omitted (See profile number 0 on page 1)	
4		Current page		4		Page change request	
5		01: PV_L1		5		01: H.TSP_L1	
6		01: CSP_L1		6		01: H.SP_L1	
7		01: SEG_RTIME		7		01: H.TM_L1	
8		01: LSP_L1		8		01: LSP_L1	
9		01: OUT_L1		9		01: MOUT_L1	
10		01: C.PTNO.		10		01: PTNO.	
11		01: SEG.N		11		01: SST	
12		(Unused)		12		(Unused)	
13		02: PV_L1		13		02: H.TSP_L1	
14		02: CSP_L1		14		02: H.SP_L1	
15		02: SEG_RTIME		15		02: H.TM_L1	

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Word		IN area					A1100		
Word		DD/D - 1 - Not als			OUT area				
	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →				PRO	FIBUS-E	DP/DeviceNet master → PROFIBUS-DP/		
	PROFIBUS-DP/DeviceNet master					De	eviceNet slave (UTAdvanced)		
	Bit	Contents	of assignment		Word	Bit	Contents of assignment		
position	position		o or assignment		position	position			
16		02: LSP_L1			16		02: LSP_L1		
17		02: OUT_L1			17		02: MOUT_L1		
18		02: C.PTNO.			18		02: PTNO.		
19		02: SEG.N			19		02: SST		
20	0	01: RST_ON			20	0	01: RST_ON		
	1	01: PRG_ON				1	01: PRG_ON		
	2	01: LOC_ON				2	01: LOC_ON		
	3	01: HOLD				3	01: HOLD		
	4	(Unused)				4	01: ADV		
	5	01: A.M_L1				5	01: A.M_L1		
	6	01: ALM1_L1				6	(Unused)		
	7	01: ALM2_L1				7	(Unused)		
	8	01: PV EV1				8	(Unused)		
	9	01: PV_EV2				9	(Unused)		
	10	01: PV_EV3				10	(Unused)		
	11	01: PV EV4				11	(Unused)		
	12	01: PV_EV5	LIDSEALunused			12	(Unused)		
	13	01: PV_EV6	UP35A: unused			13	(Unused)		
	14	01: PV EV7				14	(Unused)		
	15	01: PV_EV8				15	(Unused)		
21	0	01: TIME EV1	<u>, </u>		21	0	(Unused)		
	1	01: TIME EV2				1	(Unused)		
	2	01: TIME_EV3				2	(Unused)		
	3	01: TIME EV4				3	(Unused)		
	4	01: TIME EV5)			4	(Unused)		
	5	01: TIME_EV6				5	(Unused)		
	6	01: TIME_EV7				6	(Unused)		
	7	01: TIME_EV8				7	(Unused)		
	8	01: TIME_EV9				8	(Unused)		
	9	01: TIME_EV10	LIDOSA		9	(Unused)			
	10	01: TIME_EV11	UP35A: unused			10	(Unused)		
	11	01: TIME_EV12				11	(Unused)		
	12	01: TIME_EV13				12	(Unused)		
	13	01: TIME_EV14				13	(Unused)		
	14	01: TIME_EV15				14	(Unused)		
	15	01: TIME_EV16				15	(Unused)		
22	0	02: RST_ON			22	0	02: RST_ON		
	1	02: PRG_ON				1	02: PRG_ON		
	2	02: LOC_ON				2	02: LOC_ON		
	3	02: HOLD				3	02: HOLD		
	4	(Unused)				4	02: ADV		
	5	02: A.M_L1				5	02: A.M_L1		
	6	02: ALM1_L1				6	(Unused)		
[7	02: ALM2_L1				7	(Unused)		
	8	02: PV_EV1				8	(Unused)		
	9	02: PV_EV2				9	(Unused)		
L		02: PV_EV3				10	(Unused)		
	11	02: PV_EV4				11	(Unused)		
	12	02: PV_EV5	·			12	(Unused)		
L	13	02: PV_EV6				13	(Unused)		
	14	02: PV_EV7				14	(Unused)		
	15	02: PV_EV8	J			15	(Unused)		

Pro	ofile nu	mber 11 (Sim	ple PID control with	2 (connected controllers) on page 1			
		IN area			OUT area			
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →				PRO	FIBUS-E	DP/DeviceNet master → PROFIBUS-DP/	
	PR	OFIBUS-DP/Device	Net master			De	eviceNet slave (UTAdvanced)	
Word	Bit	Contents of assignment			Word	Bit	Contents of assignment	
position	position	Contents	o or assignment		position	position	Contents of assignment	
23	0	02: TIME_EV1			21	0	(Unused)	
	1	02: TIME_EV2				1	(Unused)	
	2	02: TIME_EV3				2	(Unused)	
	3	02: TIME_EV4				3	(Unused)	
	4	02: TIME_EV5)			4	(Unused)	
	5	02: TIME_EV6				5	(Unused)	
	6	02: TIME_EV7				6	(Unused)	
	7	02: TIME_EV8				7	(Unused)	
	8	02: TIME_EV9				8	(Unused)	
	9	02: TIME_EV10	UP35A: unused			9	(Unused)	
	10	02: TIME_EV11	OFSSA. unuseu			10	(Unused)	
	11	02: TIME_EV12				11	(Unused)	
	12	02: TIME_EV13				12	(Unused)	
	13	02: TIME_EV14				13	(Unused)	
	14	02: TIME_EV15				14	(Unused)	
	15	02: TIME_EV16	J			15	(Unused)	

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Pro	ofile nu	mber 11 (Simple PID control with 2	connec	ted co	ntrollers) on page 2	
		IN area			OUT area	
PR		-DP/DeviceNet slave (UTAdvanced) →	PRO		DP/DeviceNet master → PROFIBUS-DP/	
Word	Bit	OFIBUS-DP/DeviceNet master	Word	Bit	eviceNet slave (UTAdvanced)	
	position	Contents of assignment		position	Contents of assignment	
0	0	Receive data valid	0	0	Rescan request	
	1	During-write		1	(Reserved)	
	2	Write acknowledgement		2	Write request	
	3	(Reserved)		3	(Reserved)	
	4	(Reserved)		4	(Reserved)	
	5	(Reserved)		5	(Reserved)	
	6	(Reserved)		6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted	
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)	
4		Current page	4		Page change request	
5		01: P_L1_1	5		01: P_L1_1	
6		01: I_L1_1	6		01: I_L1_1	
7		01: D_L1_1	7		01: D_L1_1	
8		01: L.PID	8		01: L.PID	
9		01: A1_L1_1	9		01: A1_L1_1	
10		01: A2_L1_1	10		01: A2_L1_1	
11		01: A3_L1_1	11		01: A3_L1_1	
12		(Unused)	12		(Unused)	
13		02: P_L1_1	13		02: P_L1_1	
14		02: I_L1_1	14		02: I_L1_1	
15		02: D_L1_1	15		02: D_L1_1	
16		02: L.PID	16		02: L.PID	
17		02: A1_L1_1	17		02: A1_L1_1	
18		02: A2_L1_1	18		02: A2_L1_1	
19		02: A3_L1_1	19		02: A3_L1_1	
20		(Unused)	20		(Unused)	
21		(Unused)	21	-	(Unused)	
22		(Unused)	22	L	(Unused)	

Page 3

	PR	IN area -DP/DeviceNet slave (U OFIBUS-DP/DeviceNet i	,		OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of a	ssignment	Word position	Bit position	Contents	of assignment	
0	0	Receive data valid		0	0	Rescan request		
	1	During-write			1	(Reserved)		
	2	Write acknowledgement	t		2	Write request		
	3	(Reserved)			3	(Reserved)		
	4	(Reserved)			4	(Reserved)		
	5	(Reserved)			5	(Reserved)		
	6	(Reserved)			6	(Reserved)		
	7	(Reserved)			7	(Reserved)		
•	•	The fixed-par (See profile numb		•	•		part is omitted imber 0 on page 1)	
4		Current page		4		Page change request		
5		01: L.TY1		5		01: L.TY1		
6		01: L.EV1		6		01: L.EV1		
7		01: L.TY2		7		01: L.TY2		
8		01: L.EV2		8		01: L.EV2		
9		01: L.TY3		9		01: L.TY3		
10		01: L.EV3		10		01: L.EV3	_	
11		01: L.TY4		11		01: L.TY4	_	
12		01: L.EV4		12		01: L.EV4	_	
13		01: L.TY5	≻UP35A: unused	13		01: L.TY5	LIDSEALURUS	
14		01: L.EV5.	OF35A: unused	14		01: L.EV5.	— }UP35A: unuse∈	
15		01: L.TY6		15		01: L.TY6		
16		01: L.EV6		16		01: L.EV6		
17		01: L.TY7		17		01: L.TY7		
18		01: L.EV7		18		01: L.EV7		
19		(Unused)		19		(Unused)		
20		(Unused)		20		(Unused)		
21		(Unused) (Unused)		21		(Unused)		

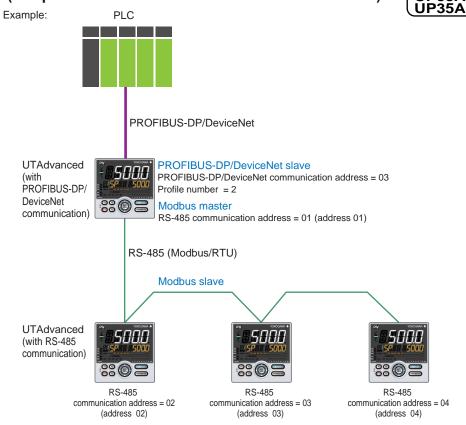
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Page 4

	PR	IN area -DP/DeviceNet slave (U ⁻ DFIBUS-DP/DeviceNet r			De	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of	assignment		
0	0	Receive data valid		0	0	Rescan request			
	1	During-write			1	(Reserved)			
	2	Write acknowledgement			2	Write request			
	3	(Reserved)			3	(Reserved)			
	4	(Reserved)			4	(Reserved)			
	5	(Reserved)			5	(Reserved)			
	6	(Reserved)			6	(Reserved)			
	7	(Reserved)			7	(Reserved)			
		The fixed-part is omitted (See profile number 0 on page 1)		•	•	The fixed-pa (See profile num			
4		Current page		4		Page change request			
5		02: L.TY1		5		02: L.TY1			
6		02: L.EV1		6		02: L.EV1			
7		02: L.TY2		7		02: L.TY2			
8		02: L.EV2		8		02: L.EV2			
9		02: L.TY3		9		02: L.TY3			
10		02: L.EV3		10		02: L.EV3			
11		02: L.TY4		11		02: L.TY4			
12		02: L.EV4		12		02: L.EV4			
13		02: L.TY5	≻UP35A: unused	13		02: L.TY5	≻UP35A: unused		
14		02: L.EV5.	Or Joh. ulluseu	14		02: L.EV5.	or John. unuseu		
15		02: L.TY6		15		02: L.TY6			
16		02: L.EV6		16		02: L.EV6			
17		02: L.TY7		17		02: L.TY7			
18		02: L.EV7		18		02: L.EV7	J		
19		(Unused)		19		(Unused)			
20		(Unused)		20		(Unused)			
21		(Unused)		21		(Unused)			
22		(Unused)		22		(Unused)			

Profile number 12 (Simple PID control with 4 connected controllers)





Page 1

Pro	ofile nu	mber 12 (Simple PID control with	4 c	onnec	ted co	ntrollers) on page 1		
		IN area		OUT area				
PF		-DP/DeviceNet slave (UTAdvanced) →		PRO		DP/DeviceNet master → PROFIBUS-DP/		
		OFIBUS-DP/DeviceNet master	-	DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
0	0	Receive data valid		0	0	Rescan request		
	1	During-write			1	(Reserved)		
	2	Write acknowledgement			2	Write request		
	3	(Reserved)			3	(Reserved)		
	4	(Reserved)			4	(Reserved)		
	5	(Reserved)			5	(Reserved)		
	6	(Reserved)			6	(Reserved)		
	7	(Reserved)	L		7	(Reserved)		
•	•	The fixed-part is omitted (See profile number 0 on page 1)		•	•	The fixed-part is omitted (See profile number 0 on page 1)		
4		Current page		4		Page change request		
5		01: PV_L1		5		01: H.TSP_L1		
6		01: CSP_L1		6		01: H.SP_L1		
7		01: SEG_RTIME	Ī	7		01: H.TM_L1		
8		01: LSP_L1	Ī	8		01: LSP_L1		
9		01: OUT_L1		9		01: MOUT_L1		
10		01: C.PTNO.	Ī	10		01: PTNO.		
11		01: SEG.N	Ī	11		01: SST		
12		(Unused)		12		(Unused)		
13		02: PV_L1		13		02: H.TSP_L1		

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Pro	ofile nu	ımber 12 (Sim	ple PID control witl	ո 4 (connec	ted co	ntrollers) on page 1
PF			/e (UTAdvanced) →		PRO		OUT area DP/DeviceNet master → P
Word	Bit	OFIBUS-DP/Device	Net master s of assignment		Word	Bit	eviceNet slave (UTAdvanc
position 14	position	02: CSP_L1		-	position 14	position	02: H.SP_L1
15		02: SEG_RTIME		-	15		02: H.TM_L1
16		02: LSP_L1		1	16		02: LSP_L1
17		02: OUT_L1		-	17		02: MOUT_L1
18		02: C.PTNO.		-	18		02: PTNO.
19		02: SEG.N		1	19		02: SST
20		(Unused)			20		(Unused)
21		03: PV L1		-	21		03: H.TSP_L1
22		03: CSP_L1		1	22		03: H.SP_L1
23		03: SEG RTIME		-	23		03: H.TM_L1
24		03: LSP_L1		-	24		03: LSP_L1
25		03: OUT_L1		-	25		03: MOUT_L1
26		03: C.PTNO.		-	26		03: PTNO.
				-			
27		03: SEG.N		-	27		03: SST
28		(Unused)		1	28		(Unused)
29		04: PV_L1		-	29		04: H.TSP_L1
30		04: CSP_L1		-	30		04: H.SP_L1
31		04: SEG_RTIME		-	31		04: H.TM_L1
32		04: LSP_L1		-	32		04: LSP_L1
33		04: OUT_L1			33		04: MOUT_L1
34		04: C.PTNO.		-	34		04: PTNO.
35		04: SEG.N			35		04: SST
36	1	01: RST_ON 01: PRG_ON		-	36	1	01: RST_ON 01: PRG_ON
	2	01: LOC_ON		1		2	01: LOC_ON
	3	01: HOLD				3	01: HOLD
	4 5	(Unused) 01: A.M L1		-		4 5	01: ADV 01: A.M_L1
	6	01: ALM1_L1		1		6	(Unused)
	7	01: ALM2_L1				7	(Unused)
	8	01: PV_EV1		-		8	(Unused)
	9	01: PV_EV2 01: PV_EV3)	-		9	(Unused)
	11	01: PV_EV4				11	(Unused)
	12	01: PV_EV5	LIDSEAL unused			12	(Unused)
	13	01: PV_EV6	UP35A: unused			13	(Unused)
	14	01: PV_EV7				14	(Unused)
	15	01: PV_EV8	J			15	(Unused)
37	0	01: TIME_EV1		-	37	0	(Unused)
	2	01: TIME_EV2 01: TIME_EV3		-		2	(Unused)
	3	01: TIME EV4		1		3	(Unused)
	4	01: TIME_EV5)	1		4	(Unused)
	5	01: TIME EV6				5	(Unused)
	6	01: TIME_EV7				6	(Unused)
	7	01: TIME_EV8				7	(Unused)
	8	01: TIME_EV9				8	(Unused)
	9	01: TIME_EV10	UP35A: unused			9	(Unused)
	10	01: TIME_EV11	2. 55 4.14554			10	(Unused)
	11	01: TIME_EV12				11	(Unused)
	12 13	01: TIME_EV13 01: TIME_EV14				12 13	(Unused)
	14	01: TIME_EV14				14	(Unused)
	15	01: TIME_EV15				15	(Unused)
	1 10	V IIVIL_L V IO .	,	1	I	1 10	1(0114304)

DD(OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/						
PRO		eviceNet slave (UTAdvanced)					
Word	Bit	Contents of assignment					
position 14	position	02: H.SP L1					
15		02: H.TM L1					
16		02: LSP_L1					
17		02: MOUT_L1					
18		02: PTNO.					
19		02: SST					
20		(Unused)					
21		03: H.TSP_L1					
22		03: H.SP_L1					
23		03: H.TM_L1					
24		03: LSP_L1					
25		03: MOUT_L1					
26		03: PTNO.					
27		03: SST					
28		(Unused)					
29		04: H.TSP_L1					
30		04: H.SP_L1					
31		04: H.TM_L1					
32		04: LSP_L1					
33		04: MOUT_L1					
34		04: PTNO.					
35		04: SST					
36	0	01: RST_ON					
	1	01: PRG_ON					
	2	01: LOC_ON					
	3 4	01: HOLD 01: ADV					
	5	01: A.M_L1					
	6	(Unused)					
	7	(Unused)					
	8 9	(Unused)					
	10	(Unused)					
	11	(Unused)					
	12 13	(Unused)					
	14	(Unused)					
	15	(Unused)					
37	1	(Unused)					
	2	(Unused)					
	3	(Unused)					
	<u>4</u> 5	(Unused)					
	6	(Unused)					
	7	(Unused)					
	8	(Unused)					
	9	(Unused)					
	11	(Unused)					
	12	(Unused)					
	13 14	(Unused)					
L	15	(Unused)					

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Pro	ofile nu		ple PID control with	4 connec	ted co	ntrollers) on page 1
PR		IN area -DP/DeviceNet slav OFIBUS-DP/Device	ve (UTAdvanced) → Net master	PR		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word position	Bit position	Contents	s of assignment	Word position	Bit position	Contents of assignment
38	0	02: RST_ON		38	0	02: RST_ON
	1	02: PRG_ON			1	02: PRG_ON
	2	02: LOC_ON			2	02: LOC_ON
	3	02: HOLD			3	02: HOLD
	4	(Unused)			4	02: ADV
	5	02: A.M_L1			5	02: A.M_L1
	6	02: ALM1_L1			6	(Unused)
	7	02: ALM2_L1			7	(Unused)
	8	02: PV EV1		İ	8	(Unused)
	9	02: PV EV2		İ	9	(Unused)
	10	02: PV EV3)		10	(Unused)
	11	02: PV_EV4			11	(Unused)
	12	02: PV_EV5	LIDOSA		12	(Unused)
	13	02: PV_EV6	UP35A: unused		13	(Unused)
	14	02: PV_EV7			14	(Unused)
	15	02: PV EV8			15	(Unused)
39	0	02: TIME EV1	/	39	0	(Unused)
00	1	02: TIME_EV2			1	(Unused)
	2	02: TIME_EV3			2	(Unused)
	3	02: TIME_EV4			3	(Unused)
	4	02: TIME_EV5	`		4	(Unused)
	5	02: TIME_EV6			5	(Unused)
	6	02: TIME_EV7			6	(Unused)
						(Unused)
	7 8	02: TIME_EV8			7	(Unused)
	9	02: TIME_EV9			9	(Unused)
	10	02: TIME_EV10	UP35A: unused			,
		02: TIME_EV11			10	(Unused)
	11	02: TIME_EV12			11	(Unused)
	12	02: TIME_EV13			12	(Unused)
	13	02: TIME_EV14			13	(Unused)
	14	02: TIME_EV15			14	(Unused)
40	15	02: TIME_EV16)	40	15	(Unused)
40	0	03: RST_ON		40	0	03: RST_ON
	1	03: PRG_ON		-	1	03: PRG_ON
	2	03: LOC_ON		\dashv	2	03: LOC_ON
	3	03: HOLD		_	3	03: HOLD
	4	(Unused)			4	03: ADV
	5	03: A.M_L1			5	03: A.M_L1
	6	03: ALM1_L1			6	(Unused)
	7	03: ALM2_L1			7	(Unused)
	8	03: PV_EV1			8	(Unused)
	9	03: PV_EV2			9	(Unused)
	10	03: PV_EV3]		10	(Unused)
	11	03: PV_EV4			11	(Unused)
	12	03: PV_EV5	UP35A: unused		12	(Unused)
	13	03: PV_EV6			13	(Unused)
	14	03: PV_EV7			14	(Unused)
	15	03: PV_EV8	J		15	(Unused)

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Pro	ofile nu	mber 12 (Sim	ple PID control with	4 connec	connected controllers) on page 1			
		IN area			OUT area			
PR		-DP/DeviceNet slav OFIBUS-DP/Device	ve (UTAdvanced) → Net master	PR		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word	Bit position	Contents	s of assignment	Word	Bit position	Contents of assignment		
41	0	03: TIME EV1		41	0	(Unused)		
	1	03: TIME EV2			1	(Unused)		
	2	03: TIME EV3			2	(Unused)		
	3	03: TIME EV4			3	(Unused)		
		03: TIME_EV5)		4	(Unused)		
	5	03: TIME_EV6			5	(Unused)		
	6	03: TIME EV7			6	(Unused)		
	7	03: TIME_EV8			7	(Unused)		
	8	03: TIME EV9			8	(Unused)		
	9	03: TIME_EV10			9	(Unused)		
	10	03: TIME EV11	UP35A: unused		10	(Unused)		
	11	03: TIME_EV12			11	(Unused)		
	12	03: TIME_EV13			12	(Unused)		
	13	03: TIME_EV14			13	(Unused)		
	14	03: TIME_EV15			14	(Unused)		
	15	03: TIME_EV16			15	(Unused)		
42	0	04: RST_ON		42	0	04: RST_ON		
	1	04: PRG_ON			1	04: PRG_ON		
	2	04: LOC_ON			2	04: LOC_ON		
	3	04: HOLD			3	04: HOLD		
	4	(Unused)			4	04: ADV		
	5	04: A.M_L1			5	04: A.M_L1		
	6	04: ALM1_L1			6	(Unused)		
	7	04: ALM2_L1			7	(Unused)		
	8	04: PV_EV1			8	(Unused)		
	9	04: PV_EV2			9	(Unused)		
	10	04: PV_EV3			10	(Unused)		
	11	04: PV_EV4			11	(Unused)		
	12	04: PV_EV5	UP35A: unused		12	(Unused)		
	13	04: PV_EV6			13	(Unused)		
	14	04: PV_EV7			14	(Unused)		
	15	04: PV_EV8	J		15	(Unused)		
43	0	04: TIME_EV1		43	0	(Unused)		
	1	04: TIME_EV2			1	(Unused)		
	2	04: TIME_EV3			2	(Unused)		
	3	04: TIME_EV4			3	(Unused)		
	4	04: TIME_EV5			4	(Unused)		
	5 6	04: TIME_EV6			5 6	(Unused)		
		04: TIME_EV7 04: TIME_EV8				(Unused)		
	8	04: TIME_EV8			8	(Unused)		
	9	04: TIME_EV9			9	(Unused)		
	10	04: TIME_EV10	UP35A: unused		10	(Unused)		
	11	04: TIME_EV11			11	(Unused)		
	12	04: TIME_EV12			12	(Unused)		
	13	04: TIME_EV13			13	(Unused)		
	14	04: TIME_EV15			14	(Unused)		
	15	04: TIME_EV16			15	(Unused)		
			,			1()		

Pro	ofile nu	mber 12 (Simple PID control with	4 connec	connected controllers) on page 2			
		IN area		OUT area			
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment		
position	position 0	Receive data valid	position	position 0	Rescan request		
U	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted			The fixed-part is omitted		
		(See profile number 0 on page 1)			(See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: P_L1_1	5		01: P_L1_1		
6		01: I_L1_1	6		01: I_L1_1		
7		01: D_L1_1	7		01: D_L1_1		
8		01: L.PID	8		01: L.PID		
9		01: A1_L1_1	9		01: A1_L1_1		
10		01: A2_L1_1	10		01: A2_L1_1		
11		01: A3_L1_1	11		01: A3_L1_1		
12		(Unused)	12		(Unused)		
13		02: P_L1_1	13		02: P_L1_1		
14		02: I_L1_1	14		02: I_L1_1		
15		02: D_L1_1	15		02: D_L1_1		
16		02: L.PID	16		02: L.PID		
17		02: A1_L1_1	17		02: A1_L1_1		
18		02: A2_L1_1	18		02: A2_L1_1		
19		02: A3_L1_1	19		02: A3_L1_1		
20		(Unused)	20		(Unused)		
21		03: P_L1_1	21		03: P_L1_1		
22		03: I_L1_1	22		03: I_L1_1		
23		03: D_L1_1	23		03: D_L1_1		
24		03: L.PID	24		03: L.PID		
25		03: A1_L1_1	25	 	03: A1_L1_1		
26		03: A2_L1_1	26		03: A2_L1_1		
27		03: A3_L1_1	27	 	03: A3_L1_1		
28		(Unused)	28	 	(Unused)		
29		04: P_L1_1	29		04: P_L1_1		
30		04: I_L1_1	30		04: I_L1_1		
31		04: D_L1_1	31	-	04: D_L1_1		
32		04: L.PID	32		04: L.PID		
33		04: A1_L1_1	33	-	04: A1_L1_1		
34		04: A2_L1_1	34	-	04: A2_L1_1		
35		04: A3_L1_1	35	 	04: A3_L1_1		
36		(Unused)	36		(Unused)		
37		,	37	-	,		
38		(Unused)	38	-	(Unused)		
39		(Unused)	39		(Unused)		
40		(Unused)	40		(Unused)		
41		(Unused)	40	-	(Unused)		
42		(Unused)	41	-	(Unused)		
		(Unused)			(Unused)		
43		(Unused)	43	<u> </u>	(Unused)		

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		Imber 12 (Simple F IN area -DP/DeviceNet slave (UT				ntrollers) on page OUT area OP/DeviceNet master –	
Pr		OFIBUS-DP/DeviceNet m		PRO		eviceNet slave (UTAdva	
Word	Bit position	Contents of as	ssignment	Word	Bit position	Contents of	assignment
0	0	Receive data valid		0	0	Rescan request	
	1	During-write			1	(Reserved)	
	2	Write acknowledgement			2	Write request	
	3	(Reserved)			3	(Reserved)	
	4	(Reserved)			4 5	(Reserved)	
	5 6	(Reserved)			6	(Reserved)	
	7	(Reserved)			7	(Reserved)	
		,				,	
•	•	The fixed-part	is omitted	•	•	The fixed-pa	rt is omitted
•	•	(See profile number		•	•	(See profile numb	
•	•	(CCC promo name	o o pago .,	•	•	(eee premerium	oo. c o page .,
4		Current page		4		Page change request	
5		01: L.TY1		5		01: L.TY1	
6		01: L.EV1		6		01: L.EV1	
7		01: L.TY2		7		01: L.TY2	
8		01: L.EV2		8		01: L.EV2	
9		01: L.TY3)	9		01: L.TY3	
10		01: L.EV3	>UP35A: unused	10		01: L.EV3	
11		01: L.TY4		11		01: L.TY4	
12		01: L.EV4		12		01: L.EV4	
13		01: L.TY5		13		01: L.TY5	
14		01: L.EV5.		14		01: L.EV5.	UP35A: unused
15		01: L.TY6		15		01: L.TY6	
16		01: L.EV6		16		01: L.EV6	
17		01: L.TY7		17		01: L.TY7	
18		01: L.EV7		18		01: L.EV7	
19		(Unused)	,	19		(Unused))
20		(Unused)		20		(Unused)	
21		02: L.TY1		21		02: L.TY1	
22		02: L.EV1		22		02: L.EV1	
23		02: L.TY2		23		02: L.TY2	
24		02: L.EV2		24		02: L.EV2	
25		02: L.TY3]	25		02: L.TY3	
26		02: L.EV3		26		02: L.EV3	
27		02: L.TY4		27		02: L.TY4	
28		02: L.EV4		28		02: L.EV4	
29		02: L.TY5		29		02: L.TY5	
30		02: L.EV5.	UP35A: unused	30		02: L.EV5.	UP35A: unuse
31		02: L.TY6		31		02: L.TY6	
32		02: L.EV6		32		02: L.EV6	
33		02: L.TY7		33		02: L.TY7	
34		02: L.EV7		34		02: L.EV7	
35		(Unused)		35		(Unused)	
36		(Unused)		36		(Unused)	
37		(Unused)		37		(Unused)	
38 39		(Unused)		38		(Unused)	
40		(Unused)		40		(Unused)	
41		(Unused)		41		(Unused)	
42		(Unused)		42		(Unused)	
43		(Unused)		43		(Unused)	

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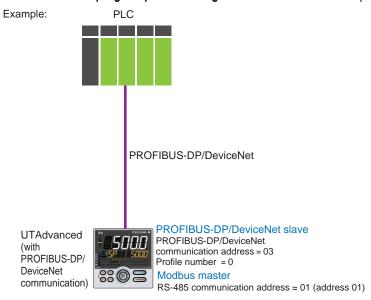
Pro	ofile nu	mber 12 (Simple	PID control with	4 connec	ted co		4
PR		IN area -DP/DeviceNet slave (U OFIBUS-DP/DeviceNet		PRO		OUT area PP/DeviceNet master → eviceNet slave (UTAdva	
Word	Bit	Contents of a		Word	Bit	Contents of a	•
0	0	Receive data valid		0	0	Rescan request	
	1	During-write			1	(Reserved)	
	2	Write acknowledgemen	t		2	Write request	
	3	(Reserved)			3	(Reserved)	
	4	(Reserved)			4	(Reserved)	
	5 6	(Reserved)			5 6	(Reserved)	
	7	(Reserved)			7	(Reserved)	
		((1000))				(110001100)	
•	•	The fixed-par	t is amitted	•	•	The fixed-par	t is omitted
•	•	(See profile numb		•	•	(See profile numb	
•	•	(Occ prome name	er o on page 1)	•	•	(Occ prome name	rer o on page 1)
4		Current page		4		Page change request	
5		03: L.TY1		5		03: L.TY1	
6		03: L.EV1		6		03: L.EV1	
7		03: L.EV1		7		03: L.EV1	
8		03: L.EV1		8		03: L.EV1	
9		03: L.TY3		9		03: L.TY3	\
10		03: L.EV3		10		03: L.EV3	
11		03: L.TY4		11		03: L.TY4	
12		03: L.EV4	>UP35A: unused	12		03: L.EV4	
13		03: L.TY5		13		03: L.TY5	>UP35A: unused
14		03: L.EV5.		14		03: L.EV5.	0. 00/ 1. 0000 0
15		03: L.TY6		15		03: L.TY6	
16		03: L.EV6		16		03: L.EV6	
17		03: L.TY7		17		03: L.TY7	
18		03: L.EV7		18		03: L.EV7	J
19		(Unused)		19		(Unused)	
20		(Unused)		20		(Unused)	
21		04: L.TY1		21		04: L.TY1	
22		04: L.EV1		22		04: L.EV1	
23 24		04: L.TY2 04: L.EV2		23		04: L.TY2 04: L.EV2	
25		04: L.TY3		25		04: L.TY3	<u> </u>
26		04: L.EV3		26		04: L.EV3	
27		04: L.TY4		27		04: L.TY4	
28		04: L.EV4		28		04: L.EV4	
29		04: L.TY5	UP35A: unused	29		04: L.TY5	>UP35A: unused
30		04: L.EV5.	2. 55 3114554	30		04: L.EV5.	
31		04: L.TY6		31		04: L.TY6	
32		04: L.EV6		32		04: L.EV6	
33		04: L.TY7		33		04: L.TY7	
34		04: L.EV7	<u> </u>	34		04: L.EV7	J
35		(Unused)		35		(Unused)	
36		(Unused)		36		(Unused)	
37		(Unused)		37		(Unused)	
38 39		(Unused) (Unused)		38		(Unused) (Unused)	
40		(Unused)		40		(Unused)	
41		(Unused)		41		(Unused)	
42		(Unused)		42		(Unused)	
43		(Unused)		43		(Unused)	<u> </u>

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Profile number 13 (Simple PID control with program patern setting for 1 connected controller)





Page 1

PROFIB	US-DP/D	IN area eviceNet slave (UTAdvanced) → PROFIBUS- DP/DeviceNet master	PRO	ern setting for 1 connected controller) on page 1 OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: PV_L1	5		01: H.TSP_L1		
6		01: CSP_L1	6		01: H.SP_L1		
7		01: CSP_L2 UP35A: unused	7		01: H.SP_L2 UP35A: unused		
8		01: SEG.RTIME	8		01: H.TM_L1		
9		01: OUT_L1	9		01: LSP_L1		
10		01: LSP_L2 UP35A: unused	10		01: LSP_L2 UP35A: unused		
11		01: OUT_L1	11		01:MOUT_L1		
12		01: H.OUT_L1	12		01:MOUT_L1		
13		01: C.OUT_L1	13		01: MOUTc_L1		
14		01: C.PTNO.	14		01: PTNO.		
15		01: SEG.N	15		01: SST		
16		(Unused)	16		(Unused)		
		:					
77		(Unused)	77		(Unused)		

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ROFIB	US-DP/D	IN area eviceNet slave (UTAdvand DP/DeviceNet master	ced) → PROFIBUS-	PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word osition	Bit position	Contents of as	signment	Word position	Bit position	Contents of assignment		
78	0	01: RST_ON		78	0	01: RST_ON		
	1	01: PRG_ON			1	01: PRG_ON		
	2	01: LOC_ON			2	01: LOC_ON		
	3	01: HOLD			3	01: HOLD		
	4	(Unused)			4	01: ADV		
	5	01: A.M_L1			5	01: A.M_L1		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	(Unused)			8	(Unused)		
	9	(Unused)			9	(Unused)		
	10	(Unused)			10	(Unused)		
	11	(Unused)			11	(Unused)		
	12	(Unused)			12	(Unused)		
	13	(Unused)			13	(Unused)		
	14	(Unused)			14	(Unused)		
	15	(Unused)			15	(Unused)		
79	0	01: PV_EV1		79	0	(Unused)		
	1	01: PV EV2			1	(Unused)		
	2	01: PV EV3			2	(Unused)		
	3	01: PV EV4			3	(Unused)		
	4	01: PV EV5			4	(Unused)		
	5	01: PV EV6	JP35A: unused		5	(Unused)		
	6	01: PV_EV0			6	(Unused)		
	7	 -			7	(Unused)		
	8	01: PV_EV8			8	(Unused)		
	9	01: ALW1_L1			9	(Unused)		
			P35A: unused			,		
	10		P35A: unused		10 11	(Unused)		
			-SSA. unuseu			(Unused)		
	12	(Unused)			12	(Unused)		
	13	(Unused)			13	(Unused)		
	14	(Unused)			14	(Unused)		
80	15	(Unused)		80	15	(Unused)		
00	0	01: TIME_EV1		00	0	(Unused)		
	1	01: TIME_EV2			1	(Unused)		
	2	01: TIME_EV3			2	(Unused)		
	3	01: TIME_EV4			3	(Unused)		
	4	01: TIME_EV5			4	(Unused)		
	5	01: TIME_EV6			5	(Unused)		
	6	01: TIME_EV7			6	(Unused)		
	7	01: TIME_EV8			7	(Unused)		
	8	01: TIME_EV9			8	(Unused)		
	9	01: TIME_EV10	UP35A: unused		9	(Unused)		
	10	01: TIME_EV11	OI 33A. ulluseu		10	(Unused)		
	11	01: TIME_EV12			11	(Unused)		
	12	01: TIME_EV13			12	(Unused)		
	13	01: TIME_EV14			13	(Unused)		
	14	01: TIME_EV15			14	(Unused)		
	15	01: TIME EV16			15	(Unused)		

Page 2

Pro	file num	ber 13 (Simple PID co	ntrol with program բ	oattern settir	ng for 1 c	connected controller)	on page 2
PF		IN area -DP/DeviceNet slave (U OFIBUS-DP/DeviceNet		PRO		OOT area DP/DeviceNet master → eviceNet slave (UTAdva	
Word position	Bit position	Contents of a	ssignment	Word	Bit position	Contents of a	assignment
0	0	Receive data valid		0	position	Rescan request	
	1	During-write			1	(Reserved)	
	2	Write acknowledgemen	t		2	Write request	
	3	(Reserved)			3	(Reserved)	
	4	(Reserved)			4	(Reserved)	
	5	(Reserved)			5	(Reserved)	
	6	(Reserved)			6	(Reserved)	
	7	(Reserved)			7	(Reserved)	
•	•	The fixed-par	t is omitted	•	•	The fixed-par	rt is omitted
•	•	(See profile numb	er 0 on page 1)	•	•	(See profile numb	per 0 on page 1)
•	•			•	•		
4		Current page		4		Page change request	
5		01: P_L1_1		5		01: P_L1_1	
6		01: I_L1_1		6		01: I_L1_1	
7		01: D_L1_1		7		01: D_L1_1	
8		01: Pc_L1_1		8		01: Pc_L1_1	
9		01: lc_L1_1		9		01: lc_L1_1	
10		01: Dc_L1_1		10		01: Dc_L1_1	
11 12		01: L.PID		11		01: L.PID	
13		01: A1_L1_1 01: A2_L1_1		13		01: A1_L1_1 01: A2_L1_1	
14			JP35A: unused	14			UP35A: unused
15			JP35A: unused	15			UP35A: unused
16		(Unused)	J. 007 II UI 1000	16		(Unused)	0. 007.1. 0.1.0000
17		(Unused)		17		(Unused)	
18		(Unused)		18		(Unused)	
19		(Unused)		19		(Unused)	
20		(Unused)		20		(Unused)	
21		(Unused)		21		(Unused)	
22		(Unused)		22		(Unused)	
23 24		(Unused)		23		(Unused) (Unused)	
25		(Unused) 01: L.TY1		25		01: L.TY1	
26		01: L.EV1		26		01: L.EV1	
27		01: L.TY2		27		01: L.TY2	
28		01: L.EV2		28		01: L.EV2	
29		01: L.TY3		29		01: L.TY3)
30		01: L.EV3		30		01: L.EV3	
31		01: L.TY4		31		01: L.TY4	
32		01: L.EV4		32		01: L.EV4	
33		01: L.TY5		33		01: L.TY5	
34		01: L.EV5.	UP35A: unused	34	-	01: L.EV5.	UP35A: unused
35		01: L.TY6		35		01: L.TY6	
36 37		01: L.EV6 01: L.TY7		36	-	01: L.EV6 01: L.TY7	
38		01: L.EV7		38	-	01: L.EV7	
39		01: L.TY8		39		01: L.TY8	
40		01: L.EV8		40		01: L.EV8	J
41		(Unused)		41		(Unused)	
:		•		•		•	
74		(Unused)		74		(Unused)	
75		01: LR.P		75		01: CLR.P	
76		01: CLR.TRG		76		01: CLR.TRG	
77		01: PTN.ERR		77		(Unused)	
78		(Unused)		78		(Unused)	
79		(Unused)		79		(Unused)	
80		(Unused)		80		(Unused)	

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Page 3

Pro	file num	ber 13 (Simple PID co	ntrol with program	pattern settir	ng for 1 c	onnected controller) on page 3		
		IN area			OUT area				
PR		-DP/DeviceNet slave (U OFIBUS-DP/DeviceNet		PRO		DP/DeviceNet master eviceNet slave (UTAd			
Word position	Bit position	Contents of a	assignment	Word position	Bit position	Contents o	f assignment		
0	0	Receive data valid		0	0	Rescan request			
	1	During-write		_	1	(Reserved)			
	2	Write acknowledgemen	t	-	2	Write request			
	3	(Reserved)		-	3	(Reserved)			
	5	(Reserved)		-	5	(Reserved)			
	6	(Reserved)		-	6	(Reserved)			
	7	(Reserved)		1	7	(Reserved)			
		,				,			
•	•	The fixed-par	t is omitted	•	•	The fixed n	art is omitted		
•	•	(See profile numb		•	•	•	nber 0 on page 1)		
•	•	(See profile fluific	er o on page 1)	•	•	(See profile flui	iber o on page 1)		
4		Current page		4		Page change request			
5		01: PTNOC		5		01: PTNOC			
6		01: PTN.SEG		6		01: PTN.SEG			
7		01: SSP_L1		7		01: SSP_L1			
8			JP35A: unused	8		01: SSP_L2	UP35A: unused		
9		01: STC		9		01: STC			
10		01: WT.SW1		10		01: WT.SW1			
11		01: WZ.UP1		11		01: WZ.UP1			
13		01: WZ.LO1 01: WT.TM1		13		01: WZ.LO1 01: WT.TM1			
14		01: WT.SW2)	14		01: WT.SW2)		
15		01: WZ.UP2		15		01: WZ.UP2	-		
16		01: WZ.LO2		16		01: WZ.LO2	-		
17		01: WT.TM2		17		01: WT.TM2	-		
18		01: WT.SW3		18		01: WT.SW3	-		
19		01: WZ.UP3		19		01: WZ.UP3			
20		01: WZ.LO3		20		01: WZ.LO3	_		
21		01: WT.TM3	UP35A: unused	21		01: WT.TM3	UP35A: unused		
22		01: WT.SW4	Or oor ii anaooa	22		01: WT.SW4	-		
23		01: WZ.UP4		23		01: WZ.UP4	-		
24 25		01: WZ.LO4 01: WT.TM4		24		01: WZ.LO4 01: WT.TM4	-		
26		01: WT.SW5		26		01: WT.SW5	-		
27		01: WZ.UP5		27		01: WZ.UP5	-		
28		01: WZ.LO5		28		01: WZ.LO5	-		
29		01: WT.TM5	J	29		01: WT.TM5	- J		
30		01: R.CYCL		30		01: R.CYCL			
31		01: R.STRT		31		01: R.STRT			
32		01: R.END		32		01: R.END			
33		(Unused)		33	-	(Unused)			
34		(Unused)		34		(Unused)			
35		01: P.NAME		35		01: P.NAME			
36		01: P.NAME 01: P.NAME		36		01: P.NAME 01: P.NAME			
38		01: P.NAME		38		01: P.NAME			
39		01: P.NAME		39		01: P.NAME			
40		01: P.NAME		40		01: P.NAME			
41		01: P.NAME		41		01: P.NAME			
42		01: P.NAME		42		01: P.NAME	•		
43		01: P.NAME		43		01: P.NAME			
44		01: P.NAME		44		01: P.NAME			
45		01: P.NAME		45		01: P.NAME			
46 47		(Unused)		46	-	(Unused)			
•		(Unused)		•		(Unused)	•		
•		(•		(Llauses)	•		
80		(Unused)		80		(Unused)			

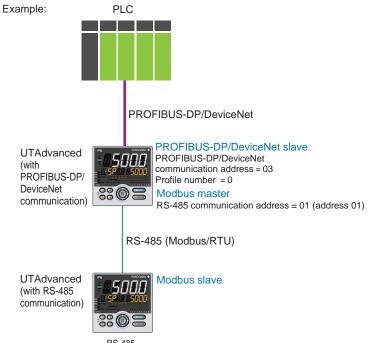
Pro	file num	ber 13 (Simple PID co	ontrol with program pa	attern settir	ng for 1 c	connected controller)	on page 4
PF		IN area -DP/DeviceNet slave (U OFIBUS-DP/DeviceNet		PRO		OUT area DP/DeviceNet master – eviceNet slave (UTAdv	
Word position	Bit position	Contents of	assignment	Word	Bit position	Contents of	assignment
0	0	Receive data valid		0	0	Rescan request	
	2	During-write Write acknowledgemer	nt .		2	(Reserved) Write request	
	3	(Reserved)	ıt		3	(Reserved)	
	4	(Reserved)			4	(Reserved)	
	5 6	(Reserved)			5 6	(Reserved)	
	7	(Reserved)			7	(Reserved)	
•		The fixed-pa	rt is omitted	•		The fixed-pa	art is omitted
•	•	(See profile numb	per 0 on page 1)	•	•	(See profile num	ber 0 on page 1)
4		Current page		4		Page change request	
5		01: PTNOC		5		01: PTNOC	
6		01: PTN.SEG		6		01: PTN.SEG	
7		01: TSP_L1		7		01: TSP_L1	
8		01: TSP_L2	UP35A: unused	8		01: TSP_L2	UP35A: unused
9		01: TIME		9		01: TIME	
10		01: TM.RT		10		01: TM.RT	
11		01: S.PID		11		01: S.PID	
12		01: JC		12		01: JC	
13		01: PV.TY1		13		01: PV.TY1	
14		01: PV.EV1		14		01: PV.EV1	
15		01: PV.TY2		15		01: PV.TY2	
16		01: PV.EV2		16		01: PV.EV2	
17		01: PV.TY3		17		01: PV.TY3	
18		01: PV.EV3		18		01: PV.EV3	•
19		01: PV.TY4		19		01: PV.TY4	
20		01: PV.EV4		20		01: PV.EV4	
21		01: PV.TY5		21		01: PV.TY5	
22		01: PV.EV5		22		01: PV.EV5	
23		01: PV.TY6	UP35A: unused	23		01: PV.TY6	UP35A: unused
24		01: PV.EV6		24		01: PV.EV6	
25		01: PV.TY7		25		01: PV.TY7	
26		01: PV.EV7		26		01: PV.EV7	
27		01: PV.TY8		27		01: PV.TY8	
28		01: PV.EV8	J	28		01: PV.EV8	
29		01: TME1		29		01: TME1	
30		01: T.ON1		30		01: T.ON1	
31		01: T.OF1		31		01: T.OF1	
32		01: TME2		32		01: TME2	
33		01: T.ON2		33		01: T.ON2	
34		01: T.OF2		34		01: T.OF2	
35		01: TME3		35		01: TME3	
36		01: T.ON3		36		01: T.ON3	

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Pro	file numl		D control with program p	attern settir	ng for 1 c			
PR		IN area -DP/DeviceNet slav OFIBUS-DP/Device	ve (UTAdvanced) →	PRO		OUT area OP/DeviceNet mast eviceNet slave (UT	ter → PROFIBUS-DP/	
Word	Bit position		s of assignment	Word Bit				
37		01: T.OF3		37		01: T.OF3		
38		01: TME4		38		01: TME4		
39		01: T.ON4		39		01: T.ON4		
40		01: T.OF4		40		01: T.OF4		
41		01: TME5		41		01: TME5		
42		01: T.ON5		42		01: T.ON5		
43		01: T.OF5		43		01: T.OF5		
44		01: TME6		44		01: TME6		
45		01: T.ON6		45		01: T.ON6		
46		01: T.OF6		46		01: T.OF6		
47		01: TME7		47		01: TME7		
48		01: T.ON7		48		01: T.ON7		
49		01: T.OF7		49		01: T.OF7		
50		01: TME8		50		01: TME8		
51		01: T.ON8		51		01: T.ON8		
52		01: T.OF8		52		01: T.OF8		
53		01: TME9		53		01: TME9		
54		01: T.ON9		54		01: T.ON9		
55		01: T.OF9		55		01: T.OF9		
56		01: TME10		56		01: TME10		
57		01: T.ON10		57		01: T.ON10	UP35A: unused	
58		01: T.OF10	≻UP35A: unused	58		01: T.OF10		
59		01: TME11		59		01: TME11		
60		01: T.ON11		60		01: T.ON11		
61		01: T.OF11		61		01: T.OF11		
62		01: TME12		62		01: TME12		
63		01: T.ON12		63		01: T.ON12		
64		01: T.OF12		64		01: T.OF12		
65		01: TME13		65		01: TME13		
66		01: T.ON13		66		01: T.ON13		
67		01: T.OF13		67		01: T.OF13		
68		01: TME14		68		01: TME14		
69		01: T.ON14		69		01: T.ON14		
70		01: T.OF14		70		01: T.OF14		
71		01: TME15		71		01: TME15		
72		01: T.ON15		72		01: T.ON15		
73		01: T.OF15		73		01: T.OF15		
74		01: TME16		74		01: TME16		
75		01: T.ON16		75		01: T.ON16		
76		01: T.OF16	J	76		01: T.OF16	J	
77		01: PTN.ERR		77		(Unused)		
78 79		(Unused)		78 79		(Unused)		
80		(Unused)		80		(Unused)		

Profile number 14 (Cascade control with 2 connected controllers)





RS-485 communication address = 02 (address 02)

Page 1

Pro	ofile nu	mber 14 (Cascade control with 2	cor	necte	d conti	rollers) on page 1	
		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
0	0	Receive data valid		0	0	Rescan request	
	1	During-write			1	(Reserved)	
	2	Write acknowledgement			2	Write request	
	3	(Reserved)			3	(Reserved)	
	4	(Reserved)			4	(Reserved)	
	5	(Reserved)			5	(Reserved)	
	6	(Reserved)			6	(Reserved)	
	7	(Reserved)			7	(Reserved)	
•	•	The fixed-part is omitted (See profile number 0 on page 1)		•	•	The fixed-part is omitted (See profile number 0 on page 1)	
4		Current page		4		Page change request	
5		01: PV_L1		5		01: H.TSP_L1	
6		01: CSP_L1		6		01: H.SP_L1	
7		01: SEG_RTIME		7		01: H.TM_L1	
8		01: LSP_L1		8		01: LSP_L1	
9		01: OUT_L2		9		01: MOUT_L2	
10		(Unused)		10		(Unused)	
11		(Unused)		11		(Unused)	
12		01: C.PTNO.		12		01: PTNO.	
13		01: SEG.N		13		01: SST	
14		(Unused)		14		(Unused)	
15		01: PV_L2		15		(Unused)	
16		01: CSP_L2		16		01: LSP_L2	

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Pro	Profile number 14 (Cascade control with 2 connected controllers) on page 1							
PF		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
17	position	01: OUT_L2	17	position	01: MOUT_L2			
18		(Unused)	18		(Unused)			
19		(Unused)	19		(Unused)			
20		(Unused)	20		(Unused)			
21		02: PV_L1	21		02: H.TSP_L1			
22		02: CSP_L1	22		02: H.SP_L1			
23		_			02: H.TM_L1			
		02: SEG_RTIME	23					
24		02: LSP_L1	24		02: LSP_L1			
25		02: OUT_L2	25		02: MOUT_L2			
26		(Unused)	26		(Unused)			
27		(Unused)	27		(Unused)			
28		02: C.PTNO.	28		02: PTNO.			
29		02: SEG.N	29		02: SST			
30		(Unused)	30		(Unused)			
31		02: PV_L2	31		(Unused)			
32		02: CSP_L2	32		02: LSP_L2			
33		02: OUT_L2	33		02: MOUT_L2			
34		(Unused)	34		(Unused)			
35		(Unused)	35		(Unused)			
36	0	01: RST_ON	36	0	01: RST_ON			
00	1	01: PRG_ON		1	01: PRG_ON			
	2	01: LOC_ON		2	01: LOC_ON			
	3 4	01: HOLD (Unused)		3	01: HOLD 01: ADV			
	5	01: A.M_L2		5	01: A.M_L2			
	6	(Unused)		6	(Unused)			
	7 8	(Unused) 01: L.C		7 8	(Unused) 01: L.C			
	9	(Unused)		9	(Unused)			
	10	(Unused)		10	(Unused)			
	11 12	(Unused)		11 12	(Unused)			
	13	(Unused)		13	(Unused)			
	14	(Unused)		14	(Unused)			
37	15 0	(Unused) 01: PV EV1	37	15 0	(Unused)			
"	1	01: PV_EV2		1	(Unused)			
	2	01: PV_EV3		2	(Unused)			
	3	01: PV_EV4 01: PV EV5		3	(Unused)			
	5	01: PV_EV6		5	(Unused)			
		01: PV_EV7		6	(Unused)			
	7 8	01: PV_EV8 01: ALM1_L1		8	(Unused)			
	9	01: ALM2_L1		9	(Unused)			
	10	01: ALM3_L1		10	(Unused)			
	11 12	01: ALM4_L1 (Unused)		11 12	(Unused)			
	13	(Unused)		13	(Unused)			
	14	(Unused)		14	(Unused)			
	15	(Unused)		15	(Unused)			

Pro	ofile nu	mber 14 (Cascade control with 2	COI	nnecte	d contr	rollers) on page 1
		IN area				OUT area
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment
position		ū		•	position	-
38	0	01: TIME_EV1		38	0	(Unused)
	1	01: TIME_EV2			1	(Unused)
	2	01: TIME_EV3			2	(Unused)
	3	01: TIME_EV4			3	(Unused)
	4	01: TIME_EV5			4	(Unused)
	5	01: TIME_EV6			5	(Unused)
	6	01: TIME_EV7			6	(Unused)
	7	01: TIME_EV8			7	(Unused)
	8	01: TIME_EV9			8	(Unused)
	9	01: TIME_EV10			9	(Unused)
	10	01: TIME_EV11			10	(Unused)
	11	01: TIME_EV12			11	(Unused)
	12	01: TIME_EV13			12	(Unused)
	13	01: TIME_EV14			13	(Unused)
	14	01: TIME_EV15			14	(Unused)
	15	01: TIME_EV16			15	(Unused)
39	0	(Unused)		39	0	(Unused)
	1	(Unused)			1	(Unused)
	2	(Unused)			2	(Unused)
	3	(Unused)			3	(Unused)
	4	(Unused)			4	(Unused)
	5	(Unused)			5	(Unused)
	6	(Unused)			6	(Unused)
	7	(Unused)			7	(Unused)
	8	(Unused)			8	(Unused)
	9	(Unused)			9	(Unused)
	10	(Unused)			10	(Unused)
	11	(Unused)			11	(Unused)
	12	(Unused)			12	(Unused)
	13	(Unused)			13	(Unused)
	14	(Unused)			14	(Unused)
	15	(Unused)		40	15	(Unused)
40	0	02: RST_ON		40	0	02: RST_ON
	1	02: PRG_ON			1	02: PRG_ON
	2	02: LOC_ON			2	02: LOC_ON
	3	02: HOLD			3	02: HOLD
	4	(Unused)			4	02: ADV
	5	02: A.M_L2			5	02: A.M_L2
	7	(Unused)			6 7	(Unused)
	8	02: L.C (Unused)			8 9	02: L.C (Unused)
						;
	10	(Unused)			10	(Unused)
	11	(Unused)			11	(Unused)
	12	,			12	,
	13	(Unused)			13	(Unused)
	14	(Unused)			14	(Unused)
	15	(Unused)			15	(Unused)

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Pro	ofile nu	mber 14 (Cascade control with 2	СО	onnected controllers) on page 1			
		IN area		OUT area			
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
41	0	02: PV_EV1		41	0	(Unused)	
	1	02: PV_EV2	1		1	(Unused)	
	2	02: PV_EV3	1		2	(Unused)	
	3	02: PV_EV4	1		3	(Unused)	
	4	02: PV_EV5	1		4	(Unused)	
	5	02: PV_EV6	1		5	(Unused)	
	6	02: PV_EV7	1		6	(Unused)	
	7	02: PV_EV8	1		7	(Unused)	
	8	02: ALM1_L1	1		8	(Unused)	
	9	02: ALM2_L1	1		9	(Unused)	
	10	02: ALM3_L1	1		10	(Unused)	
	11	02: ALM4_L1	1		11	(Unused)	
	12	(Unused)	1		12	(Unused)	
	13	(Unused)	1		13	(Unused)	
	14	(Unused)			14	(Unused)	
	15	(Unused)	ĺ		15	(Unused)	
42	0	02: TIME_EV1	1	42	0	(Unused)	
	1	02: TIME_EV2	1		1	(Unused)	
	2	02: TIME_EV3			2	(Unused)	
	3	02: TIME_EV4	ĺ		3	(Unused)	
	4	02: TIME_EV5	1		4	(Unused)	
	5	02: TIME_EV6	1		5	(Unused)	
	6	02: TIME_EV7	1		6	(Unused)	
	7	02: TIME_EV8	İ		7	(Unused)	
	8	02: TIME_EV9	1		8	(Unused)	
	9	02: TIME_EV10	1		9	(Unused)	
	10	02: TIME_EV11	1		10	(Unused)	
	11	02: TIME_EV12	1		11	(Unused)	
	12	02: TIME_EV13	1		12	(Unused)	
	13	02: TIME_EV14	1		13	(Unused)	
	14	02: TIME_EV15	1		14	(Unused)	
	15	02: TIME_EV16			15	(Unused)	
43	0	(Unused)		43	0	(Unused)	
	1	(Unused)			1	(Unused)	
	2	(Unused)			2	(Unused)	
	3	(Unused)			3	(Unused)	
	4	(Unused)			4	(Unused)	
	5	(Unused)			5	(Unused)	
	6	(Unused)			6	(Unused)	
	7	(Unused)			7	(Unused)	
	8	(Unused)			8	(Unused)	
	9	(Unused)			9	(Unused)	
	10	(Unused)			10	(Unused)	
	11	(Unused)			11	(Unused)	
	12	(Unused)			12	(Unused)	
	13	(Unused)			13	(Unused)	
	14	(Unused)			14	(Unused)	
	15	(Unused)			15	(Unused)	

PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			PRO	PROFIBUS-DP/DeviceNet master → PROFIBUS-DP DeviceNet slave (UTAdvanced)			
Word osition	Bit position	Contents of assignment	Word	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
ŭ	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•		The fixed-part is omitted			The fixed-part is omitted		
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: P_L1_1	5		01: P_L1_1		
6		01: I_L1_1	6		01: I_L1_1		
7		01: D_L1_1	7		01: D_L1_1		
8		01: L.PID	8		01: L.PID		
9		01: A1_L1_1	9		01: A1_L1_1		
10		01: A2_L1_1	10		01: A2_L1_1		
11		01: A3_L1_1	11		01: A3_L1_1		
12		01: A4_L1_1	12		01: A4_L1_1		
13		01: P_L2_1	13		01: P_L2_1		
14			14				
		01: I_L2_1			01: I_L2_1		
15		01: D_L2_1	15		01: D_L2_1		
16		01: A1_L2_1	16		01: A1_L2_1		
17		01: A2_L2_1	17		01: A2_L2_1		
18		01: A3_L2_1	18		01: A3_L2_1		
19		01: A4_L2_1	19		01: A4_L2_1		
20		(Unused)	20		(Unused)		
21		02: P_L1_1	21		02: P_L1_1		
22		03: I_L1_1	22		03: I_L1_1		
23		02: D_L1_1	23		02: D_L1_1		
24		02: L.PID	24		02: L.PID		
25		02: A1_L1_1	25		02: A1_L1_1		
26		02: A2_L1_1	26		02: A2_L1_1		
27		02: A3_L1_1	27		02: A3_L1_1		
28		02: A4_L1_1	28		02: A4_L1_1		
29		02: P_L2_1	29		02: P_L2_1		
30		02: I_L2_1	30		02: I_L2_1		
31		02: D_L2_1	31		02: D_L2_1		
32		02: A1_L2_1	32		02: A1_L2_1		
33		02: A2_L2_1	33		02: A1_L2_1		
34		02: A3_L2_1	34		02: A3_L2_1		
35		02: A4_L2_1	35		02: A4_L2_1		
36		(Unused)	36		(Unused)		
:		:			:		

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Page 3

		Profile number 14 (Cascade control with 2 connected controllers) on page 3							
		IN area		OUT area					
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit	Contents of assignment				
0	0	Receive data valid	0	0	Rescan request				
	1	During-write		1	(Reserved)				
	2	Write acknowledgement		2	Write request				
	3	(Reserved)		3	(Reserved)				
	4	(Reserved)		4	(Reserved)				
	5	(Reserved)		5	(Reserved)				
	6 7	(Reserved)		7	(Reserved)				
	,	(Neserved)			(Neserveu)				
		The fixed-part is omitted			The fixed-part is omitted				
		(See profile number 0 on page 1)	•		(See profile number 0 on page 1)				
4		Current page	4		Page change request				
5		01: L.TY1	5		01: L.TY1				
6		01: L.EV1	6		01: L.EV1				
7		01: L.TY2	7	L	01: L.TY2				
8		01: L.EV2	8		01: L.EV2				
9		01: L.TY3	9		01: L.TY3				
10		01: L.EV3	10		01: L.EV3				
11		01: L.TY4	11		01: L.TY4				
12		01: L.EV4	12		01: L.EV4				
13		01: L.TY5	13		01: L.TY5				
14		01: L.EV5.	14		01: L.EV5.				
15		01: L.TY6	15		01: L.TY6				
16		01: L.EV6	16		01: L.EV6				
17		01: L.TY7	17		01: L.TY7				
18		01: L.EV7	18		01: L.EV7				
19		01: L.TY8	19		01: L.TY8				
20		01: L.EV8	20		01: L.EV8				
21		(Unused)	21		(Unused)				
22		(Unused)	22	ļ	(Unused)				
23		(Unused)	23		(Unused)				
24		(Unused)	24		(Unused)				
25 26		(Unused)	25		(Unused)				
27		(Unused)	26 27	-	(Unused)				
28		(Unused)	28	-	(Unused)				
29		(Unused)	29		(Unused)				
30		(Unused)	30		(Unused)				
31		(Unused)	31	1	(Unused)				
32		(Unused)	32		(Unused)				
33		(Unused)	33		(Unused)				
34		(Unused)	34		(Unused)				
35		(Unused)	35	-	(Unused)				
36		(Unused)	36	-	(Unused)				
37		(Unused)	37	-	(Unused)				
38 39		(Unused)	38		(Unused)				
40		(Unused)	40	-	(Unused)				
41		(Unused)	40	+	(Unused)				
		(Unused)	42		(Unused)				
42		1\		-	1/				

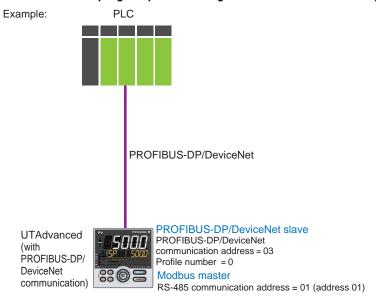
Pro	ofile nu	mber 14 (Cascade control with 2	connecte	d conti	rollers) on page 4			
		IN area		OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PR	-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		De	DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	2	During-write		2	(Reserved) Write request			
	3	Write acknowledgement (Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•		The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
•	•	(See profile number of on page 1)	•	•	(See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		02: L.TY1	5		02: L.TY1			
6		02: L.EV1	6		02: L.EV1			
7		02: L.TY2	7		02: L.TY2			
8		02: L.EV2	8		02: L.EV2			
9		02: L.TY3	9		02: L.TY3			
10		02: L.EV3	10		02: L.EV3			
11		02: L.TY4	11		02: L.TY4			
12		02: L.EV4	12		02: L.EV4			
13		02: L.TY5	13		02: L.TY5			
14		02: L.EV5.	14		02: L.EV5.			
15		02: L.TY6	15		02: L.TY6			
16		02: L.EV6	16		02: L.EV6			
17		02: L.TY7	17		02: L.TY7			
18		02: L.EV7	18		02: L.EV7			
19		02: L.TY8	19		02: L.TY8			
20		02: L.EV8	20		02: L.EV8			
21		(Unused)	21		(Unused)			
23		(Unused)	23		(Unused)			
24		(Unused)	24		(Unused)			
25		(Unused)	25		(Unused)			
26 27		(Unused)	26		(Unused)			
28		(Unused)	28		(Unused)			
29		(Unused)	29		(Unused)			
30		(Unused)	30		(Unused)			
31		(Unused)	31		(Unused)			
32		(Unused)	32		(Unused)			
33 34	-	(Unused)	33		(Unused)			
35	-	(Unused)	35		(Unused)			
36		(Unused)	36		(Unused)			
37		(Unused)	37		(Unused)			
38		(Unused)	38		(Unused)			
39	-	(Unused)	39		(Unused)			
40 41		(Unused)	40		(Unused)			
42		(Unused)	42		(Unused)			
43		(Unused)	43		(Unused)			

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Profile number 15 (Cascade control with program patern setting for 1 connected controller)





Page 1

	PR	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Receive data valid		
	1	During-write		1	During-write		
	2	Write acknowledgement		2	Write acknowledgement		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
		The fixed-part is omitted			The fixed-part is omitted		
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)		
4		Current page	4		Current page		
5		01: PV_L1	5		(Unused)		
6		01: CSP_L1	6		01: H.SP_L1		
7		01: SEG_RTIME	7		01: H.TM_L1		
8		01: LSP_L1	8		01: LSP_L1		
9		01: OUT_L2	9		01: MOUT_L2		
10		(Unused)	10		(Unused)		
11		(Unused)	11		(Unused)		
12		01: C.PTNO.	12		01: PTNO.		
13		01: SEG.N	13		01: SST		
14		(Unused)	14		(Unused)		
15		01: PV_L2	15		(Unused)		
16		01: CSP_L2	16		01: LSP_L2		
17		01: OUT_L2	17		01: MOUT_L2		
18		(Unused)	18		(Unused)		
:		:	:		:		

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Pro	Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 1							
	IN area			OUT area				
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →			PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PR	OFIBUS-DP/DeviceNet master		DeviceNet slave (UTAdvanced)				
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment			
	position	ū	position	position	· ·			
77		(Unused)	77		(Unused)			
78	0	01: RST_ON	78	0	01: RST_ON			
	1	01: PRG_ON		1	01: PRG_ON			
	2	01: LOC_ON		2	01: LOC_ON			
	3	01: HOLD		3	01: HOLD			
	4	(Unused)		4	01: ADV			
	5	01: A.M_L2		5	01: A.M_L2			
	6	(Unused)		6	(Unused)			
	7	(Unused)		7	(Unused)			
	8	01: L.C		8	01: L.C			
	9	(Unused)		9	(Unused)			
	10	(Unused)		10	(Unused)			
	11	(Unused)		11	(Unused)			
	12	(Unused)		12	(Unused)			
	13	(Unused)		13	(Unused)			
	14	(Unused)		14	(Unused)			
	15	(Unused)		15	(Unused)			
79	0	01: PV_EV1	79	0	(Unused)			
	1	01: PV_EV2		1	(Unused)			
	2	01: PV_EV3		2	(Unused)			
	3	01: PV_EV4		3	(Unused)			
	4	01: PV_EV5		4	(Unused)			
	5	01: PV_EV6		5	(Unused)			
	6	01: PV_EV7		6	(Unused)			
	7	01: PV_EV8		7	(Unused)			
	8	01: ALM1_L1		8	(Unused)			
	9	01: ALM2_L1		9	(Unused)			
	10	01: ALM3_L1		10	(Unused)			
	11	01: ALM4_L1		11	(Unused)			
	12	(Unused)		12	(Unused)			
	13	(Unused)		13	(Unused)			
	14	(Unused)		14	(Unused)			
90	15	(Unused)	80	15 0	(Unused)			
80	1	01: TIME_EV1 01: TIME_EV2	00	1	(Unused)			
		_			,			
	3	01: TIME_EV3 01: TIME_EV4		3	(Unused)			
	4	01: TIME_EV4 01: TIME_EV5		4	(Unused)			
	5	01: TIME_EV5 01: TIME_EV6		5	(Unused)			
	6	01: TIME_EV0 01: TIME_EV7		6	(Unused)			
	7	01: TIME_EV7 01: TIME_EV8		7	(Unused)			
		01: TIME_EV8 01: TIME_EV9		8	(Unused)			
	9	01: TIME_EV9		9	(Unused)			
	10	01: TIME_EV10		10	(Unused)			
	11	01: TIME_EV12		11	(Unused)			
	12	01: TIME_LV12		12	(Unused)			
	13	01: TIME_EV14		13	(Unused)			
	14	01: TIME_EV15		14	(Unused)			
	15	01: TIME EV16		15	(Unused)			
		-			,			

Pro	tile num	ber 15 (Cascade control with program par	ttern setting	n setting for 1 connected controller) on page 2 OUT area				
PF		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•		The fixed-part is omitted			The fixed-part is omitted			
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: P_L1_1	5		01: P_L1_1			
6		01: I_L1_1	6		01: I_L1_1			
7		01: D_L1_1	7		01: D_L1_1			
8		01: L.PID	8		01: L.PID			
9		01: A1_L1_1	9		01: A1_L1_1			
10		01: A2_L1_1	10		01: A2_L1_1			
11		01: A3_L1_1	11		01: A3_L1_1			
12		01: A4_L1_1	12		01: A4_L1_1			
13		01: P_L2_1	13		01: P_L2_1			
14		01: I_L2_1	14		01: I_L2_1			
15		01: D_L2_1	15		01: D_L2_1			
16		01: A1_L2_1	16		01: A1_L2_1			
17		01: A2_L2_1 01: A3_L2_1	17		01: A2_L2_1 01: A3_L2_1			
19		01: A4_L2_1	19		01: A4_L2_1			
20		(Unused)	20		(Unused)			
21		(Unused)	21		(Unused)			
22		(Unused)	22		(Unused)			
23		(Unused)	23		(Unused)			
24		(Unused)	24		(Unused)			
25		01: L.TY1	25		01: L.TY1			
26		01: L.EV1	26		01: L.EV1			
27		01: L.TY2	27		01: L.TY2			
28		01: L.EV2	28		01: L.EV2			
29		01: L.TY3	29		01: L.TY3			
30		01: L.EV3	30		01: L.EV3			
31		01: L.TY4	31		01: L.TY4			
32		01: L.EV4	32		01: L.EV4			
33		01: L.TY5	33		01: L.TY5			
34		01: L.EV5.	34		01: L.EV5.			
35		01: L.TY6	35		01: L.TY6			
36		01: L.EV6	36		01: L.EV6			

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Pro	file num	ber 15 (Cascade control with program pat	tern	setting	for 1 cor	nnected controller) on page 2		
PF	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
37		01: L.TY7		37		01: L.TY7		
38		01: L.EV7		38		01: L.EV7		
39		01: L.TY8		39		01: L.TY8		
40		01: L.EV8		40		01: L.EV8		
41		(Unused)		41		(Unused)		
:		:		:		:		
•		•		•		•		
74		(Unused)		74		(Unused)		
75		01: CLR.P		75		01: CLR.P		
76		01: CLR.TRG		76		01: CLR.TRG		
77		01: PTN.ERR		77		(Unused)		
78		(Unused)		78		(Unused)		
79		(Unused)		79		(Unused)		
80		(Unused)		80		(Unused)		

Pro	Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 3							
		IN area -DP/DeviceNet slave (UTAdvanced) →		OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
Word	PR Bit	OFIBUS-DP/DeviceNet master	Word	De Bit	eviceNet slave (UTAdvanced)			
	position	Contents of assignment		position	Contents of assignment			
0	0	Receive data valid	0	0	Receive data valid			
	2	During-write Write acknowledgement		2	During-write Write acknowledgement			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5 6	(Reserved)		5 6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted			
•		(See profile number 0 on page 1)	•		(See profile number 0 on page 1)			
4		Current page	4		Current page			
5		01: PTNOC	5		01: PTNOC			
6		01: SEGNOC	6		01: SEGNOC			
7		01: SSP_L1	7		01: SSP_L1			
8		(Unused)	8		(Unused)			
9		01: STC	9		01: STC			
10		01: WT.SW1	10		01: WT.SW1			
11		01: WZ.UP1	11		01: WZ.UP1			
12		01: WZ.LO1	12		01: WZ.LO1			
13		01: WT.TM1	13		01: WT.TM1			
14		01: WT.SW2	14		01: WT.SW2			
15		01: WZ.UP2	15		01: WZ.UP2			
16		01: WZ.LO2	16		01: WZ.LO2			
17		01: WT.TM2	17		01: WT.TM2			
18		01: WT.SW3	18		01: WT.SW3			
19		01: WZ.UP3	19		01: WZ.UP3			
20		01: WZ.LO3	20		01: WZ.LO3			
21		01: WT.TM3	21		01: WT.TM3			
22		01: WT.SW4	22		01: WT.SW4			
23		01: WZ.UP4	23		01: WZ.UP4			
24		01: WZ.LO4	24		01: WZ.LO4			
25		01: WT.TM4	25		01: WT.TM4			
26		01: WT.SW5	26		01: WT.SW5			
27		01: WZ.UP5	27		01: WZ.UP5			
28		01: WZ.LO5	28		01: WZ.LO5			
29		01: WT.TM5	29		01: WT.TM5			
30		01: R.CYCL	30		01: R.CYCL			
31		01: R.STRT	31		01: R.STRT			
32		01: R.END	32		01: R.END			
33		(Unused)	33		(Unused)			
34		(Unused)	34		(Unused)			
35		01: P.NAME	35		01: P.NAME			
36		01: P.NAME	36		01: P.NAME			
37		01: P.NAME	37		01: P.NAME			

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Pro	Profile number 15 (Cascade control with program patter				setting for 1 connected controller) on page 3				
	IN area			OUT area					
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word	Bit			Word	Bit				
position	position	Contents of assignment		position	position	Contents of assignment			
38		01: P.NAME		38		01: P.NAME			
39		01: P.NAME		39		01: P.NAME			
40		01: P.NAME		40		01: P.NAME			
41		01: P.NAME		41		01: P.NAME			
42		01: P.NAME		42		01: P.NAME			
43		01: P.NAME		43		01: P.NAME			
44		01: P.NAME		44		01: P.NAME			
45		01: P.NAME		45		01: P.NAME			
46		01: PTN.ERR		46		(Unused)			
47		(Unused)		47		(Unused)			
:		:				•			
•		•		•		•			
80		(Unused)		80		(Unused)			

Profile number 15 (Cascade control with program patte								
PF		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment			
-	position		-	position	_			
0	0	Receive data valid During-write	0	1	Rescan request (Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
				١.				
•		The fixed-part is omitted	•		The fixed-part is omitted			
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: PTNOC	5		01: PTNOC			
6		01: SEGNOC	6		01: SEGNOC			
7		01: TSP_L1	7		01: TSP_L1			
8		(Unused)	8		(Unused)			
9		01: TIME	9		01: TIME			
10		01: TM.RT	10		01: TM.RT			
11		01: S.PID	11		01: S.PID			
12		01: JC	12		01: JC			
13		01: PV.TY1	13		01: PV.TY1			
14		01: PV.EV1	14		01: PV.EV1			
15		01: PV.TY2	15		01: PV.TY2			
16		01: PV.EV2	16		01: PV.EV2			
17		01: PV.TY3	17		01: PV.TY3			
18		01: PV.EV3	18		01: PV.EV3			
19		01: PV.TY4	19		01: PV.TY4			
20		01: PV.EV4	20		01: PV.EV4			
21		01: PV.TY5	21		01: PV.TY5			
22		01: PV.EV5	22		01: PV.EV5			
23		01: PV.TY6	23		01: PV.TY6			
24		01: PV.EV6	24		01: PV.EV6			
25		01: PV.TY7	25		01: PV.TY7			
26		01: PV.EV7	26		01: PV.EV7			
27		01: PV.TY8	27		01: PV.TY8			
28		01: PV.EV8	28		01: PV.EV8			
29		01: TME1	29		01: TME1			
30		01: T.ON1	30		01: T.ON1			
31		01: T.OF1	31		01: T.OF1			
32		01: TME2	32		01: TME2			
33		01: T.ON2	33		01: T.ON2			
34		01: T.OF2	34		01: T.OF2			
35		01: TME3	35		01: TME3			
36		01: T.ON3	36		01: T.ON3			

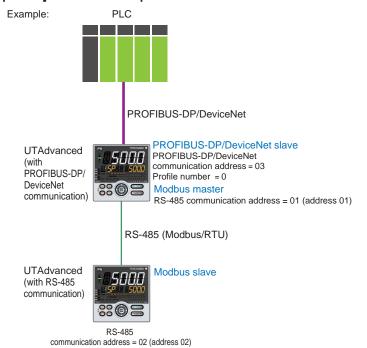
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Pro	Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 4 UN area							
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →			OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PR	OFIBUS-DP/DeviceNet master		DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		Vord sition	Bit position	Contents of assignment		
37		01: T.OF3		37		01: T.OF3		
38		01: TME4		38		01: TME4		
39		01: T.ON4		39		01: T.ON4		
40		01: T.OF4		40		01: T.OF4		
41		01: TME5		41		01: TME5		
42		01: T.ON5		42		01: T.ON5		
43		01: T.OF5		43		01: T.OF5		
44		01: TME6		44		01: TME6		
45		01: T.ON6		45		01: T.ON6		
46		01: T.OF6		46		01: T.OF6		
47		01: TME7		47		01: TME7		
48		01: T.ON7		48		01: T.ON7		
49		01: T.OF7		49		01: T.OF7		
50		01: TME8		50		01: TME8		
51		01: T.ON8		51		01: T.ON8		
52		01: T.OF8		52		01: T.OF8		
53		01: TME9		53		01: TME9		
54		01: T.ON9		54		01: T.ON9		
55		01: T.OF9		55		01: T.OF9		
56		01: TME10		56		01: TME10		
57		01: T.ON10		57		01: T.ON10		
58		01: T.OF10		58		01: T.OF10		
59		01: TME11		59		01: TME11		
60		01: T.ON11		60		01: T.ON11		
61		01: T.OF11		61		01: T.OF11		
62		01: TME12		62		01: TME12		
63		01: T.ON12		63		01: T.ON12		
64		01: T.OF12		64		01: T.OF12		
65		01: TME13		65		01: TME13		
66		01: T.ON13		66		01: T.ON13		
67		01: T.OF13		67		01: T.OF13		
68		01: TME14		68		01: TME14		
69		01: T.ON14	++	69		01: T.ON14		
70		01: T.OF14		70		01: T.OF14		
71		01: TME15		71		01: TME15		
72		01: T.ON15	++	72		01: T.ON15		
73		01: T.OF15		73		01: T.OF15		
74		01: TME16	++	74		01: TME16		
75		01: T.ON16		75		01: T.ON16		
76		01: T.OF16	↓	76		01: T.OF16		
77		01: PTN.ERR	_	77		(Unused)		
78		(Unused)		78		(Unused)		
79		(Unused)		79		(Unused)		
80		(Unused)		80		(Unused)		

3.9.3 Profile List for UT75A

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers])





Page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP DeviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)	1	5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
	8	(Reserved)		8	(Reserved)		
	9	(Reserved)		9	(Reserved)		
	10	(Reserved)		10	(Reserved)		
	11	(Reserved)		11	(Reserved)		
	12	(Reserved)		12	(Reserved)		
	13	(Reserved)		13	(Reserved)		
	14	(Reserved)		14	(Reserved)		
	15	(Reserved)		15	(Reserved)		
1	0	Normal connection slave (address 01)	1	0	Batch write request (address 01)		
	1	Normal connection slave (address 02)		1	Batch write request (address 02)		
	2	Normal connection slave (address 03)	1	2	Batch write request (address 03)		
	3	Normal connection slave (address 04)		3	Batch write request (address 04)		
	4	Normal connection slave (address 05)		4	Batch write request (address 05)		
	5	Normal connection slave (address 06)		5	Batch write request (address 06)		
	6	Normal connection slave (address 07)		6	Batch write request (address 07)		
	7	Normal connection slave (address 08)		7	Batch write request (address 08)		
	8	Normal connection slave (address 09)		8	Batch write request (address 09)		
	9	Normal connection slave (address 10)		9	Batch write request (address 10)		
	10	Normal connection slave (address 11)		10	Batch write request (address 11)		
	11	Normal connection slave (address 12)		11	Batch write request (address 12)		
	12	Normal connection slave (address 13)		12	Batch write request (address 13)		
	13	Normal connection slave (address 14)		13	Batch write request (address 14)		
	14	Normal connection slave (address 15)		14	Batch write request (address 15)		
	15	Normal connection slave (address 16)	1	15	Batch write request (address 16)		

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Profi	le numl	per 0 (User profile [initial value: simp	le PID con	trol with	
PR		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment
2	0	Normal connection slave (address 17)	2	0	Batch write request (address 17)
	1	Normal connection slave (address 18)		1	Batch write request (address 18)
	2	Normal connection slave (address 19)		2	Batch write request (address 19)
	3	Normal connection slave (address 20)		3	Batch write request (address 20)
	4	Normal connection slave (address 21)		4	Batch write request (address 21)
	5	Normal connection slave (address 22)		5	Batch write request (address 22)
	6 7	Normal connection slave (address 23) Normal connection slave (address 24)		7	Batch write request (address 23) Batch write request (address 24)
	8	Normal connection slave (address 24)		8	Batch write request (address 24)
	9	Normal connection slave (address 26)		9	Batch write request (address 25)
	10	Normal connection slave (address 27)		10	Batch write request (address 27)
	11	Normal connection slave (address 28)		11	Batch write request (address 28)
	12	Normal connection slave (address 29)		12	Batch write request (address 29)
	13	Normal connection slave (address 30)		13	Batch write request (address 30)
	14	Normal connection slave (address 31)		14	Batch write request (address 31)
	15	Normal connection slave (address 32)		15	Batch write request (address 32)
3		Current profile number	3		(Unused)
4		Current page	4		Page change request
5		01: PV_L1	5		(Unused)
6		02: PV_L1	6		(Unused)
7		01: CSP_L1	7		01: SP_L1_1
8		02: CSP_L1	8		02: SP_L1_1
9		01: OUT_L1	9		01: MOUT_L1
10		02: OUT_L1	10		02: MOUT_L1
11	1	01: A.M_L1 01: R.L_L1	11	1	01: A.M_L1 01: R.L_L1
	2	01: S.R_L1		2	01: S.R_L1
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	01: ALM1_L1		8	(Unused)
	9	01: ALM2_L1		9	(Unused)
	10	01: ALM3_L1		10	(Unused)
	11	01: ALM4_L1		11	(Unused)
	12 13	01: ALM5_L1 01: ALM6_L1		12	(Unused)
	14	01: ALM7_L1		14	(Unused)
	15	01: ALM7_L1 01: ALM8_L1		15	(Unused)
12	0	02: A.M_L1	12	0	02: A.M_L1
	1	02: R.L_L1		1	02: R.L_L1
	2	02: S.R_L1		2	02: S.R_L1
	3	(Unused)		3	(Unused)
	4	(Unused)		4	(Unused)
	5	(Unused)		5	(Unused)
	6	(Unused)		6	(Unused)
	7	(Unused)		7	(Unused)
	8	02: ALM1_L1		8	(Unused)
	9	02: ALM2_L1		9	(Unused)
	10	02: ALM3_L1		10	(Unused)
	11 12	02: ALM4_L1 02: ALM5_L1		11	(Unused)
	13	02: ALM6_L1		13	(Unused)
	14	02: ALM7_L1		14	(Unused)
	15	02: ALM8_L1		15	(Unused)
				1 10	1(0000)

Profi	ile numl	ber 0 (User profile [initial value: simpl	e PID cont	rol with	2 connected controllers]) on page 2
		IN area			OUT area
PF		-DP/DeviceNet slave (UTAdvanced) →	PRO		DP/DeviceNet master → PROFIBUS-DP/
		OFIBUS-DP/DeviceNet master			eviceNet slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		02: P_L1_1	6		02: P_L1_1
7		01: I_L1_1	7		01: I_L1_1
8		02: I_L1_1	8		02: I_L1_1
9		01: D_L1_1	9		01: D_L1_1
10		02: D_L1_1	10		02: D_L1_1
11		01: SPNO.	11		01: SPNO.
12		02: SPNO.	12		02: SPNO.

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Page 3

PR	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master		PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP DeviceNet slave (UTAdvanced)			
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: Pc_L1_1	5		01: Pc_L1_1		
6		02: Pc_L1_1	6		02: Pc_L1_1		
7		01: lc_L1_1	7		01: lc_L1_1		
8		02: lc_L1_1	8		02: lc_L1_1		
9		01: Dc_L1_1	9		01: Dc_L1_1		
10		02: Dc_L1_1	10		02: Dc_L1_1		
11		01: SPNO.	11		01: SPNO.		
12		02: SPNO.	12		02: SPNO.		

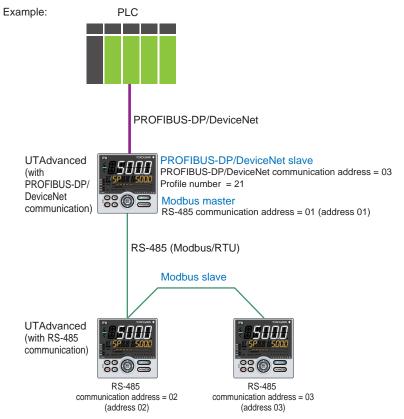
Profi	ile numl	per 0 (User profile [initial value: simple	e PID cont	rol with	2 connected controllers]) on page 4			
		IN area			OUT area			
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →			PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PROFIBUS-DP/DeviceNet master			DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: A1_L1_1	5		01: A1_L1_1			
6		02: A1_L1_1	6		02: A1_L1_1			
7		01: A2_L1_1	7		01: A2_L1_1			
8		02: A2_L1_1	8		02: A2_L1_1			
9		01: A3_L1_1	9		01: A3_L1_1			
10		02: A3_L1_1	10		02: A3_L1_1			
11		01: A4_L1_1	11		01: A4_L1_1			
12		02: A4_L1_1	12		02: A4_L1_1			

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Profile number 21 (Simple PID control with 3 connected controllers)





Page 1

IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master		PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
0	0	Receive data valid	0	0	Rescan request	
	1	During-write		1	(Reserved)	
	2	Write acknowledgement		2	Write request	
	3	(Reserved)		3	(Reserved)	
	4	(Reserved)		4	(Reserved)	
	5	(Reserved)		5	(Reserved)	
	6	(Reserved)		6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)	
4		Current page	4		Page change request	
5		01: PV_L1	5		(Unused)	
6		02: PV_L1	6		(Unused)	
7		03: PV_L1	7		(Unused)	
8		01: CSP_L1	8		01: SP_L1_1	
9		02: CSP_L1	9		02: SP_L1_1	
10		03: CSP_L1	10		03: SP_L1_1	
11		01: OUT_L1	11		01: MOUT_L1	
12		02: OUT_L1	12		02: MOUT_L1	

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Pro	ofile nu	mber 21 (Simple PID control with	130	connec	ted co	ntrollers) on page 1		
		IN area		OUT area				
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
13		03: OUT_L1		13		03: MOUT_L1		
14		01: H.OUT_L1		14		01: MOUT_L1		
15		02: H.OUT_L1		15		02: MOUT_L1		
16		03: H.OUT_L1		16		03: MOUT_L1		
17		01: C.OUT_L1		17		01: MOUTc_L1		
18		02: C.OUT_L1		18		02: MOUTc_L1		
19		03: C.OUT_L1		19		03: MOUTc_L1		
20	0	01: A.M_L1		20	0	01: A.M_L1		
	1	01: R.L_L1			1	01: R.L_L1		
	2	01: S.R_L1			2	01: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	7	(Unused)			6 7	(Unused)		
	8	01: ALM1_L1			8	(Unused)		
	9	01: ALM2_L1			9	(Unused)		
	10	01: ALM3_L1			10	(Unused)		
	11	01: ALM4_L1			11	(Unused)		
	12	01: ALM5_L1			12	(Unused)		
	13	01: ALM6_L1			13	(Unused)		
	14	01: ALM7_L1			14	(Unused)		
	15	01: ALM8_L1			15	(Unused)		
21	0	02: A.M_L1		21	0	02: A.M_L1		
	1	02: R.L_L1			1	02: R.L_L1		
	2	02: S.R_L1	1		2	02: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	02: ALM1_L1			8	(Unused)		
	9	02: ALM2_L1			9	(Unused)		
	10	02: ALM3_L1			10	(Unused)		
	11	02: ALM4_L1			11	(Unused)		
	12	02: ALM5_L1			12	(Unused)		
	13	02: ALM6_L1	-		13	(Unused)		
	14	02: ALM7_L1	-		14	(Unused)		
22	15 0	02: ALM8_L1 03: A.M_L1	-	22	15 0	(Unused) 03: A.M_L1		
22	1	03: R.L_L1	1	22	1	03: R.L_L1		
	2	03: S.R_L1			2	03: S.R_L1		
	3	(Unused)	1		3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)	1		6	(Unused)		
	7	(Unused)	1		7	(Unused)		
	8	03: ALM1_L1	1		8	(Unused)		
	9	03: ALM2_L1	1		9	(Unused)		
	10	03: ALM3_L1	1		10	(Unused)		
	11	03: ALM4_L1	1		11	(Unused)		
	12	03: ALM5_L1			12	(Unused)		
	13	03: ALM6_L1			13	(Unused)		
	14	03: ALM7_L1			14	(Unused)		
	15	03: ALM8_L1			15	(Unused)		

Pro	ofile nu	mber 21 (Simple PID control with	3 connec	ted co	ntrollers) on page 2			
		IN area		OUT area				
		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
		The fixed-part is omitted			The fixed-part is omitted			
		(See profile number 0 on page 1)			(See profile number 0 on page 1)			
·			,	·				
4		Current page	4		Page change request			
5		01: P_L1_1	5		01: P_L1_1			
6		02: P_L1_1	6		02: P_L1_1			
7		03: P_L1_1	7		03: P_L1_1			
8		01: I_L1_1	8		01: I_L1_1			
9		02: I_L1_1	9		02: I_L1_1			
10		03: I_L1_1	10		03: I_L1_1			
11		01: D_L1_1	11		01: D_L1_1			
12		02: D_L1_1	12		02: D_L1_1			
13		03: D_L1_1	13		03: D_L1_1			
14		01: SPNO.	14		01: SPNO.			
15		02: SPNO.	15		02: SPNO.			
16		03: SPNO.	16		03: SPNO.			
17		(Unused)	17		(Unused)			
18		(Unused)	18		(Unused)			
19		(Unused)	19		(Unused)			
20		(Unused)	20		(Unused)			
21		(Unused)	21		(Unused)			
22		(Unused)	22		(Unused)			

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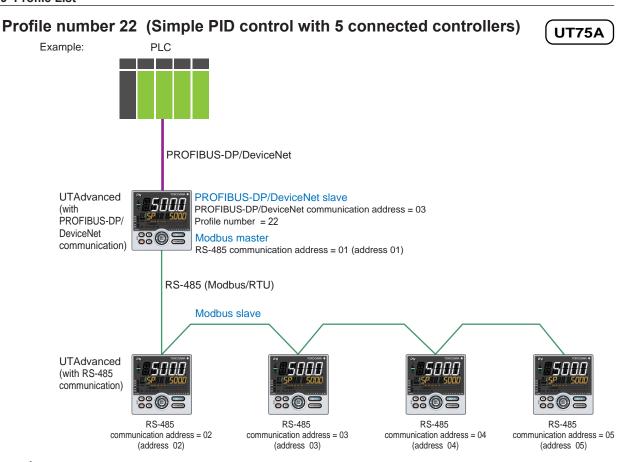
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	PR	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
0	0	Receive data valid	0	0	Rescan request	
	1	During-write		1	(Reserved)	
	2	Write acknowledgement		2	Write request	
	3	(Reserved)		3	(Reserved)	
	4	(Reserved)		4	(Reserved)	
	5	(Reserved)		5	(Reserved)	
	6	(Reserved)		6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
		The fixed part is emitted	•		The fixed part is emitted	
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted	
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)	
4		Current page	4		Page change request	
5		01: Pc_L1_1	5		01: Pc_L1_1	
6		02: Pc_L1_1	6		02: Pc_L1_1	
7		03: Pc_L1_1	7		03: Pc_L1_1	
8		01: lc_L1_1	8		01: lc_L1_1	
9		02: lc_L1_1	9		02: lc_L1_1	
10		03: lc_L1_1	10		03: lc_L1_1	
11		01: Dc_L1_1	11		01: Dc_L1_1	
12		02: Dc_L1_1	12		02: Dc_L1_1	
13		03: Dc_L1_1	13		03: Dc_L1_1	
14		01: SPNO.	14		01: SPNO.	
15		02: SPNO.	15		02: SPNO.	
16		03: SPNO.	16		03: SPNO.	
17		(Unused)	17		(Unused)	
18		(Unused)	18		(Unused)	
19		(Unused)	19		(Unused)	
20		(Unused)	20		(Unused)	
21		(Unused)	21		(Unused)	
22		(Unused)	22		(Unused)	

Pro	ofile nu	mber 21 (Simple PID control with	3 connec	ted co	ntrollers) on page 4
		IN area			OUT area
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write]	1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
		The fixed-part is omitted			The fixed-part is omitted
		(See profile number 0 on page 1)			(See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: A1_L1_1	5		01: A1_L1_1
6		02: A1_L1_1	6		02: A1_L1_1
7		03: A1_L1_1	7		03: A1_L1_1
8		01: A2_L1_1	8		01: A2_L1_1
9		02: A2_L1_1	9		02: A2_L1_1
10		03: A2_L1_1	10		03: A2_L1_1
11		01: A3_L1_1	11		01: A3_L1_1
12		02: A3_L1_1	12		02: A3_L1_1
13		03: A3_L1_1	13		03: A3_L1_1
15		01: A4_L1_1 02: A4_L1_1	15		01: A4_L1_1 02: A4_L1_1
16		02: A4_L1_1 03: A4_L1_1	16		02: A4_L1_1 03: A4_L1_1
17		01: A5_L1_1	17		01: A5_L1_1
18		02: A5_L1_1	18		02: A5_L1_1
19		03: A5_L1_1	19		03: A5_L1_1
20		(Unused)	20		(Unused)
21		(Unused)	21	-	(Unused)
22		(Unused)	22		(Unused)

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Page 1

		mber 22 (Simple PID control with			OUT area			
DE	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →		DDC	PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PROFIBUS-DP/DeviceNet master				eviceNet slave (UTAdvanced)			
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: PV_L1	5		(Unused)			
6		02: PV_L1	6		(Unused)			
7		03: PV_L1	7		(Unused)			
8		04: PV_L1	8		(Unused)			
9		05: PV_L1	9		(Unused)			
10		01: CSP_L1	10		01: SP_L1_1			
11		02: CSP_L1	11		02: SP_L1_1			

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Profile number 22 (Simple PID control with								
P		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		OUT area DP/DeviceNet master → PROFI eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignn			
12		03: CSP_L1	12		03: SP_L1_1			
13		04: CSP_L1	13		04: SP_L1_1			
14		05: CSP_L1	14		05: SP_L1_1			
15		01: OUT_L1	15		01: MOUT_L1			
16		02: OUT_L1	16		02: MOUT_L1			
17		03: OUT_L1	17		03: MOUT_L1			
18		04: OUT_L1	18		04: MOUT_L1			
19		05: OUT_L1	19		05: MOUT_L1			
20		01: H.OUT_L1	20		01: MOUT_L1			
21		02: H.OUT L1	21		02: MOUT_L1			
		_			_			
22		03: H.OUT_L1	22		03: MOUT_L1			
23		04: H.OUT_L1	23		04: MOUT_L1			
24		05: H.OUT_L1	24		05: MOUT_L1			
25		01: C.OUT_L1	25		01: MOUTc_L1			
26		02: C.OUT_L1	26		02: MOUTc_L1			
27		03: C.OUT_L1	27		03: MOUTc_L1			
28		04: C.OUT_L1	28		04: MOUTc_L1			
29		05: C.OUT_L1	29		05: MOUTc_L1			
30	0	01: A.M_L1	30	0	01: A.M_L1			
	1	01: R.L_L1		1	01: R.L_L1			
	2	01: S.R_L1	_	2	01: S.R_L1			
	3	(Unused)		3	(Unused)			
	4	(Unused)		4	(Unused)			
	5	(Unused)	_	5	(Unused)			
	6	(Unused)	_	6	(Unused)			
	7	(Unused)	⊣	7	(Unused)			
	8	01: ALM1_L1		8	(Unused)			
	9	01: ALM2_L1		9	(Unused)			
	10	01: ALM3_L1		10	(Unused)			
	11	01: ALM4_L1	7	11	(Unused)			
	12	01: ALM5_L1		12	(Unused)			
	13	01: ALM6_L1	-	13	(Unused)			
	14	01: ALM7_L1	-	14	(Unused)			
	15	01: ALM8_L1	\dashv	15	(Unused)			
24	_							
31	0	02: A.M_L1	31	0	02: A.M_L1			
	1	02: R.L_L1	⊣	1	02: R.L_L1			
	2	02: S.R_L1		2	02: S.R_L1			
	3	(Unused)		3	(Unused)			
	4	(Unused)	7	4	(Unused)			
	5	(Unused)	-	5	(Unused)			
	6	(Unused)		6	(Unused)			
			-		, , , , , , , , , , , , , , , , , , ,			
	7	(Unused)	⊣	7	(Unused)			
	8	02: ALM1_L1	⊣	8	(Unused)			
	9	02: ALM2_L1		9	(Unused)			
	10	02: ALM3_L1		10	(Unused)			
	11	02: ALM4_L1		11	(Unused)			
	12	02: ALM5_L1	_	12	(Unused)			
		02: ALM6 L1		1.3	(Unused)			
	13	02: ALM6_L1		13	(Unused)			
		02: ALM6_L1 02: ALM7_L1 02: ALM8_L1		13 14 15	(Unused) (Unused)			

Pro	ofile nu	mber 22 (Simple PID control with	5	5 connected controllers) on page 1				
	IN area			OUT area				
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
32	0	03: A.M_L1		32	0	03: A.M_L1		
	1	03: R.L_L1			1	03: R.L_L1		
	2	03: S.R_L1			2	03: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	03: ALM1_L1			8	(Unused)		
	9	03: ALM2_L1			9	(Unused)		
	10	03: ALM3_L1			10	(Unused)		
	11	03: ALM4_L1			11	(Unused)		
	12	03: ALM5_L1			12	(Unused)		
	13	03: ALM6_L1			13	(Unused)		
	14	03: ALM7_L1			14	(Unused)		
	15	03: ALM8_L1			15	(Unused)		
33	0	04: A.M_L1		33	0	04: A.M_L1		
	1	04: R.L_L1			1	04: R.L_L1		
	2	04: S.R_L1			2	04: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	04: ALM1_L1			8	(Unused)		
	9	04: ALM2_L1			9	(Unused)		
	10	04: ALM3_L1			10	(Unused)		
	11	04: ALM4_L1			11	(Unused)		
	12	04: ALM5_L1			12	(Unused)		
	13	04: ALM6_L1			13	(Unused)		
	14	04: ALM7_L1			14	(Unused)		
	15	04: ALM8_L1			15	(Unused)		
34	0	05: A.M_L1		34	0	05: A.M_L1		
	1	05: R.L_L1			1	05: R.L_L1		
	2	05: S.R_L1			2	05: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	05: ALM1_L1			8	(Unused)		
	9	05: ALM2_L1			9	(Unused)		
	10	05: ALM3_L1			10	(Unused)		
	11	05: ALM4_L1			11	(Unused)		
	12	05: ALM5_L1			12	(Unused)		
	13	05: ALM6_L1			13	(Unused)		
	14	05: ALM7_L1			14	(Unused)		
	15	05: ALM8_L1			15	(Unused)		

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Pro	ofile nu	mber 22 (Simple PID control with	5 connec	connected controllers) on page 2					
		IN area		OUT area					
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment				
0	0	Receive data valid	0	0	Rescan request				
	1	During-write		1	(Reserved)				
	2	Write acknowledgement		2	Write request				
	3	(Reserved)		3	(Reserved)				
	5	(Reserved)		5	(Reserved)				
	6	(Reserved)		6	(Reserved)				
	7	(Reserved)		7	(Reserved)				
		,							
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted				
•	•	(See profile number 0 on page 1)	•		(See profile number 0 on page 1)				
•	•	(coo prome number o on page 1)	•	•	(coo promo nambor o en pago 1)				
4		Current page	4		Page change request				
5		01: P_L1_1	5		01: P_L1_1				
6		02: P_L1_1	6		02: P_L1_1				
7		03: P_L1_1	7		03: P_L1_1				
8		04: P_L1_1	8		04: P_L1_1				
9		05: P_L1_1	9		05: P_L1_1				
10		01: I_L1_1	10		01: I_L1_1				
11		02: I_L1_1	11		02: I_L1_1				
12		03: I_L1_1	12		03: I_L1_1				
13		04: I_L1_1	13		04: I_L1_1				
14		05: I_L1_1	14		05: I_L1_1				
15		01: D_L1_1	15		01: D_L1_1				
16		02: D_L1_1	16		02: D_L1_1				
17		03: D_L1_1	17		03: D_L1_1				
18		04: D_L1_1	18		04: D_L1_1				
19		05: D_L1_1	19		05: D_L1_1				
20		01: SPNO.	20		01: SPNO.				
21		02: SPNO.	21		02: SPNO.				
22		03: SPNO.	22		03: SPNO.				
23		04: SPNO.	23		04: SPNO.				
24		05: SPNO.	24		05: SPNO.				
25		(Unused)	25	-	(Unused)				
26 27		(Unused)	26 27		(Unused)				
28		(Unused)	28		(Unused)				
29		(Unused)	29		(Unused)				
30		(Unused)	30		(Unused)				
31		(Unused)	31		(Unused)				
32		(Unused)	32	-	(Unused)				
33		(Unused)	33	-	(Unused)				
34		(Unused)	34		(Unused)				

Pro	Profile number 22 (Simple PID control with 5 connected controllers) on page 3							
PR		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted			
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)			
•	•	(**************************************	•	•	(111)			
4		Current page	4		Page change request			
5		01: Pc_L1_1	5		01: Pc_L1_1			
6		02: Pc_L1_1	6		02: Pc_L1_1			
7		03: Pc_L1_1	7		03: Pc_L1_1			
8		04: Pc_L1_1	8		04: Pc_L1_1			
9		05: Pc_L1_1	9		05: Pc_L1_1			
10		01: lc_L1_1	10		01: lc_L1_1			
11		02: lc_L1_1	11		02: lc_L1_1			
12		03: lc_L1_1	12		03: lc_L1_1			
13		04: lc_L1_1	13		04: lc_L1_1			
14		05: lc_L1_1	14		05: lc_L1_1			
15		01: Dc_L1_1	15		01: Dc_L1_1			
16		02: Dc_L1_1	16		02: Dc_L1_1			
17		03: Dc_L1_1	17		03: Dc_L1_1			
18		04: Dc_L1_1	18		04: Dc_L1_1			
19		05: Dc_L1_1	19		05: Dc_L1_1			
20		01: SPNO.	20		01: SPNO.			
21		02: SPNO.	21		02: SPNO.			
22		03: SPNO.	22		03: SPNO.			
23		04: SPNO.	23		04: SPNO.			
24		05: SPNO.	24		05: SPNO.			
25 26		(Unused)	25 26	-	(Unused)			
27		(Unused)	27		(Unused)			
28		(Unused)	28		(Unused)			
29		(Unused)	29		(Unused)			
30		(Unused)	30		(Unused)			
31		(Unused)	31		(Unused)			
32		(Unused)	32		(Unused)			
33		(Unused)	33		(Unused)			
34		(Unused)	34		(Unused)			

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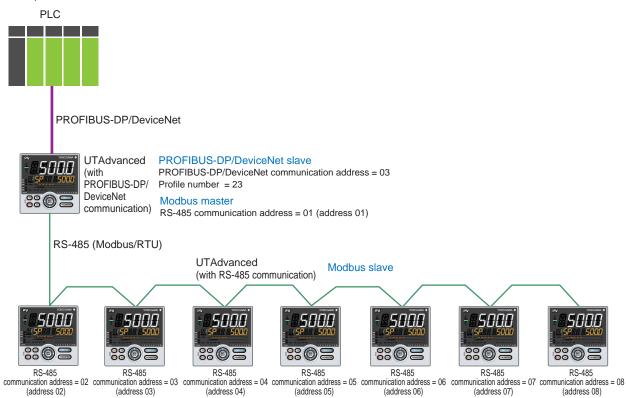
Page 4

Pro	ofile nu	mber 22 (Simple PID control with	5 connected controllers) on page 4					
	ROFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OFIBUS-E	OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment			
position 0	position 0	Receive data valid	position 0	position 0				
0	1	During-write	0	1	Rescan request (Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	7	(Reserved)		7	(Reserved)			
:	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: A1_L1_1	5		01: A1_L1_1			
6		02: A1_L1_1	6		02: A1_L1_1			
7		03: A1_L1_1	7		03: A1_L1_1			
8		04: A1_L1_1	8		04: A1_L1_1			
9		05: A1_L1_1	9		05: A1_L1_1			
10		01: A2_L1_1	10		01: A2_L1_1			
11		02: A2_L1_1	11		02: A2_L1_1			
12		03: A2_L1_1	12		03: A2_L1_1			
13		04: A2_L1_1	13		04: A2_L1_1			
14		05: A2_L1_1	14		05: A2_L1_1			
15		01: A3_L1_1	15		01: A3_L1_1			
16		02: A3_L1_1	16		02: A3_L1_1			
17		03: A3_L1_1	17		03: A3_L1_1			
18		04: A3_L1_1	18		04: A3_L1_1			
19		05: A3_L1_1	19		05: A3_L1_1			
20		01: A4_L1_1	20		01: A4_L1_1			
21		02: A4_L1_1	21		02: A4_L1_1			
22		03: A4_L1_1	22		03: A4_L1_1			
23		04: A4_L1_1	23		04: A4_L1_1			
24		05: A4_L1_1	24		05: A4_L1_1			
25		01: A5_L1_1	25		01: A5_L1_1			
26		02: A5_L1_1	26		02: A5_L1_1			
27		03: A5_L1_1	27		03: A5_L1_1			
28		04: A5_L1_1	28		04: A5_L1_1			
29		05: A5_L1_1	29		05: A5_L1_1			
30		(Unused)	30		(Unused)			
31		(Unused)	31		(Unused)			
32		(Unused)	32		(Unused)			
33		(Unused)	33		(Unused)			
34		(Unused)	34		(Unused)			

Profile number 23 (Simple PID control with 8 connected controllers)







Page 1

IN area				connected controllers) on page 1 OUT area			
PROFIBUS-DP/DeviceNet slave (UTAdvanœd) → PROFIBUS-			PRO	OFIBUS-I	OP/DeviceNet master → PROFIBUS-DP/		
		DP/DeviceNet master		De	eviceNet slave (UTAdvanced)		
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	6	(Reserved)		6	(Reserved)		
	7	(Reserved)		7	(Reserved)		
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: PV_L1	5		(Unused)		
6		02: PV_L1	6		(Unused)		
7		03: PV_L1	7		(Unused)		
8		04: PV_L1	8		(Unused)		
9		05: PV_L1	9		(Unused)		
10		06: PV_L1	10		(Unused)		
11		07: PV L1	11		(Unused)		

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IN area PROFIBUS-DP/DeviceNet slave (UTAdvanœd) → PROFIBUS- DP/DeviceNet master						
Word	Bit	Contents of assignment				
position 12	position	08: PV_L1				
13		01: CSP_L1				
14		02: CSP_L1				
15		03: CSP_L1				
16		04: CSP_L1				
17		05: CSP_L1				
18		06: CSP_L1				
19		07: CSP_L1				
20		08: CSP_L1				
21		01: OUT_L1				
22		02: OUT_L1				
23		03: OUT_L1				
24		04: OUT_L1				
25		05: OUT_L1				
26		06: OUT_L1				
27		07: OUT_L1				
28		08: OUT_L1				
29		01: H.OUT_L1				
30		02: H.OUT_L1				
31		03: H.OUT_L1				
32		04: H.OUT_L1				
33		05: H.OUT_L1				
34		06: H.OUT_L1				
35		07: H.OUT_L1				
36		08: H.OUT_L1				
37		01: C.OUT_L1				
38		02: C.OUT_L1				
39		03: C.OUT_L1				
40		04: C.OUT_L1				
41		05: C.OUT_L1				
42		06: C.OUT_L1				
43		07: C.OUT_L1				
44		08: C.OUT_L1				

Profile number 23 (Simple PID control with 8 connected controllers) on page 1

PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)						
Word	Bit position	Contents of assignment					
12	ρυσιτιυπ	(Unused)					
13		01: SP_L1_1					
14		02: SP_L1_1					
15		03: SP_L1_1					
16		04: SP_L1_1					
17		05: SP_L1_1					
18		06: SP_L1_1					
19		07: SP_L1_1					
20		08: SP_L1_1					
21		01: MOUT_L1					
22		02: MOUT_L1					
23		03: MOUT_L1					
24		04: MOUT_L1					
25		05: MOUT_L1					
26		06: MOUT_L1					
27		07: MOUT_L1					
28		08: MOUT_L1					
29		01: MOUT_L1					
30		02: MOUT_L1					
31		03: MOUT_L1					
32		04: MOUT_L1					
33		05: MOUT_L1					
34		06: MOUT_L1					
35		07: MOUT_L1					
36		08: MOUT_L1					
37		01: MOUTc_L1					
38		02: MOUTc_L1					
39		03: MOUTc_L1					
40		04: MOUTc_L1					
41		05: MOUTc_L1					
42		06: MOUTc_L1					
43		07: MOUTc_L1					
44		08: MOUTc_L1					

_			_					
Pro	ofile nu	mber 23 (Simple PID control with						
		IN area		OUT area				
PROFIE	PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-			PROFIBUS-DP/DeviceNet master → PROFIBUS-D				
1 1101 12	DP/DeviceNet master					eviceNet slave (UTAdvanced)		
Word	Bit	Di /Devicertet master		Word	Bit	Price (o i Advanced)		
		Contents of assignment				Contents of assignment		
•	position	3			position	_		
45	0	01: A.M_L1		45	0	01: A.M_L1		
	1	01: R.L_L1			1	01: R.L_L1		
	2	01: S.R_L1			2	01: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
						,		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	01: ALM1_L1			8	(Unused)		
	9	01: ALM2_L1			9	(Unused)		
	10	01: ALM3_L1			10	(Unused)		
	11	01: ALM4_L1			11	(Unused)		
	12				12	(Unused)		
		01: ALM5_L1				,		
	13	01: ALM6_L1			13	(Unused)		
	14	01: ALM7_L1			14	(Unused)		
	15	01: ALM8_L1			15	(Unused)		
46	0	02: A.M_L1		46	0	02: A.M_L1		
	1	02: R.L_L1			1	02: R.L_L1		
	2	02: S.R_L1			2	02: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
		,				()		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	02: ALM1_L1			8	(Unused)		
	9	02: ALM2_L1			9	(Unused)		
	10	02: ALM3_L1			10	(Unused)		
	11	02: ALM4_L1			11	(Unused)		
	12	02: ALM5_L1			12	(Unused)		
	13	02: ALM6_L1			13	(Unused)		
	14				14	(Unused)		
		02: ALM7_L1				,		
47	15	02: ALM8_L1		47	15	(Unused)		
47	0	03: A.M_L1		47	0	03: A.M_L1		
	1	03: R.L_L1			1	03: R.L_L1		
	2	03: S.R_L1			2	03: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
		03: ALM1_L1				(Unused)		
	8				8	,		
	9	03: ALM2_L1			9	(Unused)		
	10	03: ALM3_L1			10	(Unused)		
	11	03: ALM4_L1			11	(Unused)		
	12	03: ALM5_L1			12	(Unused)		
	13	03: ALM6_L1			13	(Unused)		
	14	03: ALM7_L1			14	(Unused)		
	15	03: ALM8 L1			15	(Unused)		
48	0	04: A.M_L1		48	0	04: A.M_L1		
	1	04: R.L_L1			1	04: R.L_L1		
	2	04: S.R_L1			2	04: S.R_L1		
	3	(Unused)			3	(Unused)		
	4	(Unused)			4	(Unused)		
	5	(Unused)			5	(Unused)		
	6	(Unused)			6	(Unused)		
	7	(Unused)			7	(Unused)		
	8	04: ALM1_L1			8	(Unused)		
	9	04: ALM2_L1			9	(Unused)		
	10	04: ALM3_L1			10	(Unused)		
	11					(Unused)		
	_	04: ALM4_L1			11	,		
	12	04: ALM5_L1			12	(Unused)		
	13	04: ALM6_L1			13	(Unused)		
	14	04: ALM7_L1			14	(Unused)		
	15	04: ALM8_L1			15	(Unused)		

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Pro	Profile number 23 (Simple PID control with 8 connected controllers) on page 1							
		IN area			OUT area			
PROFIE	BUS-DP/E	DeviceNet slave (UTAdvanced) → PROFIBUS-	P	$\textbf{PROFIBUS-DP/DeviceNet master} \rightarrow \textbf{PROFIBUS-DP/}$				
	DP/DeviceNet master			_	eviceNet slave (UTAdvanced)			
Word	Bit	Contents of assignment	Wor		Contents of assignment			
•	position		position		1			
49	0	05: A.M_L1	49	0	05: A.M_L1			
	1	05: R.L_L1		1	05: R.L_L1			
	2	05: S.R_L1		2	05: S.R_L1			
	3	(Unused)		3	(Unused)			
	5	(Unused)		5	(Unused)			
	6	(Unused)		6	(Unused)			
	7	(Unused)		7	(Unused)			
	8	05: ALM1_L1		8	(Unused)			
	9	05: ALM2_L1		9	(Unused)			
	10	05: ALM3_L1		10	(Unused)			
	11	05: ALM4_L1		11	(Unused)			
	12	05: ALM5_L1		12	(Unused)			
	13	05: ALM6_L1		13	(Unused)			
	14	05: ALM7_L1		14	(Unused)			
	15	05: ALM8_L1		15	(Unused)			
50	0	06: A.M_L1	50	0	06: A.M_L1			
	1	06: R.L_L1		1	06: R.L_L1			
	2	06: S.R_L1		2	06: S.R_L1			
	3	(Unused)		3	(Unused)			
	4	(Unused)		4	(Unused)			
	5 6	(Unused)		5 6	(Unused)			
	7	(Unused)		7	(Unused)			
	8	06: ALM1_L1		8	(Unused)			
	9	06: ALM2_L1		9	(Unused)			
	10	06: ALM3_L1		10	(Unused)			
	11	06: ALM4_L1		11	(Unused)			
	12	06: ALM5_L1		12	(Unused)			
	13	06: ALM6_L1		13	(Unused)			
	14	06: ALM7_L1		14	(Unused)			
	15	06: ALM8_L1		15	(Unused)			
51	0	07: A.M_L1	51	0	07: A.M_L1			
	1	07: R.L_L1		1	07: R.L_L1			
	2	07: S.R_L1		2	07: S.R_L1			
	3	(Unused)		3	(Unused)			
	5	(Unused)		5	(Unused)			
	6	(Unused)		6	(Unused)			
	7	(Unused)		7	(Unused)			
		07: ALM1_L1		8	(Unused)			
	9	07: ALM2_L1		9	(Unused)			
	10	07: ALM3_L1		10	(Unused)			
	11	07: ALM4_L1		11	(Unused)			
	12	07: ALM5_L1		12	(Unused)			
	13	07: ALM6_L1		13	(Unused)			
		07: ALM7_L1		14	(Unused)			
- 50	15	07: ALM8_L1	50	15	(Unused)			
52	0	08: A.M_L1	52	0	08: A.M_L1			
	1	08: R.L_L1		1	08: R.L_L1			
	2	08: S.R_L1		2	08: S.R_L1			
	3	(Unused)		3	(Unused)			
	5	(Unused)		5	(Unused)			
	6	(Unused)		6	(Unused)			
	7	(Unused)		7	(Unused)			
	8	08: ALM1_L1		8	(Unused)			
	9	08: ALM2_L1		9	(Unused)			
	10	08: ALM3_L1		10	(Unused)			
	11	08: ALM4_L1		11	(Unused)			
	12	08: ALM5_L1		12	(Unused)			
		08: ALM6_L1		13	(Unused)			
	14	08: ALM7_L1		14	(Unused)			
1	15	08: ALM8_L1		15	(Unused)			

Pro	ofile nu	mber 23 (Simple PID control with	8 connec	ted co	ntrollers) on page 2
	ROFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OFIBUS-E	OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment
position	position 0	Receive data valid	position 0	position 0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
		The fixed-part is omitted			The fixed-part is omitted
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)
4		Current page	4		Page change request
5		01: P_L1_1	5		01: P_L1_1
6		02: P_L1_1	6		02: P_L1_1
7		03: P_L1_1	7		03: P_L1_1
8		04: P_L1_1	8		04: P_L1_1
9		05: P_L1_1	9		05: P_L1_1
10		06: P_L1_1	10		06: P_L1_1
11		07: P_L1_1	11		07: P_L1_1
12		08: P_L1_1	12		08: P_L1_1
13		01: I_L1_1	13		01: I_L1_1
14		02: I_L1_1	14		02: I_L1_1
15		03: I_L1_1 04: I_L1_1	15		03: I_L1_1
16		05: I_L1_1	16		04: I_L1_1 05: I_L1_1
18		06: I_L1_1	18		06: I_L1_1
19		07: I_L1_1	19		07: I_L1_1
20		08: I_L1_1	20		08: I_L1_1
21		01: D_L1_1	21		01: D_L1_1
22		02: D_L1_1	22		02: D_L1_1
23		03: D_L1_1	23		03: D_L1_1
24		04: D_L1_1	24		04: D_L1_1
25		05: D_L1_1	25		05: D_L1_1
26		06: D_L1_1	26		06: D_L1_1
27		07: D_L1_1	27		07: D_L1_1
28		08: D_L1_1	28		08: D_L1_1
29		01: SPNO.	29		01: SPNO.
30		02: SPNO.	30		02: SPNO.
31		03: SPNO.	31		03: SPNO.

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Pro	ofile nu	ntrollers) on page 2						
	IN area			OUT area				
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →			PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PR	OFIBUS-DP/DeviceNet master			De	eviceNet slave (UTAdvanced)		
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment		
	position				position			
32		04: SPNO.		32		04: SPNO.		
33		05: SPNO.		33		05: SPNO.		
34		06: SPNO.		34		06: SPNO.		
35		07: SPNO.		35		07: SPNO.		
36		08: SPNO.		36		08: SPNO.		
37		(Unused)		45		(Unused)		
38		(Unused)		46		(Unused)		
39		(Unused)		47		(Unused)		
40		(Unused)		48		(Unused)		
41		(Unused)		49		(Unused)		
42		(Unused)		50		(Unused)		
43		(Unused)		51		(Unused)		
44		(Unused)		52		(Unused)		
45		(Unused)		53		(Unused)		
46		(Unused)		46		(Unused)		
47		(Unused)		47		(Unused)		
48		(Unused)		48		(Unused)		
49		(Unused)		49		(Unused)		
50		(Unused)		50		(Unused)		
51		(Unused)		51		(Unused)		
52		(Unused)		52		(Unused)		

Pro	file nu	mber 23 (Simple PID control with	8 connec	ted co	ntrollers) on page 3	
	OFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master		OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)		
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment	
position 0	position 0	Receive data valid	position	position 0	Rescan request	
	1	During-write		1	(Reserved)	
	2	Write acknowledgement		2	Write request	
	3	(Reserved)		3	(Reserved)	
	<u>4</u> 5	(Reserved)		5	(Reserved)	
	6	(Reserved)		6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
	•	The fixed-part is omitted	•	•	The fixed-part is omitted	
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)	
4		Current page	4		Page change request	
5		01: Pc_L1_1	5		01: Pc_L1_1	
6		02: Pc_L1_1	6		02: Pc_L1_1	
7		03: Pc_L1_1	7		03: Pc_L1_1	
8		04: Pc_L1_1	8		04: Pc_L1_1	
9		05: Pc_L1_1	9		05: Pc_L1_1	
10		06: Pc_L1_1	10		06: Pc_L1_1	
11		07: Pc_L1_1	11		07: Pc_L1_1	
12		08: Pc_L1_1	12		08: Pc_L1_1	
13		01: lc_L1_1	13		01: lc_L1_1	
14		02: lc_L1_1	14		02: lc_L1_1	
15		03: lc_L1_1	15		03: lc_L1_1	
16		04: lc_L1_1	16		04: lc_L1_1	
17		05: lc_L1_1	17		05: lc_L1_1	
18		06: lc_L1_1	18		06: lc_L1_1	
19		07: lc_L1_1	19		07: lc_L1_1	
20		08: lc_L1_1	20		08: lc_L1_1	
21		01: Dc_L1_1	21		01: Dc_L1_1	
22		02: Dc_L1_1	22		02: Dc_L1_1	
23		03: Dc_L1_1	23		03: Dc_L1_1	
24		04: Dc_L1_1	24		04: Dc_L1_1	
25		05: Dc_L1_1	25		05: Dc_L1_1	
26		06: Dc_L1_1	26		06: Dc_L1_1	
27		07: Dc_L1_1	27		07: Dc_L1_1	
28		08: Dc_L1_1	28		08: Dc_L1_1	
29		01: SPNO.	29		01: SPNO.	
30		02: SPNO.	30		02: SPNO.	
31		03: SPNO.	31		03: SPNO.	

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Pro	Profile number 23 (Simple PID control with 8 connected controllers) on page 3							
		IN area		OUT area				
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →			PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
	PR	OFIBUS-DP/DeviceNet master			De	eviceNet slave (UTAdvanced)		
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment		
position	position	Contents of assignment		position	position	Contents of assignment		
32		04: SPNO.		32		04: SPNO.		
33		05: SPNO.		33		05: SPNO.		
34		06: SPNO.		34		06: SPNO.		
35		07: SPNO.		35		07: SPNO.		
36		08: SPNO.		36		08: SPNO.		
37		(Unused)		37		(Unused)		
38		(Unused)		38		(Unused)		
39		(Unused)		39		(Unused)		
40		(Unused)		40		(Unused)		
41		(Unused)		41		(Unused)		
42		(Unused)		42		(Unused)		
43		(Unused)		43		(Unused)		
44		(Unused)		44		(Unused)		
45		(Unused)		45		(Unused)		
46		(Unused)		46		(Unused)		
47		(Unused)		47		(Unused)		
48		(Unused)		48		(Unused)		
49		(Unused)		49		(Unused)		
50		(Unused)		50		(Unused)		
51		(Unused)		51		(Unused)		
52		(Unused)		52		(Unused)		

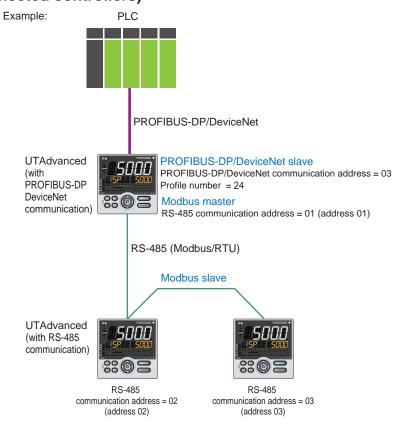
Pro	ofile nu	mber 23 (Simple PID control with	8 connec	ted co	ntrollers) on page 4
	ROFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) →		OFIBUS-E	OUT area DP/DeviceNet master → PROFIBUS-DP/
Word	PR:	OFIBUS-DP/DeviceNet master	Word	Bit	eviceNet slave (UTAdvanced)
	position	Contents of assignment		position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	2	During-write		1	(Reserved) Write request
	3	Write acknowledgement (Reserved)		3	(Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	7	(Reserved)		7	(Reserved)
	,	(Nessived)		,	(Nobel Vou)
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)
•	•		•	•	
4		Current page	4		Page change request
5		01: A1_L1_1	5		01: A1_L1_1
6		02: A1_L1_1	6		02: A1_L1_1
7		03: A1_L1_1	7		03: A1_L1_1
8		04: A1_L1_1	8		04: A1_L1_1
9		05: A1_L1_1	9		05: A1_L1_1
10		06: A1_L1_1	10		06: A1_L1_1
11		07: A1_L1_1	11		07: A1_L1_1
12		08: A1_L1_1	12		08: A1_L1_1
13		01: A2_L1_1	13		01: A2_L1_1
14		02: A2_L1_1	14		02: A2_L1_1
15		03: A2_L1_1	15		03: A2_L1_1
16		04: A2_L1_1	16		04: A2_L1_1
17		05: A2_L1_1	17		05: A2_L1_1
18		06: A2_L1_1	18		06: A2_L1_1
19		07: A2_L1_1	19		07: A2_L1_1
20		08: A2_L1_1	20		08: A2_L1_1
21		01: A3_L1_1	21		01: A3_L1_1
22		02: A3_L1_1	22		02: A3_L1_1
23		03: A3_L1_1	23		03: A3_L1_1
24		04: A3_L1_1	24		04: A3_L1_1
25		05: A3_L1_1	25		05: A3_L1_1
26		06: A3_L1_1	26		06: A3_L1_1
27		07: A3_L1_1	27		07: A3_L1_1
28		08: A3_L1_1	28		08: A3_L1_1
29		01: A4_L1_1	29		01: A4_L1_1
30		02: A4_L1_1	30		02: A4_L1_1
31		03: A4_L1_1	31		03: A4_L1_1
32		04: A4_L1_1	32		04: A4_L1_1
33		05: A4_L1_1	33		05: A4_L1_1

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Pro	ofile nu	mber 23 (Simple PID control with	ted co	ntrollers) on page 4				
	IN area			OUT area				
PF	PROFIBUS-DP/DeviceNet slave (UTAdvanced) →			PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/				
		OFIBUS-DP/DeviceNet master				eviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
34		06: A4_L1_1		34		06: A4_L1_1		
35		07: A4_L1_1		35		07: A4_L1_1		
36		08: A4_L1_1		36		08: A4_L1_1		
37		01: A5_L1_1		37		01: A5_L1_1		
38		02: A5_L1_1		38		02: A5_L1_1		
39		03: A5_L1_1		39		03: A5_L1_1		
40		04: A5_L1_1		40		04: A5_L1_1		
41		05: A5_L1_1		41		05: A5_L1_1		
42		06: A5_L1_1		42		06: A5_L1_1		
43		07: A5_L1_1		43		07: A5_L1_1		
44		08: A5_L1_1		44		08: A5_L1_1		
45		(Unused)		45		(Unused)		
46		(Unused)		46		(Unused)		
47		(Unused)		47		(Unused)		
48		(Unused)		48		(Unused)		
49		(Unused)		49		(Unused)		
50		(Unused)		50		(Unused)		
51		(Unused)		51		(Unused)		
52		(Unused)		52		(Unused)		

Profile number 24 (Cascade control / Dual-loop control with 3 connected controllers)





Page 1

Pro	Profile number 24 (Cascade control / Dual-loop control with 3 connected controllers) on page 1							
		IN area		OUT area				
PF		-DP/DeviceNet slave (UTAdvanced) →	PRO		DP/DeviceNet master → PROFIBUS-DP/			
	1	OFIBUS-DP/DeviceNet master			eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)		6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)			
4		Current page	4		Page change request			
5		01: PV_L1	5		(Unused)			
6		02: PV_L1	6		(Unused)			
7		03: PV_L1	7		(Unused)			
8		01: PV_L2	8		(Unused)			
9		02: PV_L2	9		(Unused)			
10		03: PV_L2	10		(Unused)			
11		01: CSP_L1	11		01: SP_L1_1			
					1			

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		IN area -DP/DeviceNet slave (UTAdvanced) →			
PROFIBUS-DP/DeviceNet master					
Word	Bit	Contents of assignment			
position 12	position	02: CSP_L1			
12		luz. CSF_L1			
13		03: CSP_L1			
14		01: CSP_L2			
14		01. 001 _L2			
15		02: CSP_L2			
16		03: CSP_L2			
		00.001			
17		01: C.A.M			
18		02: C.A.M			
19		03: C.A.M			
20		01: OUT_L1			
21		02: OUT_L1			
22		03: OUT_L1			
23		01: OUT_L2			
24		02: OUT_L2			
25		03: OUT_L2			
26		(Unused)			
27		(Unused)			
28		(Unused)			
29	0	01:A.M_L1			
	1	01: R.L_L1			
	3	01: S.R_L1 (Unused)			
	4	(Unused)			
	5	(Unused)			
	6	(Unused)			
	7	(Unused)			
	8	01: ALM1_L1			
	9 10	01: ALM2_L1 01: ALM3_L1			
	11	01: ALM3_L1 01: ALM4_L1			
	12	(Unused)			
	13	(Unused)			
	14	(Unused)			
20	15	(Unused)			
30	1	01:A.M_L2 01: R.L_L2			
	2	01: S.R_L2			
	3	(Unused)			
	4	(Unused)			
	5	(Unused)			
	6	(Unused)			
	7 8	(Unused) 01: ALM1_L2			
	9	01: ALM1_L2 01: ALM2_L2			
	10	01: ALM3_L2			
	11	01: ALM4_L2			
	12	(Unused)			
	13	(Unused)			
	14	(Unused)			
	15	(Unused)			

Profile number 24 (Cascade control / Dual-loop control with 3 connected controllers) on page 1

PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/					
1100	DeviceNet slave (UTAdvanced)					
Word position	Bit position	Contents of assignment				
12		02: SP_L1_1				
13		03: SP_L1_1				
14		01: SP_L2_1				
15		02: SP_L2_1				
16		03: SP_L2_1				
17		01: C.A.M				
18		02: C.A.M				
19		03: C.A.M				
20		01: MOUT_L1				
21		02: MOUT_L1				
22		03: MOUT_L1				
23		01: MOUT_L2				
24		02: MOUT_L2				
25		03: MOUT_L2				
26		(Unused)				
27		(Unused)				
28 29	0	(Unused) 01:A.M_L1				
29	1	01: R.L_L1				
	2	01: S.R_L1				
	3	(Unused)				
	4	(Unused)				
	5	(Unused)				
	6	(Unused)				
	7	(Unused)				
	8	(Unused)				
	9 10	(Unused)				
	11	(Unused)				
	12	(Unused)				
	13	(Unused)				
	14	(Unused)				
	15	(Unused)				
30	0	01:A.M_L2				
	1	01: R.L_L2				
	2	01: S.R_L2				
	3	(Unused)				
	5	(Unused)				
	6	(Unused)				
	7	(Unused)				
	8	(Unused)				
	9	(Unused)				
	10	(Unused)				
	11	(Unused)				
	12	(Unused)				
	13 14	(Unused)				
	15	(Unused)				
	1.0	(0.10000)				

Pro	Profile number 24 (Cascade control / Dual-loop control with 3 connected controllers) on page					
PR		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)	
Word	Bit position	Contents of assignment	Word	Bit	Contents of assignment	
31	0	02:A.M_L1	31	0	02:A.M_L1	
	1	02: R.L_L1		1	02: R.L_L1	
	3	02: S.R_L1 (Unused)	-	3	02: S.R_L1 (Unused)	
	4	(Unused)	1	4	(Unused)	
	5	(Unused)]	5	(Unused)	
	7	(Unused)		7	(Unused)	
	8	02: ALM1_L1	1	8	(Unused)	
	9	02: ALM2_L1]	9	(Unused)	
	10 11	02: ALM3_L1 02: ALM4_L1		10	(Unused)	
	12	(Unused)	1	12	(Unused)	
	13	(Unused)]	13	(Unused)	
	14	(Unused)		14	(Unused)	
32	15 0	(Unused) 02:A.M_L2	32	15 0	(Unused) 02:A.M_L2	
-	1	02: R.L_L2] 5	1	02: R.L_L2	
	2	02: S.R_L2	-	2	02: S.R_L2	
	3	(Unused)	-	3	(Unused)	
	5	(Unused)	1	5	(Unused)	
	6	(Unused)]	6	(Unused)	
	7 8	(Unused) 02: ALM1_L2		7 8	(Unused)	
	9	02: ALM2_L2	1	9	(Unused)	
	10	02: ALM3_L2]	10	(Unused)	
	11 12	02: ALM4_L2 (Unused)		11	(Unused)	
	13	(Unused)	1	13	(Unused)	
	14	(Unused)]	14	(Unused)	
33	15 0	(Unused)	33	15 0	(Unused)	
33	1	03:A.M_L1 03: R.L_L1	33	1	03:A.M_L1 03: R.L_L1	
	2	03: S.R_L1]	2	03: S.R_L1	
	3	(Unused)		3	(Unused)	
	5	(Unused)	-	5	(Unused)	
	6	(Unused)]	6	(Unused)	
	7	(Unused)		7	(Unused)	
	8 9	03: ALM1_L1 03: ALM2_L1	1	8	(Unused)	
	10	03: ALM3_L1]	10	(Unused)	
	11	03: ALM4_L1	4	11	(Unused)	
	12 13	(Unused)	-	12	(Unused)	
	14	(Unused)]	14	(Unused)	
	15	(Unused)		15	(Unused)	
34	1	03:A.M_L2 03: R.L_L2	34	1	03: A.M_L2 03: R.L_L2	
	2	03: S.R_L2	1	2	03: S.R_L2	
	3	(Unused)]	3	(Unused)	
	<u>4</u> 5	(Unused)	-	5	(Unused)	
	6	(Unused)		6	(Unused)	
	7	(Unused)]	7	(Unused)	
	8 9	03: ALM1_L2 03: ALM2_L2	-	8	(Unused)	
	10	03: ALM2_L2 03: ALM3_L2		10	(Unused)	
	11	03: ALM4_L2]	11	(Unused)	
	12	(Unused)	-	12	(Unused)	
	13 14	(Unused)		13 14	(Unused)	
	15	(Unused)		15	(Unused)	

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Page 2

Profile number 24 (Cascade control / Dual-loop control with 3 connected controllers) on page 2						
PF		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment	
position 0	position 0	Receive data valid	position 0	position 0	ŭ	
U	1	During-write	0	1	Rescan request (Reserved)	
	2	Write acknowledgement		2	Write request	
	3	(Reserved)		3	(Reserved)	
	4	(Reserved)		4	(Reserved)	
	5 6	(Reserved)		5 6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted	
•		(See profile number 0 on page 1)	•	:	(See profile number 0 on page 1)	
·	ľ		•			
4		Current page	4		Page change request	
5		01: P_L1_1	5		01: P_L1_1	
6		02: P_L1_1	6		02: P_L1_1	
7		03: P_L1_1	7		03: P_L1_1	
8		01: I_L1_1	8		01: I_L1_1	
9		02: I_L1_1	9		02: I_L1_1	
10		03: I_L1_1	10		03: I_L1_1	
11		01: D_L1_1	11		01: D_L1_1	
12		02: D_L1_1	12		02: D_L1_1	
13		03: D_L1_1	13		03: D_L1_1	
14		01: SPNO.	14		01: SPNO.	
15		02: SPNO.	15		02: SPNO.	
16		03: SPNO.	16		03: SPNO.	
17		01: P_L2_1	17		01: P_L2_1	
18		02: P_L2_1	18		02: P_L2_1	
19		03: P_L2_1	19		03: P_L2_1	
20		01: I_L2_1	20		01: I_L2_1	
21		02: I_L2_1	21		02: I_L2_1	
22		03: I_L2_1 01: D_L2_1	22		03: I_L2_1 01: D_L2_1	
23		02: D_L2_1	24		01: D_L2_1 	
25		03: D_L2_1	25		03: D_L2_1	
26 27		(Unused)	26 27		(Unused)	
28		(Unused)	28		(Unused)	
29		(Unused)	29		(Unused)	
30		(Unused)	30		(Unused)	
31		(Unused)	31	-	(Unused)	
32		(Unused)	32		(Unused)	
34		(Unused)	34		(Unused)	

Pro	Profile number 24 (Cascade control / Dual-loop control with 3 connected controllers) on page 3 IN area OUT area							
PR		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
0	0	Receive data valid	0	0	Rescan request			
	1	During-write		1	(Reserved)			
	2	Write acknowledgement		2	Write request			
	3	(Reserved)		3	(Reserved)			
	4	(Reserved)		4	(Reserved)			
	5	(Reserved)		5	(Reserved)			
	6	(Reserved)	1	6	(Reserved)			
	7	(Reserved)		7	(Reserved)			
			1					
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted			
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)			
•	•	(Gee profile number of our page 1)	•	•	(Gee prome number of on page 1)			
4		Current page	4		Page change request			
5		(Unused)	5		(Unused)			
6		(Unused)	6		(Unused)			
7		(Unused)	7		(Unused)			
8		(Unused)	8		(Unused)			
9		(Unused)	9		(Unused)			
10		(Unused)	10		(Unused)			
11		(Unused)	11		(Unused)			
12		(Unused)	12		(Unused)			
13		(Unused)	13		(Unused)			
14		(Unused)	14		(Unused)			
15		(Unused)	15		(Unused)			
16		(Unused)	16		(Unused)			
17		(Unused)	17		(Unused)			
18		(Unused)	18		(Unused)			
19		(Unused)	19		(Unused)			
20		(Unused)	20		(Unused)			
21		(Unused)	21		(Unused)			
22		(Unused)	22		(Unused)			
23		(Unused)	23		(Unused)			
24		(Unused)	24		(Unused)			
25		(Unused)	25		(Unused)			
26		(Unused)	26		(Unused)			
27		(Unused)	27		(Unused)			
28		(Unused)	28	<u> </u>	(Unused)			
29		(Unused)	29		(Unused)			
30		(Unused)	30		(Unused)			
31		(Unused)	31	-	(Unused)			
		7	4 	 				
		,			,			
		,	-	 	,			
32 33 34		(Unused) (Unused) (Unused)	32 33 34		(Unused) (Unused) (Unused)			

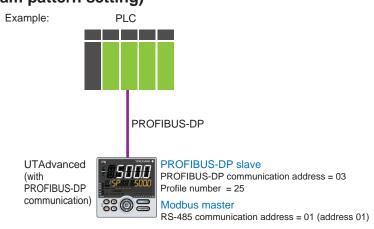
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Page 4

Pro	Profile number 24 (Cascade control / Dual-loop control with 3 connected controllers) on page 4						
		IN area	OUT area				
PF		-DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PR		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)		
Word	Bit		Word	Bit			
position	position	Contents of assignment	position	position	Contents of assignment		
0	0	Receive data valid	0	0	Rescan request		
	1	During-write		1	(Reserved)		
	2	Write acknowledgement		2	Write request		
	3	(Reserved)		3	(Reserved)		
	4	(Reserved)		4	(Reserved)		
	5	(Reserved)		5	(Reserved)		
	7	(Reserved)		7	(Reserved)		
	/	(Reserved)		/	(Reserved)		
				١.			
		The fixed-part is omitted			The fixed-part is omitted		
		(See profile number 0 on page 1)			(See profile number 0 on page 1)		
4		Current page	4		Page change request		
5		01: A1_L1_1	5		01: A1_L1_1		
6		02: A1_L1_1	6		02: A1_L1_1		
7		03: A1_L1_1	7		03: A1_L1_1		
8		01: A2_L1_1	8		01: A2_L1_1		
9		02: A2_L1_1	9		02: A2_L1_1		
10		03: A2_L1_1	10		03: A2_L1_1		
11		01: A3_L1_1	11		01: A3_L1_1		
12		02: A3_L1_1	12		02: A3_L1_1		
13		03: A3_L1_1	13		03: A3_L1_1		
14		01: A4_L1_1	14		01: A4_L1_1		
15		02: A4_L1_1	15		02: A4_L1_1		
16		03: A4_L1_1 (Unused)	16		03: A4_L1_1 (Unused)		
18		(Unused)	18		(Unused)		
19		(Unused)	19		(Unused)		
20		01: A1_L2_1	20		01: A1_L2_1		
21		02: A1_L2_1	21		02: A1_L2_1		
22		03: A1_L2_1	22		03: A1_L2_1		
23		01: A2_L2_1	23		01: A2_L2_1		
24		02: A2_L2_1	24		02: A2_L2_1		
25		03: A2_L2_1	25		03: A2_L2_1		
26		01: A3_L2_1	26		01: A3_L2_1		
27		02: A3_L2_1	27		02: A3_L2_1		
28		03: A3_L2_1	28		03: A3_L2_1		
29		01: A4_L2_1	29		01: A4_L2_1		
30		02: A4_L2_1	30		02: A4_L2_1		
31		03: A4_L2_1	31		03: A4_L2_1		
32		(Unused)	32		(Unused)		
33		(Unused)	33		(Unused)		
34		(Unused)	34		(Unused)		

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller, with program pattern setting)





Page 1

PF	ROFIBUS	IN area -DP/DeviceNet slave (UTAdvanced) →	PR	OFIBUS-I	OUT area DP/DeviceNet master → PROFIBUS-DP/	
		OFIBUS-DP/DeviceNet master			eviceNet slave (UTAdvanced)	
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
0	0	Receive data valid	0	0	Rescan request	
	1	During-write		1	(Reserved)	
	2	Write acknowledgement		2	Write request	
	3	(Reserved)		3	(Reserved)	
	4	(Reserved)		4	(Reserved)	
	5	(Reserved)		5	(Reserved)	
	6	(Reserved)		6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
•	•	The fixed-part is omitted (See profile number 0 on page 1)	•	•	The fixed-part is omitted (See profile number 0 on page 1)	
4		Current page	4		Page change request	
5		01: PV_L1	5		(Unused)	
6		01: PV_L2	6		(Unused)	
7		01: CSP_L1	7		01: SP_L1_1	
8		01: CSP_L2	8		01: SP_L2_1	
9		01: C.A.M	9		01: C.A.M	
10		01: OUT_L1	10		01: MOUT_L1	
11		01: OUT_L2	11		01: MOUT_L2	
12		01: H.OUT_L1	12		01: MOUT_L1	
13		01: H.OUT_L2	13		01: MOUT_L2	
14		01: C.OUT_L1	14		01: MOUTc_L1	
15		01: C.OUT_L2	15		01: MOUTc_L2	
16		01:SEG.N	16		(Unused)	
17		01:SEG_RTIME	17		(Unused)	

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Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 1

PF	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master				
Word	Bit position	Contents of assignment			
18	position	01:REM_RCY			
19		01:SEG_MAX			
20		(Unused)			
21		(Unused)			
22		(Unused)			
23		(Unused)			
24 25		(Unused)			
26		(Unused)			
27		(Unused)			
28		(Unused)			
29		(Unused)			
30		(Unused)			
31		(Unused)			
32		(Unused)			
33		(Unused)			
34 35		(Unused)			
36		(Unused)			
37		(Unused)			
38		(Unused)			
39		(Unused)			
40		(Unused)			
41		(Unused)			
42		(Unused)			
43		(Unused)			
44	0	(Unused)			
45	1	01: A.M_L1 01: R.L_L1			
	2	01: S.R_L1			
	3	01:HOLD			
	4	(Unused)			
	5	01:WAITF			
	6	01:PROG			
	7 8	(Unused) 01: ALM1_L1			
	9	01: ALW1_L1			
	10	01: ALM3_L1			
	11	01: ALM4_L1			
	12	01: ALM5_L1			
	13	01: ALM6_L1			
	14	01: ALM7_L1			
46	15 0	01: ALM8_L1 01: A.M_L2			
.0	1	01: R.L_L2			
	2	01: S.R_L2			
	3	(Unused)			
	4	(Unused)			
	5	(Unused)			
	7	(Unused)			
	8	01: ALM1_L2			
	9	01: ALM2_L2			
	10	01: ALM3_L2			
	11	01: ALM4_L2			
	12	01: ALM5_L2			
	13 14	01: ALM6_L2 01: ALM7_L2			
	15	01: ALM/_L2 01: ALM8_L2			

	OUT area					
PRO	PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/					
		eviceNet slave (UTAdvanced)				
Word	Bit	Contents of assignment				
	position					
18		(Unused)				
19		(Unused)				
		(6.1353)				
20		(Unused)				
21		(Unused)				
22		(Unused)				
23		(Unused)				
24		(Unused)				
25		(Unused)				
26		(Unused)				
27		(Unused)				
28		(Unused)				
30		(Unused)				
31						
32		(Unused)				
33		(Unused)				
34		(Unused)				
35		(Unused)				
36		(Unused)				
37		(Unused)				
38		(Unused)				
39		(Unused)				
40		(Unused)				
41		(Unused)				
42		(Unused)				
43		(Unused)				
44		(Unused)				
45	0	01: A.M_L1				
	1	01: R.L_L1				
	2	01: S.R_L1				
	3	01:HOLD				
	4	01:ADV				
	5	(Unused) 01:PROG				
	7	(Unused)				
	8	(Unused)				
	9	(Unused)				
	10	(Unused)				
	11	(Unused)				
	12	(Unused)				
	13	(Unused)				
	14	(Unused)				
4.0	15	(Unused)				
46	0	01: A.M_L2				
	2	01: R.L_L2 01: S.R_L2				
	3	(Unused)				
	4	(Unused)				
	5	(Unused)				
	6	(Unused)				
	7	(Unused)				
	8	(Unused)				
	9	(Unused)				
	10	(Unused)				
	11	(Unused)				
	12 13	(Unused)				
	14	(Unused)				
	15	(Unused)				
	1 10	Nonasouj				

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 1

	IN area					
PR	OFIBUS	-DP/DeviceNet slave (UTAdvanced) →				
	PROFIBUS-DP/DeviceNet master					
Word	Bit	Contents of assignment				
position	position	Contents of assignment				
47	0	(Unused)				
	1	(Unused)				
	2	(Unused)				
	3	(Unused)				
	4	(Unused)				
	5	(Unused)				
	6	(Unused)				
	7 8	(Unused)				
	9	(Unused)				
	10	(Unused)				
	11	(Unused)				
	12	(Unused)				
	13	(Unused)				
	14	(Unused)				
	15	(Unused)				
48	0	(Unused)				
	1	(Unused)				
	2	(Unused)				
	3	(Unused)				
	4	(Unused)				
	5	(Unused)				
	6	(Unused)				
	7	(Unused)				
	8	(Unused)				
	9	(Unused)				
	10	(Unused)				
	11	(Unused)				
	12	(Unused)				
	13 14	(Unused)				
	15	(Unused)				
49	0	(Unused)				
45	1	(Unused)				
	2	(Unused)				
	3	(Unused)				
	4	(Unused)				
	5	(Unused)				
	6	(Unused)				
	7	(Unused)				
	8	(Unused)				
	9	(Unused)				
	10	(Unused)				
	11	(Unused)				
	12	(Unused)				
	13	(Unused)				
	14	(Unused)				
FO	15	(Unused)				
50	1	(Unused)				
	2	(Unused)				
	3	(Unused)				
	4	(Unused)				
	5	(Unused)				
	6	(Unused)				
	7	(Unused)				
	8	(Unused)				
	9	(Unused)				
	10	(Unused)				
	11	(Unused)				
	12	(Unused)				
	13	(Unused)				
	14	(Unused)				
	15	(Unused)				

		eviceNet slave (UTAdvanced)
Word	Bit	Contents of assignment
oosition 47	position 0	(Unused)
47	1	(Unused)
	2	(Unused)
	3	(Unused)
	4	(Unused)
	5	(Unused)
	6	(Unused)
	7	(Unused)
	8	(Unused)
	9	(Unused)
	10 11	(Unused)
	12	(Unused)
	13	(Unused)
	14	(Unused)
	15	(Unused)
48	0	(Unused)
	1	(Unused)
	2	(Unused)
	3 4	(Unused)
	5	(Unused)
	6	(Unused)
	7	(Unused)
	8	(Unused)
	9	(Unused)
	10	(Unused)
	11	(Unused)
	12	(Unused)
	13	(Unused)
	14 15	(Unused)
49	0	(Unused)
.0	1	(Unused)
	2	(Unused)
	3	(Unused)
	4	(Unused)
	5	(Unused)
	6	(Unused)
	7 8	(Unused)
	9	(Unused)
	10	(Unused)
	11	(Unused)
	12	(Unused)
	13	(Unused)
	14	(Unused)
F0	15	(Unused)
50	1	(Unused)
	2	(Unused)
	3	(Unused)
	4	(Unused)
	5	(Unused)
	6	(Unused)
	7	(Unused)
	8	(Unused)
	9	(Unused)
	10	(Unused)
	11	(Unused)
	12 13	(Unused)
	14	(Unused)
	15	11

OUT area

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connected controller) on page 1 IN area **OUT** area $\textbf{PROFIBUS-DP/DeviceNet master} \rightarrow \textbf{PROFIBUS-DP/}$ PROFIBUS-DP/DeviceNet slave (UTAdvanced) \rightarrow PROFIBUS-DP/DeviceNet master DeviceNet slave (UTAdvanced) Word Word Bit Contents of assignment Contents of assignment position position position position (Unused) (Unused) 51 0 51 0 (Unused) (Unused) (Unused) 2 (Unused) 2 (Unused) 3 (Unused) 3 4 (Unused) 4 (Unused) (Unused) 5 (Unused) 6 6 (Unused) (Unused) (Unused) 7 (Unused) 8 8 (Unused) (Unused) 9 (Unused) (Unused) 9 10 (Unused) 10 (Unused) 11 (Unused) 11 (Unused) 12 (Unused) 12 (Unused) (Unused) 13 (Unused) 13 14 (Unused) 14 (Unused) 15 (Unused) 15 (Unused) 52 52 (Unused) 0 (Unused) Ω 1 (Unused) 1 (Unused) (Unused) (Unused) 3 3 (Unused) (Unused) 4 (Unused) 4 (Unused) 5 (Unused) 5 (Unused) (Unused) 6 (Unused) 6 7 (Unused) 7 (Unused) 8 (Unused) 8 (Unused) 9 (Unused) 9 (Unused) 10 (Unused) 10 (Unused) 11 (Unused) 11 (Unused) 12 (Unused) (Unused) 13 (Unused) 13 (Unused) 14 (Unused) 14 (Unused) 15 (Unused) 15 (Unused) 0 (Unused) 0 (Unused) 1 (Unused) 1 (Unused) 2 (Unused) 2 (Unused) 3 (Unused) 3 (Unused) (Unused) 4 (Unused) 4 5 (Unused) 5 (Unused) (Unused) (Unused) 7 (Unused) (Unused) 8 (Unused) 8 (Unused) (Unused) (Unused) 10 10 (Unused) (Unused) 11 (Unused) 11 (Unused) 12 (Unused) 12 (Unused) 13 (Unused) 13 (Unused) 14 (Unused) 14 (Unused) 15 (Unused) 15 (Unused) 0 (Unused) 0 (Unused) 1 (Unused) 1 (Unused) 2 (Unused) 2 (Unused) (Unused) 3 (Unused) (Unused) (Unused) 4 4 5 (Unused) 5 (Unused) 6 (Unused) 6 (Unused) (Unused) (Unused) 8 (Unused) 8 (Unused) 9 (Unused) 9 (Unused) (Unused) 10 (Unused) 10 11 (Unused) 11 (Unused) 12 (Unused) 12 (Unused) 13 (Unused) 13 (Unused) 14 (Unused) 14 (Unused)

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1

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15

(Unused)

15

(Unused)

connected controller) on page 2 IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			PRO	OUT area PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
0	0	Receive data valid	0	0	Rescan request	
	1	During-write		1	(Reserved)	
	3	Write acknowledgement (Reserved)		3	Write request (Reserved)	
	4	(Reserved)		4	(Reserved)	
	5	(Reserved)		5	(Reserved)	
	6	(Reserved)		6	(Reserved)	
	7	(Reserved)		7	(Reserved)	
•	•	The fixed-part is omitted	•		The fixed-part is omitted	
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)	
4		Current page	4		Page change request	
5		01: P_L1_1	5		01: P_L1_1	
6		01: I_L1_1	6		01: I_L1_1	
7		01: D_L1_1	7		01: D_L1_1	
8		01: P_L2_1	8		01: P_L2_1	
9		01: I_L2_1	9		01: I_L2_1	
10		01: D_L2_1	10		01: D_L2_1	
11		01: Pc_L1_1	11		01: Pc_L1_1	
12		01: lc_L1_1	12		01: lc_L1_1	
13		01: Dc_L1_1	13		01: Dc_L1_1	
14		01: Pc_L2_1	14		01: Pc_L2_1	
15		01: lc_L2_1	15		01: lc_L2_1	
16		01: Dc_L2_1	16		01: Dc_L2_1	
17		01: SPNO.	17		01: SPNO.	
19		01: A1_L1_1 01: A2_L1_1	19		01: A1_L1_1 01: A2_L1_1	
20		01: A3_L1_1	20		01: A3_L1_1	
21		01: A4_L1_1	21		01: A4_L1_1	
22		01: A5_L1_1	22		01: A5_L1_1	
23		01: A6_L1_1	23		01: A6_L1_1	
24		01: A7_L1_1	24		01: A7_L1_1	
25		01: A8_L1_1	25		01: A8_L1_1	
26		01: A1_L2_1	26		01: A1_L2_1	
27		01: A2_L2_1	27		01: A2_L2_1	
28		01: A3_L2_1	28		01: A3_L2_1	
29		01: A4_L2_1	29		01: A4_L2_1	
30		01: A5_L2_1	30		01: A5_L2_1	
31		01: A6_L2_1	31		01: A6_L2_1	
32		01: A7_L2_1	32		01: A7_L2_1	
33		01: A8_L2_1	33		01: A8_L2_1	

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Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 2

PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master			
Word	Bit	Contents of assignment	
position	position	Contents of assignment	
34		(Unused)	
35		(Unused)	
36		(Unused)	
37		(Unused)	
38		(Unused)	
39		(Unused)	
40		(Unused)	
41		(Unused)	
42		(Unused)	
43		(Unused)	
44		(Unused)	
45		(Unused)	
46		(Unused)	
47		(Unused)	
48		(Unused)	
49		(Unused)	
50		(Unused)	
51		(Unused)	
52		(Unused)	
53		(Unused)	
54		(Unused)	

PROFIBUS-DP/DeviceNet master → PROFIBUS-DP/ DeviceNet slave (UTAdvanced)			
Word	Bit	Comtanto of accimumant	
position	position	Contents of assignment	
34		(Unused)	
35		(Unused)	
36		(Unused)	
37		(Unused)	
38		(Unused)	
39		(Unused)	
40		(Unused)	
41		(Unused)	
42		(Unused)	
43		(Unused)	
44		(Unused)	
45		(Unused)	
46		(Unused)	
47		(Unused)	
48		(Unused)	
49		(Unused)	
50		(Unused)	
51		(Unused)	
52		(Unused)	
53		(Unused)	
54		(Unused)	

OUT area

Page 3

		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
	Bit position		Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	2	Write acknowledgement		2	Write request
	3	(Reserved)		3	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
•					
		The fixed-part is omitted			The fixed-part is omitted
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)
4		Current page	4		Page change request
5		01:STC	5		01:STC
6		01:SSP_L1	6		01:SSP_L1
7		01:SSP_L2	7		01:SSP_L2
8		01:JC	8		01:JC
9		01:WT.SW1	9		01:WT.SW1
10		01:WZ.UP1	10		01:WZ.UP1
11		01:WZ.LO1	11		01:WZ.LO1
12		01:WT.SW2	12		01:WT.SW2
13		01:WZ.UP2	13		01:WZ.UP2
14		01:WZ.LO2	14		01:WZ.LO2
15		01:R.CYC	15		01:R.CYC
16		01:TSP_L1_1	16		01:TSP_L1_1
17		01:TSP_L2_1 01:TIME_1	17		01:TSP_L2_1 01:TIME_1
18		_	18		
19 20		01:S.PID_L1_1 01:S.PID_L2_1	19		01:S.PID_L1_1 01:S.PID_L2_1
21		01:TSP_L1_2	21		01:TSP_L1_2
22		01:TSP_L2_2	22		01:TSP_L2_2
23		01:TIME_2	23		01:TIME_2
24		01:S.PID_L1_2	24		01:S.PID_L1_2
25		01:S.PID_L2_2	25		01:S.PID_L2_2
26		01:TSP_L1_3	26		01:TSP_L1_3
27		01:TSP_L2_3	27		01:TSP_L2_3
28		01:TIME_3	28		01:TIME_3
29		01:S.PID_L1_3	29		01:S.PID_L1_3
30		01:S.PID_L2_3	30		01:S.PID_L2_3
31		01:TSP_L1_4	31		01:TSP_L1_4
32		01:TSP_L2_4	32		01:TSP_L2_4
33		01:TIME_4	33		01:TIME_4

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Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 3

PF	IN area PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master					
Word	Bit position	Contents of assignment				
34	pooliion	01:S.PID_L1_4				
35		01:S.PID_L2_4				
36		01:TSP_L1_5				
37		01:TSP_L2_5				
38		01:TIME_5				
39		01:S.PID_L1_5				
40		01:S.PID_L2_5				
41		01:TSP_L1_6				
42		01:TSP_L2_6				
43		01:TIME_6				
44		01:S.PID_L1_6				
45		01:S.PID_L2_6				
46		01:TSP_L1_7				
47		01:TSP_L2_7				
48		01:TIME_7				
49		01:S.PID_L1_7				
50		01:S.PID_L2_7				
51		01:TSP_L1_8				
52		01:TSP_L2_8				
53		01:TIME_8				
54		01:S.PID_L1_8				
55		01:S.PID_L2_8				
56		01:TSP_L1_9				
57		01:TSP_L2_9				
58		01:TIME_9				
59		01:S.PID_L1_9				
60		01:S.PID_L2_9				
61		01:TSP_L1_10				
62		01:TSP_L2_10				
63		01:TIME_10				
64		01:S.PID_L1_10				
65		01:S.PID_L2_10				

	OUT area					
PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)				
Word	Bit	Contents of assignment				
34	position	01:S.PID_L1_4				
35		01:S.PID_L2_4				
36		01:TSP_L1_5				
37		01:TSP_L2_5				
38		01:TIME_5				
39		01:S.PID_L1_5				
40		01:S.PID_L2_5				
41		01:TSP_L1_6				
42		01:TSP_L2_6				
43		01:TIME_6				
44		01:S.PID_L1_6				
45		01:S.PID_L2_6				
46		01:TSP_L1_7				
47		01:TSP_L2_7				
48		01:TIME_7				
49		01:S.PID_L1_7				
50		01:S.PID_L2_7				
51		01:TSP_L1_8				
52		01:TSP_L2_8				
53		01:TIME_8				
54		01:S.PID_L1_8				
55		01:S.PID_L2_8				
56		01:TSP_L1_9				
57		01:TSP_L2_9				
58		01:TIME_9				
59		01:S.PID_L1_9				
60		01:S.PID_L2_9				
61		01:TSP_L1_10				
62		01:TSP_L2_10				
63		01:TIME_10				
64		01:S.PID_L1_10				
65		01:S.PID_L2_10				

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Page 4

PF		IN area -DP/DeviceNet slave (UTAdvanced) → OFIBUS-DP/DeviceNet master	PRO		OUT area DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
0	0	Receive data valid	0	0	Rescan request
	1	During-write		1	(Reserved)
	3	Write acknowledgement (Reserved)		3	Write request (Reserved)
	4	(Reserved)		4	(Reserved)
	5	(Reserved)		5	(Reserved)
	6	(Reserved)		6	(Reserved)
	7	(Reserved)		7	(Reserved)
•	•	The fixed-part is omitted	•	•	The fixed-part is omitted
•	•	(See profile number 0 on page 1)	•	•	(See profile number 0 on page 1)
4		Current page	4		Page change request
5		01:TSP_L1_11	5		01:TSP_L1_11
6		01:TSP_L2_11	6		01:TSP_L2_11
7		01:TIME_11	7		01:TIME_11
8		01:S.PID_L1_11	8		01:S.PID_L1_11
9		01:S.PID_L2_11	9		01:S.PID_L2_11
10		01:TSP_L1_12	10		01:TSP_L1_12
11		01:TSP_L2_12	11		01:TSP_L2_12
12		01:TIME_12	12		01:TIME_12
13		01:S.PID_L1_12	13		01:S.PID_L1_12
14		01:S.PID_L2_12	14		01:S.PID_L2_12
15		01:TSP_L1_13	15		01:TSP_L1_13
16		01:TSP_L2_13	16		01:TSP_L2_13
17		01:TIME_13	17		01:TIME_13
18		01:S.PID_L1_13	18		01:S.PID_L1_13
19		01:S.PID_L2_13	19		01:S.PID_L2_13
20		01:TSP_L1_14	20		01:TSP_L1_14
21		01:TSP_L2_14	21		01:TSP_L2_14
22		01:TIME_14	22		01:TIME_14
23		01:S.PID_L1_14	23		01:S.PID_L1_14
24		01:S.PID_L2_14	24		01:S.PID_L2_14
25		01:TSP_L1_15	25		01:TSP_L1_15
26		01:TSP_L2_15	26		01:TSP_L2_15
27		01:TIME_15	27		01:TIME_15
28		01:S.PID_L1_15	28		01:S.PID_L1_15
29		01:S.PID_L2_15	29		01:S.PID_L2_15
30		01:TSP_L1_16	30		01:TSP_L1_16
31		01:TSP_L2_16	31		01:TSP_L2_16
32		01:TIME_16	32		01:TIME_16
33		01:S.PID_L1_16	33		01:S.PID_L1_16

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Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 4

		IN area					
PR	PROFIBUS-DP/DeviceNet slave (UTAdvanced) → PROFIBUS-DP/DeviceNet master						
Word position	Bit position	Contents of assignment					
34		01:S.PID_L2_16					
35		01:TSP_L1_17					
36		01:TSP_L2_17					
37		01:TIME_17					
38		01:S.PID_L1_17					
39		01:S.PID_L2_17					
40		01:TSP_L1_18					
41		01:TSP_L2_18					
42		01:TIME_18					
43		01:S.PID_L1_18					
44		01:S.PID_L2_18					
45		01:TSP_L1_19					
46		01:TSP_L2_19					
47		01:TIME_19					
48		01:S.PID_L1_19					
49		01:S.PID_L2_19					
50		01:TSP_L1_20					
51		01:TSP_L2_20					
52		01:TIME_20					
53		01:S.PID_L1_20					
54		01:S.PID_L2_20					
55		(Unused)					
56		(Unused)					
57		(Unused)					
58		(Unused)					
59		(Unused)					
60		(Unused)					
61		(Unused)					
62		(Unused)					
63		(Unused)					
64		(Unused)					
65		(Unused)					

	OUT area						
PRO		DP/DeviceNet master → PROFIBUS-DP/ eviceNet slave (UTAdvanced)					
Word	Bit						
	position	Contents of assignment					
34		01:S.PID_L2_16					
35		01:TSP_L1_17					
36		01:TSP_L2_17					
37		01:TIME_17					
38		01:S.PID_L1_17					
39		01:S.PID_L2_17					
40		01:TSP_L1_18					
41		01:TSP_L2_18					
42		01:TIME_18					
43		01:S.PID_L1_18					
44		01:S.PID_L2_18					
45		01:TSP_L1_19					
46		01:TSP_L2_19					
47		01:TIME_19					
48		01:S.PID_L1_19					
49		01:S.PID_L2_19					
50		01:TSP_L1_20					
51		01:TSP_L2_20					
52		01:TIME_20					
53		01:S.PID_L1_20					
54		01:S.PID_L2_20					
55		(Unused)					
56		(Unused)					
57		(Unused)					
58		(Unused)					
59		(Unused)					
60		(Unused)					
61		(Unused)					
62		(Unused)					
63		(Unused)					
64		(Unused)					
65		(Unused)					

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3.10 Changing Automatic Rescan Time (SCAN in PROF/DNET Menu)

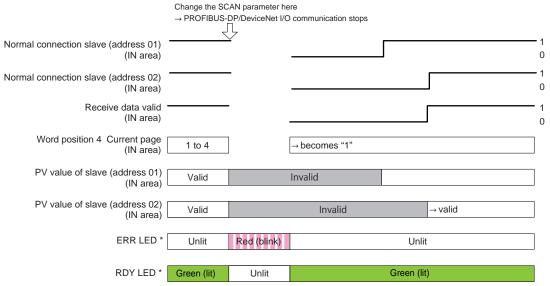
When the automatic rescan time setting is changed, UTAdvanced will perform operation in the following ways.

- (1) Stops the PROFIBUS-DP/DeviceNet I/O communication.
- (2) Sets the timer according to the SCAN parameter value.
- (3) Restarts the PROFIBUS-DP/DeviceNet I/O communication.
- (4) Sets the current page of the profile to 1 and restarts Modbus communication.

The automatic rescan time is set by the SCAN parameter in the PROFIBUS-DP Communication Settings menu (PROF) or the DeviceNet Communication Settings menu (DNET).

- ► Setting SCAN parameters: "2.1.1 Setting PROFIBUS-DP Communication (PROFIBUS-DP Slave/Modbus Master)" or "2.1.2 Setting DeviceNet Communication (DeviceNet Slave/Modbus Master)"in this manual
- ▶ 3.1 Overview: "Example: PROFIBUS-DP/DeviceNet Communication Connection" in this manual

Example of connecting 2 slaves (address 01 and 02):



^{*:} For DeviceNet, one MNS LED turns on (green) or blinks (red).

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3.11 Changing Profile Number (FILE in PROF/DNET Menu)

The profile number is set by the FILE parameter in the PROFIBUS-DP Communication Settings menu (PROF) or the DeviceNet Communication Settings menu (DNET).

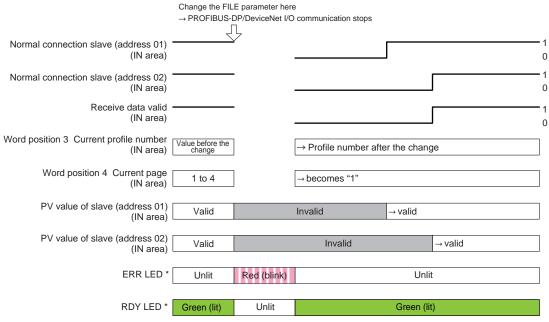
- ► Setting SCAN parameters: "2.1.1 Setting PROFIBUS-DP Communication (PROFIBUS-DP Slave/Modbus Master)" or "2.1.2 Setting DeviceNet Communication (DeviceNet Slave/Modbus Master)"in this manual
- ▶ 3.1 Overview: "Example: PROFIBUS-DP/DeviceNet Communication Connection" in this manual

When the profile number is changed, the PROFIBUS-DP/DeviceNet I/O size changes. If the configured profile number (I/O size) matches the PROFIBUS-DP/DeviceNet master, a connection can be established with the PROFIBUS-DP/DeviceNet master after the change is made. If it does not match, a connection cannot be established.

The following figure shows a case where the I/O size does not change after the profile number is changed. Since the I/O size usually changes, the PROFIBUS-DP/DeviceNet communication is disconnected either before or after, or both before and after the change (ERR LED*: blinking red).

*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

Example of connecting 2 slaves (address 01 and 02):



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

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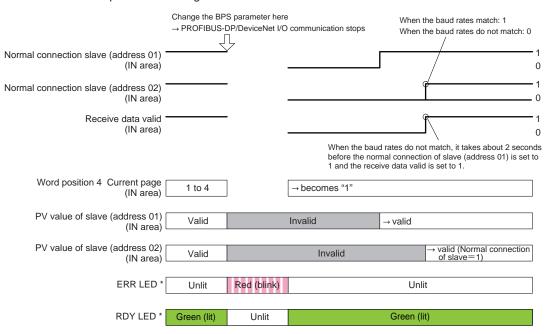
3.12 Changing RS-485 Baud Rate (BPS in PROF/DENT Menu)

Make sure that the RS-485 baud rates of the Modbus master and Modbus slaves are identical.

The RS-485 baud rate of the Modbus master is set by the BPS parameter in the PROFIBUS-DP Communication Settings menu (PROF) or the DeviceNet Communication Settings menu (DNET).

- ► Setting SCAN parameters: "2.1.1 Setting PROFIBUS-DP Communication (PROFIBUS-DP Slave/Modbus Master)" or "2.1.2 Setting DeviceNet Communication (DeviceNet Slave/Modbus Master)" in this manual
- ▶ 3.1 Overview: "Example: PROFIBUS-DP/DeviceNet Communication Connection" in this manual

Example of connecting 2 slaves address 01 and 02:



*: For DeviceNet, one MNS LED turns on (green) or blinks (red).

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3.13 PLC Memory Space

UTAdvanced that serves a PROFIBUS-DP/DeviceNet slave occupies area of the memory space of the PROFIBUS-DP/DeviceNet master. The user needs to know from the perspective of a PLC where the data of UTAdvanced is assigned in the memory space. Be careful because the size of the occupied memory space of the master varies depending on the profile number of UTAdvanced that serves as a PROFIBUS-DP/DeviceNet slave.

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4.1 Overview

CC-Link is an open field bus used in various applications for factory automation and process automation.

CC-Link is used for communication between PLCs and remote I/O, enabling high-speed data transmission.

Note.

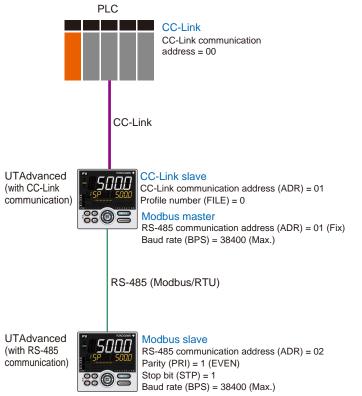
For details of CC-Link specifications and information, see the documents published from the CC-Link Partner Association in respective regions.

Note: The maximum baud rate may be 19200 bps

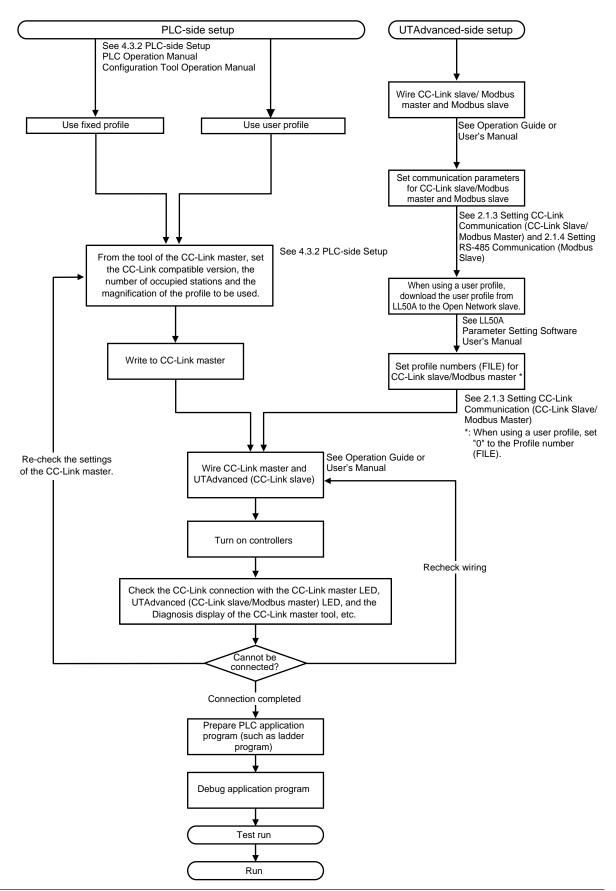
by the model.

CC-Link Partner Association: http://www.CC-Link.org/

Example: CC-Link Communication Connection



4.2 Workflow



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4.3 Setting Up Connection with Master

4.3.1 UTAdvanced-side Setup

Wiring

For wiring, see UTAdvanced Operation Guide or User's Manual.

Setting communication parameters

For setting parameters, see 2.1.2 and 2.1.3 of this manual.

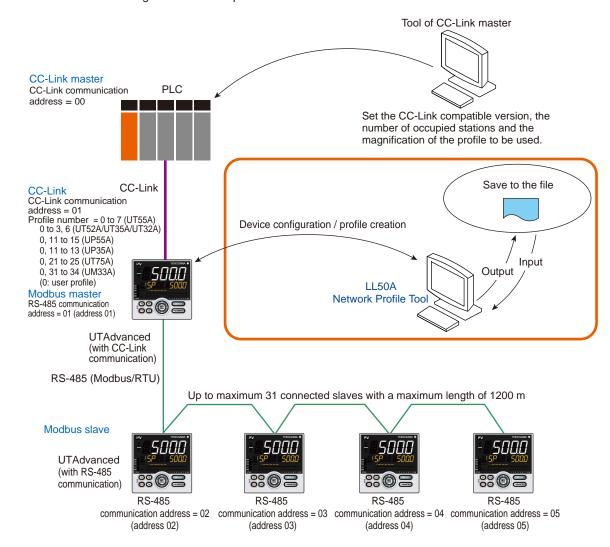
Downloading User Profile

When using a user profile, download the user profile via LL50A.

For the procedure of download, see LL50A Parameter Setting Software User's Manual.

4.3.2 PLC-side Setup

Set the CC-Link compatible version, the number of occupied stations and the magnification of the profile to be used.



4.4 Profile

4.4.1 Contents of Profile

The UT75A/UT55A/UT52A/UT35A/UT32A fixed profile contains 3-station occupation and 4-station occupation for Version 1.10 of the CC-Link master and 1-station occupation and 2-station occupation in the x8 setting for Version 2.00.

The UP55A/UP35A/UP32A fixed profile contains 3-station occupation and 4-station occupation for Version 1.10 of the CC-Link master and 3-station occupation in the x8 setting and 2-station occupation in the x4 setting for Version 2.00.

▶ "4.4.2 Type of Profile" in this manual

The profile contains a bit data area and a word data area, both of which consists of a predefined fixed area and a data area to which a parameter is assigned.

Flags to switch pages of the data-part and flags to indicate the connection status of controllers are assigned to the fixed-part.

The data-part can be used by switching pages. The number of pages of a profile is 4 (1 to 4).

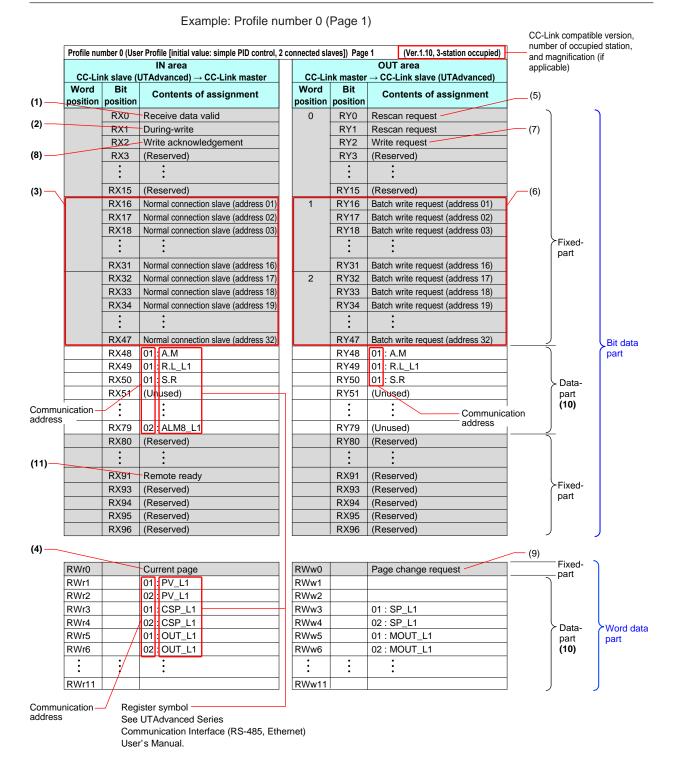
When creating a user profile with LL50A Network Profile Tool, the settings can be selected from the combination of the followings: Ver 1.10, Ver 2.00, 1-station to 4-station occupation, and x1 to x8 speed.

Note

Parameters are classified into each page of a profile, i.e. the profile is classified into pages of the more frequently used parameters for routine operation and the less frequently used parameters for startup and batch-start.

Classification for each page allows reducing the memory space occupied in the master. Furthermore, it allows optimizing the updating of the data (in the IN area) read from UTAdvanced.

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(1) Receive data valid flag (1: valid)

This flag allows checking whether the data in the IN area is valid.

If the flag is set to 1, the data in the IN area is valid. (However, this only applies to the slave data where the normal connection slave flag (address 01 to 32) is also set to 1.)

When the power is turned on or a rescan is requested, the flag is set to 0. When checking whether all slaves registered in the profile are ready for communication is finished, the flag is set to 1.

(2) During-write flag (0: write enable)

This flag allows checking whether writing to the OUT area is enabled.

If the flag is set to 0, writing to the OUT area is enabled.

When the power is turned on, the flag is set to 0. When the write communication is performed, the flag is set to 1. When a response is returned from the slave, or when the time is up, the flag returns to 0.

Note

When the flag is set to 1, a write request is not accepted and is ignored (not held).

(3) Normal connection slave flag (Address 01 to 32) (1: connected)

This flag allows checking whether each slave is connected.

The normal connection of slave flags have 32 bits in the fixed-part of the IN area. Slave (address 01) in word position 1, and bit position 0 is UTAdvanced that runs as a CC-Link slave.

Note:

If normal connection slave (address 01) is set to 0 in a profile in which the CC-Link slave (address 01) is registered, and rescan does not cause the flag to return to 1, it is a failure.

Word position 1 and bit positions RX16 to RX47, and word position 2 and bit positions 0 to 15 correspond to the Modbus slaves with communication addresses 2 to 32.

When slaves (address 01 to 32) are connected, each flag is set to 1. When the power is turned on, the flag is set to 0, and when communication becomes enabled, the flag is set to 1. When communication is disabled, the flag is set to 0. When communication becomes enabled upon a rescan request, the flag is set to 1.

(4) Current pager

The currently used profile page number (1 to 4) is displayed.

(5) Rescan request flag

A rescan request is made to attempt a retry to establish communication with unconnected slaves. (When connection is normally established with all slave controllers registered in the profile, a rescan is not performed.)

Change the flag in RY0 of the OUT area from 0 to 1. Thereafter, return it to 0 when the receive data valid flag has been set to 1.

(6) Write request flag (address 01 to 32)

This flag allows writing all parameters that are assigned to the OUT area together to the slave for each communication address.

Write request flags have 32 bits in the fixed-part of the OUT area. Slave (address 01) in RY16 is UTAdvanced that runs as a CC-Link slave.

RY16 to RY14, correspond to the Modbus slaves with communication addresses 2 to 32.

Change the write request flags from 0 to 1 for the slaves (address 01 to 32) to be written, while the during-write flag is set to 0. Thereafter, return them to 0 when the writing process is completed.

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(7) Write request flag and (8) Write acknowledgment flag When a write request is made, regardless of whether the writing is performed individually or all together, both the write acknowledgement flag and write request flag need to be used to reliably recognize that the writing is completed. When the write request flag is set to 1, while the during-write flag and write acknowledgement flag are set to 0, the write acknowledgment flag is set to 1. Set the data-part of the OUT area when the write acknowledgment flag is set to 1. Thereafter, returning the write request flag to 0 prompts the writing to be performed. When the writing is completed, the write acknowledgment flag is set to 0.

▶ "4.6 Reading and Writing UTAdvanced Data" in this manual

(9) Page change request

This request switches the currently used profile page.

Set the value in OUT area RWw0 to a value (any of 1 to 4) that is different from the current page (in IN area RWr0).

The receive data valid flag remains set to 0 until the page is switched upon the page change request and the data acquisition is completed.

► "4.7 Switching Pages" in this manual

(10) Data-part

The data format is the same as that of the displayed value of UTAdvanced.

(11) Remote ready (1: valid)

The function and operation are same as those of the receive data valid flag. The bit position varies depending on the profile.

4.4.2 Types of Profile

UT55A provides one user profile and 7 fixed profiles.

UT75A/UP55A provides one user profile and 5 fixed profiles.

UT52A/UT35A/UT32A/UM33A provides one user profile and 4 fixed profiles.

UP35A/UP32A provides one user profile and 3 fixed profiles.

Set each profile numbers according to the configurations.

Profile numbers can be set with the FILE parameter in the CC-Link Communication Settings menu (CC-L).

► Setting FILE parameters: "2.1.3 Setting CC-Link Communication (CC-Link Slave/Modbus Master)" in this manual

Example: UT55A/UT35A

For a simple PID control with 6 connected controllers, use "Profile number 3: 8 simple PID controllers".

For a simple PID control with 10 connected controllers, set the connection devices using "Profile number 0: User profile" with Network Profile Tool of LL50A.

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UT55A/UT52A/UT35A/UT32A

				CC-Link version	Applicable control mode and control type		
Profile number	Profile Name Page Item		Number of occupied stations and magnification I/O size (RX/RY: bit, RWr/RWw: word)	Control mode	Control type Control type		
	User profile	1	Process value, operation mode, alarm status				
	(Initial value: Simple PID	2	PID parameter	Ver.1.10			
0	Control,	3	Heating/cooling PID parameter	3-station occupation 96/96, 12/12			
	2 connected slaves)	4	Alarm setpoint	,			
	Simple PID	1	Process value, operation mode, alarm status				
	Control,	2	PID parameter	Ver.1.10	All		
1	3 connected	3	Heating/cooling PID parameter	4-station occupation 128/128, 16/16	All modes except for		
	slaves	4	Alarm setpoint	120/120, 10/10	Cascade		
	Simple PID	1	Process value, operation mode, alarm status	Ver.2.00 1-station occupation x8	Control (4: CAS)		
	Control,	2	PID parameter			All type	
2	5 connected	3	Heating/cooling PID parameter	setting			
	slaves	4	Alarm setpoint	128/128, 32/32			
	Simple PID	1	Process value, operation mode, alarm status	Ver.2.00			
3	Control,	2	PID parameter	2-station occupation x8			
3	8 connected slaves	3	Heating/cooling PID parameter	setting 384/384, 64/64			
		4	Alarm setpoint				
	Cascade	1	Process value, operation mode, alarm status	Ver.2.00			
4	Control,	2	PID parameter	1-station occupation x8 setting			
1	3 connected	3	Heating/cooling PID parameter				
	slaves	4	Alarm setpoint	128/128, 32/32	Cascade Control		
	Cascade	1	Process value, operation mode, alarm status	Ver.2.00	(4: CAS)		
5	Control,	2	PID parameter	2-station occupation x8	,		
	5 connected	3	Heating/cooling PID parameter	setting			
	slaves	4	Alarm setpoint	384/384, 64/64			
	Simple PID	1	Process value, operation mode, alarm status	1/07440	All modes		
6	Control,	2	PID parameter	Ver.1.10 3-station occupation	except for Cascade		
	1 connected slave	3	Heating/cooling PID parameter	96/96, 12/12	Control (4:		
	Slave	4	Alarm setpoint		CAS)		
	Cascade	1	Process value, operation mode, alarm status	Ver.2.00	Cascade		
7	Control,	2	PID parameter	1-station occupation x8	Cascade		
	1 connected slave	3	Heating/cooling PID parameter	setting 128/128, 32/32	(4: CAS)		
	Siave	4	Alarm setpoint	120/120, 32/32			

UP55A/UP35A/UP32A

				CC-Link version Number of occupied stations	Applicable control mode and control type	
Profile number	rofile Name Page Item		and magnification I/O size (RX/RY: bit, RWr/RWw: word)	Control mode	Control type	
	User profile	1	Process value, operation mode, alarm status			
	(Initial value: Simple PID	2	PID parameter (for address 1)	Ver.1.10		
0	Control,	3	PID parameter (for address 2)	3-station occupation 96/96, 12/12		
	2 connected slaves)	4	Local event-1 to -2 setpoint (for address 1, 2)	·		
	Simple PID	1	Process value, operation mode, alarm status			
11	Control,	2	PID parameter, Alarm setpoint	Ver.1.10		
''	2 connected	3	Local event-1 to -7 setpoint (for address 1)	4-station occupation 128/128, 16/16		
	slaves	4	Local event-1 to -7 setpoint (for address 2)	1		
	Simple PID	1	Process value, operation mode, alarm status	Ver.2.00		
12	Control, 4 connected	2	PID parameter, Alarm setpoint	2-station occupation x4 setting		
12		3	Local event-1 to -7 setpoint (for address 1, 2)			
	slaves	4	Local event-1 to -7 setpoint (for address 3, 4)	192/192, 32/32		
	Simple PID	1	Process value, operation mode, alarm status			
13	Control, 1 connected	2	PID parameter, Local event-1 to -7 setpoint, Program pattern clearance	Ver.2.00 3-station occupation x8		
	slave (with program	3	Pattern setting	setting 640/640, 96/96		
	pattern setting)	4	Segment setting	040/040, 00/00		
	Cascade	1	Process value, operation mode, alarm status	Ver.2.00		
14	Control,	2	PID parameter, Alarm setpoint	2-station occupation x4		
14	2 connected	3	Local event-1 to -7 setpoint (for address 1, 2)	setting		
	slaves	4	Local event-1 to -7 setpoint (for address 3, 4)	192/192, 32/32	Cascade	
	Cascade	1	Process value, operation mode, alarm status		Cascade	
15	Control, 1 connected	2	PID parameter, Local event-1 to -7 setpoint, Program pattern clearance	Ver.2.00 3-station occupation x8	(4: CAS)	
	slave (with program	3	Pattern setting	setting 640/640, 96/96		
	pattern setting)	4	Segment setting			

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UT75A

				CC-Link version Number of occupied stations		le control
Profile number Name Page number Item		ltem	and magnification I/O size (RX/RY: bit, RWr/RWw: word)	Control mode	Control type	
	User profile	1	Process value, operation mode, alarm status			
	(Initial value: Simple PID	2	PID parameter	Ver.1.10		
0	Control,	3	Heating/cooling PID parameter	3-station occupation 96/96, 12/12		
	2 connected slaves)	4	Alarm setpoint	,		
	Simple PID	1	Process value, operation mode, alarm status			
21	Control,	2	PID parameter	Ver.1.10	All modes	
21	3 connected	3	Heating/cooling PID parameter		except for Cascade Control (4: CAS)	
	slaves	4	Alarm setpoint	,,		
	Simple PID	1	Process value, operation mode, alarm status	Ver.2.00		
22	Control, 5 connected	2	PID parameter	1-station occupation x8		
22		3	Heating/cooling PID parameter	setting		
	slaves	4	Alarm setpoint	128/128, 32/32		
	Simple PID	1	Process value, operation mode, alarm status	Ver.2.00		
23	Control,	2	PID parameter	2-station occupation x8		
25	8 connected	3	Heating/cooling PID parameter	setting		
	slaves	4	Alarm setpoint	384/384, 64/64		
	Cascade	1	Process value, operation mode, alarm status	Ver.2.00		
24	Control / Dual- loop Control,	2	PID parameter	1-station occupation x8		
24	3 connected	3		setting		
	slaves	4	Alarm setpoint	128/128, 32/32	Cascade	
	Simple PID	1	Process value, operation mode, alarm status		Control	
	Control / Cascade	2	PID parameter	Ver.2.00	(4: CAS)	
25	Control / Dual-	3	Program pattern setting	2-station occupation x8		
	lop Control, 1 connected slave	4	Program pattern setting	setting 384/384, 64/64		

UM33A

- 01				
Profile number	Name	Page number	ltem	CC-Link version Number of occupied stations and magnification I/O size (RX/RY: bit, RWr/RWw: word)
	User profile	1	Process value, alarm status	
0	(Initial value: UM33A 2	2	Alarm setpoint	Ver.1.10
0	connected	3	Alarm setpoint	3-station occupation 96/96, 12/12
	slaves)	4		
		1	Process value, alarm status	
04	UM33A 3 connected slaves	2	Alarm setpoint	Ver.1.10
31		3	Alarm setpoint	4-station occupation 128/128, 16/16
		4		
		1	Process value, alarm status	Ver.2.00
32	UM33A 5 connected	2	Alarm setpoint	1-station occupation x8
32	slaves	3	Alarm setpoint	setting
		4		128/128, 32/32
	LINAGOA	1	Process value, alarm status	Ver.2.00
33	UM33A 8 connected	2	Alarm setpoint	2-station occupation x8
	slaves	3	Alarm setpoint	setting
		4		384/384, 64/64
	UM33A 1	1	Process value, alarm status	Ver.1.10
34	connected	2	Alarm setpoint	3-station occupation
	slave	3	Alarm setpoint	96/96, 12/12
		4		

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User profile

As the default, a parameter for a simple PID control with 2 connected controllers is set. Users can assign the data-part of the user profile with the Network Profile Tool of LL50A.

▶ LL50A Parameter Setting Software User's Manual

Fixed profile

UT75A:

Parameters for a simple PID control / Cascade Control / Dual-loop Control (with program pattern setting) with 1 connected controller are set.

UT55A/UT52A/UT35A/UT32A:

Parameters for a simple PID control with 1, 3, 5, and 8 connected controllers and for a cascade control with 1, 3 and 5 connected controllers are set.

UP55A/UP35A/UP32A:

Parameters for a simple PID control with 2, 4, and 1 (with program setting function) connected controllers and for a cascade control with 2 and 1 (with program setting function) connected controllers are set.

However, the parameters for a cascade control (Profile numbers: 4 and 5) cannot be used for UT52A/UT35A/UT32A, and the parameters for a cascade control (Profile numbers: 14 and 15) cannot be used for UP35A/UP32A.

UM33A: Parameters for 1, 3, 5, and 8 connected GM33A are set.

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4.5 Operation at the Time of Power-On

The following shows how the IN area of UTAdvanced looks like from the perspective of a PLC when UTAdvanced is turned on while the PLC power is already on.

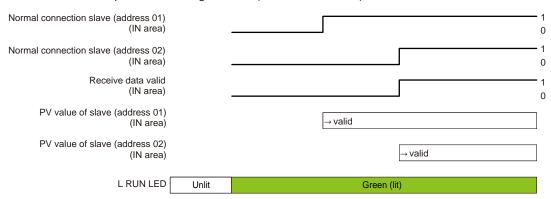
Note

The UTAdvanced data and write request in the IN area become valid when the normal connection flag for each slave is set to 1 ("→valid" in the figure below). However, it is recommended to handle them after the receive data valid flag is set to 1.

- ▶ 4.1 Overview: "Example: CC-Link Communication Connection" in this manual
- ▶ "4.9 Profile List" in this manual

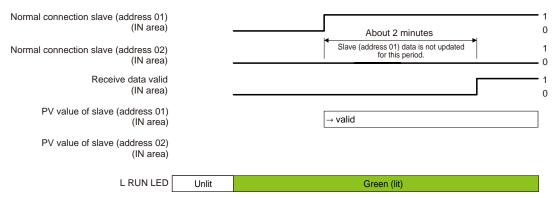
4.5.1 Example at the Time of Power-On

Example of connecting 2 slaves (address 01 and 02):



4.5.2 Example at the Time of Power-On (When Slave (address 02) is not Connected)

Example where slave (address 01) is connected, but slave (address 02) is not connected:



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4.6 Reading and Writing UTAdvanced Data

- ▶ 4.1 Overview: "Example: CC-Link Communication Connection" in this manual
- ▶ "4.9 Profile List" in this manual

4.6.1 Reading

Data in the IN area that is always updated can be read.

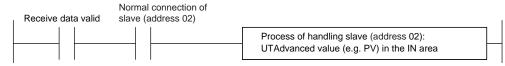
Procedure

- 1. Check that the receive data valid flag is set to 1.
- Check that the normal connection slave flag for a slave to be handled (address 01 to 32) is set to 1.
- Data for the corresponding slave (address 01 to 32) in the IN area can be handled.

Note -

If both the receive data valid flag and normal connection slave flag are set to 1, the data in the IN area is valid.

Example of ladder program



4.6.2 Writing Individual Parameters

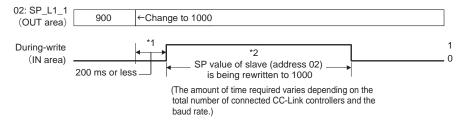
Only the parameter values to be changed in the OUT area can be written.

■ Simple procedure of writing individual parameters used when the write interval is long

Procedure

- 1. Check that the during-write flag is set to 0.
- 2. Change the value in the OUT area to which the parameter to be written is assigned.

Example of rewriting the SP value for slave (address 02):



Note

- *1 in the figure above
 - If the write value is changed multiple times during the period*1, the last write value is valid.

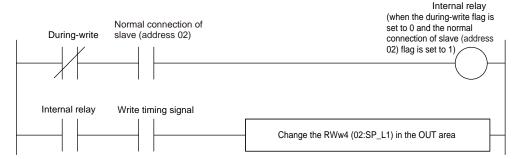
*2 in the figure above

- If the during-write flag is set to 1, a changed value in the OUT area is invalid. The change
 of the value is ignored (not held).
- Changing a value in the OUT area results in a request for writing the individual parameter.
- UTAdvanced holds the previous values in the OUT area in order to detect changes in the values in the OUT area. The previous values in the OUT area are set to 0 when the power is turned on, or when CC-Link is disconnected. If a value other than 0 is written in the OUT area of a PLC when the disconnected CC-Link is connected, UTAdvanced handles it as a request for writing the individual parameter.
 - If you want to write 0 first after the disconnected CC-Link is connected, you need to use batch writing. Furthermore, the first writing after the power is turned on needs to use batch writing.
 - ▶ Batch writing: "4.6.3 Batch writing for Each Communication Address" of this manual
- If the CC-Link baud rate is slow or the PLC scan cycle is long, the PLC program may
 be unable to detect that the during-write flag in the IN area has been set to 1. In order
 to reliably detect that the writing is completed, individual parameters need to be written
 using both the write request flag in the OUT area and the write acknowledgement flag in
 the OUT area (Procedure of reliably detecting that the writing is completed).

Note

For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.

Example of ladder program



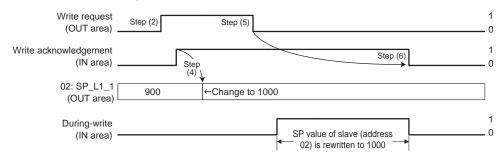
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Writing individual parameters (Procedure to reliably detect that the writing is completed)

Procedure

- 1. Check that the during-write flag is set to 0.
- 2. Change the write request flag from 0 to 1.
- 3. Check that the write acknowledgment flag has been set to 1.
- Change the value in the OUT area to which the parameter to be written is assigned.
- **5.** Return the write request flag from 1 to **0** (which is equivalent to the write start command). The timing of returning the flag to 0 may be the same as that of step 4.
- 6. When the write acknowledgment flag has been set to 0, the writing is completed.

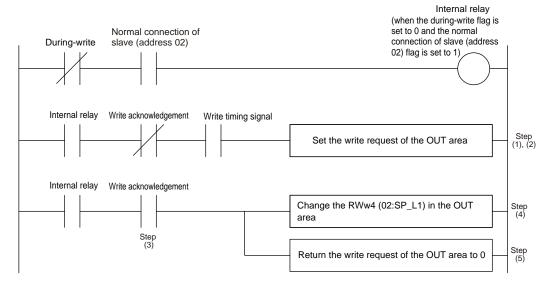
Example of rewriting the SP value of slave (address 02):



Note.

- Changing the value in the OUT area results in a request for writing the individual parameter. If you want to write the current values in the OUT area, use batch writing.
- For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.

Example of ladder program



4.6.3 Batch Writing for Each Communication Address

Batch writing can be performed on parameter values assigned to the OUT area for each communication address.

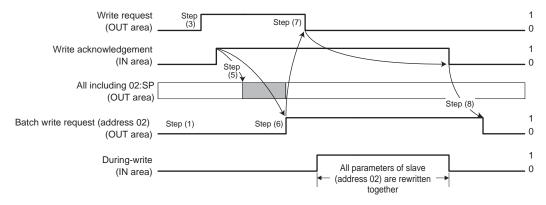
Procedure

- 1. Set the write request flag for the slaves to be written (address 01 to 32) to 0.
- 2. Check that the during-write flag is set to **0**.
- 3. Change the write request flag from 0 to 1.
- 4. Check that the write acknowledgment flag has been set to 1.
- 5. Set the values in the OUT area to which the parameters to be written are assigned. (The same values as the previously written values can also be written.)
- Change the write request flag for the slaves to be written (address 01 to 32) from 0 to 1.
- 7. Return the write request flag from 1 to 0 (which is equivalent to the write start command). The timing of returning the flag to 0 may be the same as that in steps 5 and 6
- When the write acknowledgment flag has been set to 0, the writing is completed. Return the write request flag from 1 to 0.

Note .

- Batch writing writes the values in the OUT area at the point of step (7).
- Changing the write request flag from 0 to 1 (step 6) needs to be performed when the duringwrite flag is set to 0 and the write acknowledgment flag is set to 1. If these conditions are not met, the write request is invalid.

Example of changing the write request flag for slave (address 02) from 0 to 1:

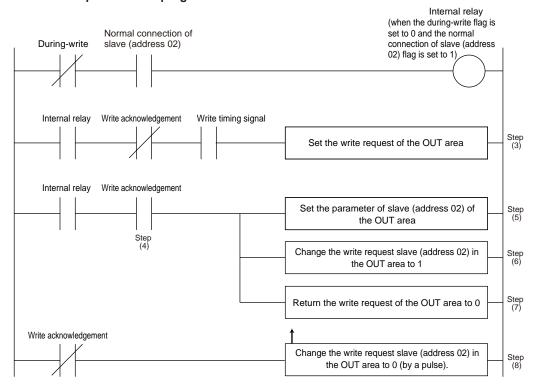


Note

- For the range and decimal point position of values to be written, see UTAdvanced Operation Guide or User's Manual.
- When the CC-Link baud rate is slow, or the scan cycle of a PLC is long, the PLC program
 may be unable to detect that the during-write flag has been set to 0.

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Example of ladder program



4.6.4 Reading Program Pattern (for UP55A/UP35A/UP32A)

Procedure

- Check that the receive data valid flag and the normal connection slave flag are set to 1.
- Check that the during-write flag is set to 0.
- 3. Change the write request flag from 0 to 1.
- 4. Check that the write acknowledgment flag has been set to 1.
- **5.** Write "0" to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
- 6. Return the write request flag from 1 to 0.
- 7. Check that the write acknowledgment flag has been set to 0.
- 8. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C), and then confirm that is "0".
- 9. Check that the during-write flag is set to 0.
- 10. Change the write request flag from 0 to 1.
- 11. Check that the write acknowledgment flag has been set to 1.
- 12. Write the required pattern number and the segment number to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
- 13. Return the write request flag from 1 to 0.
- 14. Check that the write acknowledgment flag has been set to 0.
- 15. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C), the Segment number designation (SEGNO._C) and the Read/write error information (PTN.ERR).
 - Confirm that the required pattern number and the segment number are set to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C), and then the Read/write error information (PTN.ERR) has been set to "0".
- **16.** Read the data corresponding to the segment parameters in the IN area; from the Final target setpoint (TSP_L1) to the Off time of time event 16 (T.OF16).

Note:

The following operations cannot be executed concurrently, otherwise the program pattern cannot be read/written normally.

- Access to the program pattern via Open Network.
- Upload/download of the program pattern using the LL50A Parameter Setting Tool.

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4.6.5 Writing Program Pattern (for UP55A/UP35A/UP32A)

Procedur<u>e</u>

- Check that the receive data valid flag and the normal connection slave flag are set to 1.
- 2. Check that the during-write flag is set to 0.
- 3. Change the write request flag from 0 to 1.
- 4. Check that the write acknowledgment flag has been set to 1.
- **5.** Write "0" to the OUT area corresponding to the Program pattern number selection (PTNO._C) and the Segment number designation (SEGNO._C).
- 6. Return the write request flag from 1 to 0.
- 7. Check that the write acknowledgment flag has been set to 0.
- Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C), and confirm that is "0".
- 9. Check that the during-write flag is set to 0.
- 10. Change the write request flag from 0 to 1.
- 11. Check that the write acknowledgment flag has been set to 1.
- 12. Write the required pattern number and pattern data to the OUT area corresponding to the Program pattern number selection (PTNO._C) and Starting target setpoint (SSP_L1) to Program pattern name (P.NAME).
- 13. Return the write request flag from 0 to 1.
- 14. Return the write request flag from 1 to 0.
- 15. Check that the write acknowledgment flag has been set to 0.
- 16. Return the write request flag from 1 to 0.
- 17. Read the data in the IN area corresponding to the Program pattern number selection (PTNO._C) and the Read/write error information (PTN.ERR).
 - Confirm that the required pattern number is set to the Program pattern number selection (PTNO._C) and the Read/write error information (PTN.ERR) has been set to "0".

Note

The following operations cannot be executed concurrently, otherwise the program pattern cannot be read/written normally.

- · Access to the program pattern via Open Network.
- Upload/download of the program pattern using the LL50A Parameter Setting Tool.

4.7 Switching Pages

Pages can be switched by changing the value for the page change request (in RWw0 of the OUT area fixed-part).

- ▶ 4.1 Overview: "Example: CC-Link Communication Connection" in this manual
- ▶ "4.9 Profile List" in this manual

Procedure

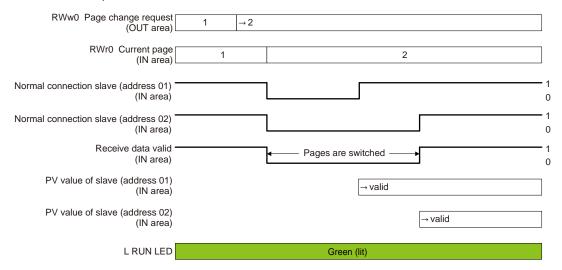
- 1. Check that the during-write flag is set to 0.
- 2. Change the value for the page change request (in RWw0 of the OUT area fixed-part) to a value (any of 1 to 4) that is different from the current page (in RWw0 of the IN area). The pages will be switched.

Note:

- The page change request is accepted even when the during-write flag is set to 1. However, the page is actually changed when the writing is completed.
- CC-Link communication remains connected during the period when the page is being changed.
- The data and write request in the IN area become valid when the normal connection flag for each slave is set to 1 ("

 valid" in the figure below). However, it is recommended to handle them after the receive data valid flag has been set to 1.
- The value for the page change request needs to be held without change for 200 ms or longer. It is recommended that the next page change request is made after the receive data valid flag has been changed from 0 to 1.

Change of the flag when the page is switched from 1 to 2 when 2 slaves (address 01 and 02) are connected:



Checking page

The current page can be checked with the RWr0 of the IN area fixed-part.

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4.8 Request for Rescanning

UTAdvanced that runs as a Modbus master attempts to establish communication with Modbus slaves registered in the profile, and if it cannot connect to a slave because of a wiring error or inconsistency in the communication conditions, it gives up the attempt to establish communication with that slave from the next time. It reduces the update cycle of the read data by reducing the time of communication with slaves that are disabled for communication.

A request for rescanning is made to attempt to start communication with slaves that were disabled for communication after errors with the wiring and communication conditions are fixed.

There are two types of request for rescanning: one type of request is made as needed, and the other is made at a constant frequency (automatic rescan time in SCAN). This section describes the type of rescan request that is made as needed.

- ▶ 4.1 Overview: "Example: CC-Link Communication Connection" in this manual
- ▶ "4.9 Profile List" in this manual

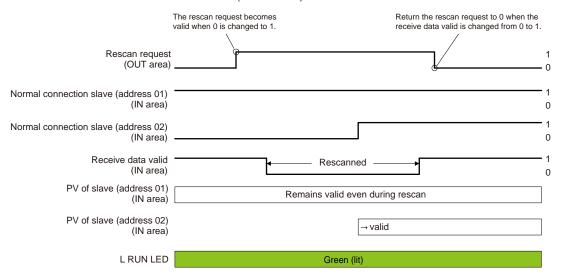
Procedure

- Change the rescan request flag (in RY0 of the OUT area fixed-part) from 0 to 1. Rescanning starts.
- 2. Return the rescan request flag from 1 to 0.

Note.

- A request for rescanning is accepted even when the during-write flag is set to 0. However, the rescan request process is actually performed after the writing is completed.
- The data and write request in the IN area become valid when the normal connection flag for each salve is set to 1 ("-valid" in the figure below). However, it is recommended to handle them after the receive data valid flag is set to 1. This is why if there are slaves to which connection cannot be established, the updating of the data of the salves to which connection can be established will be delayed by a time equaling the number of unconnected slaves multiplied by about 2 seconds. If there are many slaves that cannot be connected, it is recommended for the same reason to use the automatic rescan function.
- 0 of the rescan request flag needs to be held for 200 ms or longer before it is set to 1. Furthermore, after it is set to 1, 1 needs to be held for 200 ms or longer before it is set to 0. It is recommended to return the rescan request flag to 0 after the receive data valid flag is changed from 0 to 1.
- The rescan operation is performed on slaves that are not connected. If connection is normally established with all slaves registered in the profile, the receive data valid flag remains set to 1 even when a rescan request is made.
- "4.10 Changing Automatic Rescan Time (SCAN in CC-L Menu)" in this manual

The operation of each flag when slave (address 01) is connected and slave (address 02) is not connected, and the rescan request flag is changed from 0 to 1 in order to establish a connection with the slave (address 02):



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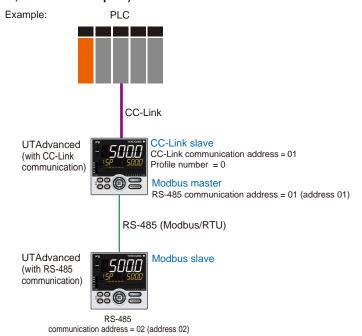
4.9 Profile List

For how to read the profile, see "4.4 Profile."

4.9.1 Profile List for UT55A/UT52A/UT35A/UT32A

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) (Ver.1.10, 3-station occupied)





Page 1

Profile r	number 0	(User profile [initial value: simple PID control w	ith 2	2 connecte	d control	lers]) on page 1 (Ver.1.10, 3-station occupied)
		IN area				OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master		C	C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	RX8	(Reserved)			RY8	(Reserved)
	RX9	(Reserved)			RY9	(Reserved)
	RX10	(Reserved)			RY10	(Reserved)
	RX11	(Reserved)			RY11	(Reserved)
	RX12	(Reserved)			RY12	(Reserved)
	RX13	(Reserved)			RY13	(Reserved)
	RX14	(Reserved)			RY14	(Reserved)
	RX15	(Reserved)			RY15	(Reserved)
	RX16	Normal connection slave (address 01)			RY16	Batch write request (address 01)
	RX17	Normal connection slave (address 02)			RY17	Batch write request (address 02)
	RX18	Normal connection slave (address 03)			RY18	Batch write request (address 03)
	RX19	Normal connection slave (address 04)			RY19	Batch write request (address 04)
	RX20	Normal connection slave (address 05)			RY20	Batch write request (address 05)
	RX21	Normal connection slave (address 06)			RY21	Batch write request (address 06)
	RX22	Normal connection slave (address 07)			RY22	Batch write request (address 07)
	RX23	Normal connection slave (address 08)			RY23	Batch write request (address 08)
	RX24	Normal connection slave (address 09)			RY24	Batch write request (address 09)
	RX25	Normal connection slave (address 10)			RY25	Batch write request (address 10)

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		IN area	itii 2 connecte	u control	llers]) on page 1 (Ver.1.10, 3-station occupi
C	C-Link sl	ave (UTAdvanced) → CC-Link master	С	C-Link m	naster → CC-Link slave (UTAdvanced)
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment
JSILIOII	-	Normal connection slave (address 11)	position	RY26	Batch write request (address 11)
	RX27	Normal connection slave (address 12)		RY27	Batch write request (address 12)
	RX28	Normal connection slave (address 13)		RY28	Batch write request (address 13)
		Normal connection slave (address 14)		RY29	Batch write request (address 14)
		Normal connection slave (address 15)		RY30	Batch write request (address 15)
	_	Normal connection slave (address 16)		RY31	Batch write request (address 16)
		Normal connection slave (address 17) Normal connection slave (address 18)		RY32 RY33	Batch write request (address 17)
		Normal connection slave (address 19)		RY34	Batch write request (address 18) Batch write request (address 19)
		Normal connection slave (address 20)		RY35	Batch write request (address 20)
		Normal connection slave (address 21)		RY36	Batch write request (address 21)
		Normal connection slave (address 22)		RY37	Batch write request (address 22)
	RX38	Normal connection slave (address 23)		RY38	Batch write request (address 23)
	RX39	Normal connection slave (address 24)		RY39	Batch write request (address 24)
	RX40	Normal connection slave (address 25)		RY40	Batch write request (address 25)
		Normal connection slave (address 26)		RY41	Batch write request (address 26)
		Normal connection slave (address 27)		RY42	Batch write request (address 27)
		Normal connection slave (address 28)		RY43	Batch write request (address 28)
		Normal connection slave (address 29)		RY44	Batch write request (address 29)
		Normal connection slave (address 30) Normal connection slave (address 31)		RY45 RY46	Batch write request (address 30)
	-	Normal connection slave (address 31)		RY47	Batch write request (address 31) Batch write request (address 32)
		01: A.M		RY48	01: A.M
		01: R.L_L1		RY49	01: R.L_L1
		01: S.R		RY50	01: S.R
		(Unused)		RY51	(Unused)
		(Unused)		RY52	(Unused)
	RX53	(Unused)		RY53	(Unused)
	RX54	(Unused)		RY54	(Unused)
	RX55	(Unused)		RY55	(Unused)
	RX56	01: ALM1_L1		RY56	(Unused)
		01: ALM2_L1		RY57	(Unused)
		01: ALM3_L1		RY58	(Unused)
		01: ALM4_L1		RY59 RY60	(Unused)
		01: ALM5_L1 01: ALM6_L1 UT35A/UT32A:		RY61	(Unused)
		01: ALM7_L1 Unused		RY62	(Unused)
		01: ALM8_L1		RY63	(Unused)
		02: A.M		RY64	02: A.M
		02: R.L_L1		RY65	02: R.L_L1
	RX66	02: S.R		RY66	02: S.R
	RX67	(Unused)		RY67	(Unused)
		(Unused)		RY68	(Unused)
		(Unused)		RY69	(Unused)
		(Unused)		RY70	(Unused)
		(Unused)		RY71	(Unused)
		02: ALM1_L1		RY72	/
		02: ALM2_L1 02: ALM3_L1		RY73 RY74	(Unused)
		02: ALM3_L1 02: ALM4_L1		RY75	(Unused)
		02: ALM4_L1 02: ALM5_L1			(Unused)
		02: ALM6_L1 UT35A/UT32A:		RY77	(Unused)
		02: ALM7_L1		RY78	(Unused)
		02: ALM8_L1		RY79	(Unused)
		(Reserved)		RY80	(Reserved)
	:			:	
	-	Remote Ready		RY91	(Reserved)
	:			:	
	RY05	(Reserved)		RY95	(Reserved)
	IVASS	(Inceserveu)		17.193	(Incodiveu)
WrO		Current page	RWw0		Page change request
Wr1		01: PV L1	RWw1		(Unused)
Wr2		02: PV L1	RWw2		(Unused)

		aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment
	RY26	Batch write request (address 11)
	RY27	Batch write request (address 12)
	RY28	Batch write request (address 13)
	RY29	Batch write request (address 14)
	RY30	Batch write request (address 15)
	RY31	Batch write request (address 16)
	RY32	Batch write request (address 17)
	RY33	Batch write request (address 18)
	RY34	Batch write request (address 19)
	RY35	Batch write request (address 20)
	RY36	Batch write request (address 21)
	RY37	Batch write request (address 22)
	RY38	Batch write request (address 23)
	RY39	Batch write request (address 24)
	RY40	Batch write request (address 25)
	RY41 RY42	Batch write request (address 26)
	RY43	Batch write request (address 27) Batch write request (address 28)
	RY44	Batch write request (address 29)
	RY45	Batch write request (address 30)
	RY46	Batch write request (address 30)
	RY47	Batch write request (address 32)
	RY48	01: A.M
	RY49	01: R.L L1
	RY50	01: S.R
	RY51	(Unused)
	RY52	(Unused)
	RY53	(Unused)
	RY54	(Unused)
	RY55	(Unused)
	RY56	(Unused)
	RY57	(Unused)
	RY58	(Unused)
	RY59	(Unused)
	RY60	(Unused)
	RY61	(Unused)
	RY62	(Unused)
	RY63	(Unused)
	RY64 RY65	02: A.M 02: R.L L1
	RY66	02: N.L_L1
	RY67	(Unused)
	RY68	(Unused)
	RY69	(Unused)
	RY70	(Unused)
	RY71	(Unused)
	RY72	(Unused)
	RY73	(Unused)
	RY74	(Unused)
	RY75	(Unused)
	RY76	(Unused)
	RY77	(Unused)
	RY78	(Unused)
	RY79	(Unused)
	RY80	(Reserved)
	•	
	RY91	(Reserved)

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)

Profile r	Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1 (Ver.1.10, 3-station occupied)							
		IN area				OUT area		
C	C-Link sl	ave (UTAdvanced) → CC-Link master		C	C-Link m	aster → CC-Link slave (UTAdvanced)		
Word position	Word Bit Contents of assignment			Word position	Bit position	Contents of assignment		
RWr3		01: CSP_L1		RWw3		01: SP_L1_1		
RWr4		02: CSP_L1		RWw4		02: SP_L1_1		
RWr5		01: OUT_L1		RWw5		01: MOUT_L1		
RWr6		02: OUT_L1		RWw6		02: MOUT_L1		
RWr7		(Unused)		RWw7		(Unused)		
RWr8		(Unused)		RWw8		(Unused)		
RWr9		(Unused)		RWw9		(Unused)		
RWr10		(Unused)		RWw10		(Unused)		
RWr11		(Unused)		RWw11		(Unused)		

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_	C Limbs - I	IN area		C I inle	OUT area	
CC-Link slave (UTAdvanced) → CC-Link master Word Bit				CC-Link master → CC-Link slave (UTAdvanced)		
	position	Contents of assignment		position	Contents of assignment	
	RX0	Receive data valid		RY0	Rescan request	
	RX1	During-write		RY1	(Reserved)	
	RX2	Write acknowledgement		RY2	Write request	
	RX3	(Reserved)		RY3	(Reserved)	
	RX4	(Reserved)		RY4	(Reserved)	
	RX5	(Reserved)		RY5	(Reserved)	
	RX6	(Reserved)		RY6	(Reserved)	
	RX7	(Reserved)		RY7	(Reserved)	
	•	The fixed-part is omitted		•	The fixed-part is omitted	
	•	(See profile number 0 on page 1)		•	(See profile number 0 on page 1)	
	•			•	(See prome number of on page 1)	
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)	
	RX48	(Unused)			(Unused)	
	:			:		
	RX79	(Unused)	1		(Unused)	
	RX80	(Reserved)		RY80	(Reserved)	
	:			:		
	RX91	Remote Ready		RY91	(Reserved)	
	:			:		
	RX95	(Reserved)		RY95	(Reserved)	
RWr0		Current page	RWw0		Page change request	
RWr1		01: P_L1_1	RWw1		01: P_L1_1	
RWr2		02: P_L1_1	RWw2	1	02: P_L1_1	
RWr3		01: I_L1_1	RWw3		01: I_L1_1	
RWr4		02: I_L1_1	RWw4		02: I_L1_1	
RWr5		01: D_L1_1	RWw5		01: D_L1_1	
RWr6		02: D_L1_1	RWw6		02: D_L1_1	
RWr7		01: SPNO.	RWw7		01: SPNO.	
RWr8		02: SPNO.	RWw8		02: SPNO.	
RWr9		(Unused)	RWw9		(Unused)	
RWr10		(Unused)	RWw10		(Unused)	
RWr11		(Unused)	RWw11		(Unused)	

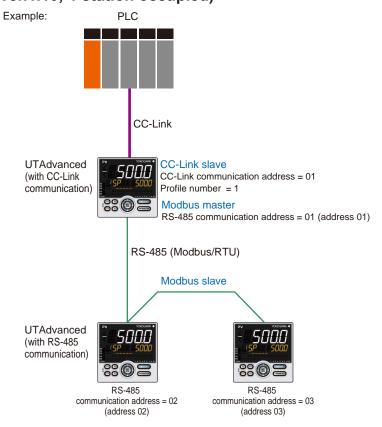
C	C-I ink sl	IN area ave (UTAdvanced) → CC-Link master	C	C-I ink m	OUT area aster → CC-Link slave (UTAdvanced)
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)			(Unused)
	:			:	
	RX79	(Unused)			(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	:			:	
	RX91	Remote Ready		RY91	(Reserved)
	:			:	
	RX95	(Reserved)		RY95	(Reserved)
DIM 0			DW 0		
RWr0		Current page	RWw0		Page change request
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1
RWr3		01: lc_L1_1	RWw3		01: lc_L1_1
RWr4		02: lc_L1_1	RWw4		02: lc_L1_1
RWr5		01: Dc_L1_1	RWw5		01: Dc_L1_1
RWr6		02: Dc_L1_1	RWw6		02: Dc_L1_1
RWr7		01: SPNO.	RWw7		01: SPNO.
RWr8		02: SPNO.	RWw8		02: SPNO.
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

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FIUIIIE	umber v	<u> </u>	ith z connecte	a control	lers]) on page 4 (Ver.1.10, 3-station occupied		
		IN area			OUT area		
		ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
	RX0	Receive data valid		RY0	Rescan request		
	RX1	During-write		RY1	(Reserved)		
	RX2	Write acknowledgement		RY2	Write request		
	RX3	(Reserved)		RY3	(Reserved)		
	RX4	(Reserved)		RY4	(Reserved)		
	RX5	(Reserved)		RY5	(Reserved)		
	RX6	(Reserved)		RY6	(Reserved)		
	RX7	(Reserved)		RY7	(Reserved)		
	•	The fixed-part is omitted		•	The fixed-part is omitted		
		(See profile number 0 on page 1)		•	(See profile number 0 on page 1)		
	•	, , ,		•	, , ,		
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)		
	RX48	(Unused)			(Unused)		
	:			:			
	RX79	(Unused)			(Unused)		
	RX80	(Reserved)		RY80	(Reserved)		
	:			:			
	RX91	Remote Ready		RY91	(Reserved)		
	:			:			
	RX95	(Reserved)		RY95	(Reserved)		
RWr0		Current page	RWw0		Page change request		
RWr1		01: A1 L1 1	RWw1		01: A1 L1 1		
RWr2		02: A1_L1_1	RWw2		02: A1_L1_1		
RWr3		01: A2_L1_1	RWw3		01: A2_L1_1		
RWr4		02: A2_L1_1	RWw4		02: A2_L1_1		
RWr5		01: A3_L1_1	RWw5		01: A3_L1_1		
RWr6	-	02: A3_L1_1	RWw6		02: A3_L1_1		
RWr7		01: A4_L1_1	RWw7		01: A4_L1_1		
RWr8	-	02: A4_L1_1	RWw8		02: A4_L1_1		
RWr9		(Unused)	RWw9		(Unused)		
RWr10		(Unused)	RWw10		(Unused)		
RWr11	-	(Unused)	RWw10		(Unused)		

Profile number 1 (Simple PID control with 3 connected controllers) (Ver.1.10, 4-station occupied)





Page 1

Pro	file numl	per 1 (Simple PID	control with 3 connected	l controller	s) on pag	e 1 (Ver.1.10, 4-station occupied)		
		IN area			OUT area			
		ave (UTAdvanced)	→ CC-Link master		CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents	of assignment	Word position	Bit position	Contents of assignment		
	RX0	Receive data valid			RY0	Rescan request		
	RX1	During-write			RY1	(Reserved)		
	RX2	Write acknowledge	ment		RY2	Write request		
	RX3	(Reserved)			RY3	(Reserved)		
	RX4	(Reserved)			RY4	(Reserved)		
	RX5	(Reserved)			RY5	(Reserved)		
	RX6	(Reserved)			RY6	(Reserved)		
	RX7	(Reserved)			RY7	(Reserved)		
	•		d-part is omitted number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)		
	RX47	Normal connection	slave (address 32)		RY47	Batch write request (address 32)		
	RX48	01: A.M			RY48	01: A.M		
	RX49	01: R.L_L1			RY49	01: R.L_L1		
	RX50	01: S.R			RY50	01: S.R		
	RX51	(Unused)			RY51	(Unused)		
	RX52	(Unused)			RY52	(Unused)		
	RX53	(Unused)			RY53	(Unused)		
	RX54	(Unused)			RY54	(Unused)		
	RX55	(Unused)			RY55	(Unused)		
	RX56	01: ALM1_L1			RY56	(Unused)		
	RX57	01: ALM2_L1			RY57	(Unused)		
	RX58	01: ALM3_L1			RY58	(Unused)		
	RX59	01: ALM4_L1			RY59	(Unused)		
	RX60	01: ALM5_L1			RY60	(Unused)		
	RX61	01: ALM6_L1	UT35A/UT32A:		RY61	(Unused)		
	RX62	01: ALM7_L1	unused		RY62	(Unused)		
	RX63	01: ALM8_L1			RY63	(Unused)		

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Name	Prof	file num	ber 1 (Simple PID	control with 3 connected	controllers	on pag	e 1 (Ver.1.10, 4-station occupied)			
Word Bit Contents of assignment Proposition Prosition										
Desilton Desilton Contents of assignment			ave (UTAdvanced)	→ CC-Link master			aster → CC-Link slave (UTAdvanced)			
RX64 02: AM			Contents	of assignment			Contents of assignment			
RX66 Q2 S.R RX68 (Unused) RX68 (Unused) RX69 (Unused) RX71 (Unused) RX71 (Unused) RX71 (Unused) RX72 Q2 ALML_L1 RX73 Q2 ALM_L1 RX75 Q2 ALM_L1 RX76 Q2 ALM_L1 RX77 Q2 ALM_L1 RX77 Q2 ALM_L1 RX77 Q2 ALM_L1 RX78 Q3 ALM_L1 RX79 Q2 ALM_L1 RX79 Q2 ALM_L1 RX79 Q2 ALM_L1 RX79 Q3 ALM_L1 RX88 Q3 ALM_L1 RX89 Q3 ALM_L1 RX89 Q3 ALM_L1 RX80 Q3 ALM_L1 RX80 Q3 ALM_L1 RX81 Q3 R_L1 RX81 Q3 R_L1 RX82 Q3 S.R RX84 (Unused) RX84 (Unused) RX85 (Unused) RX86 (Unused) RX86 (Unused) RX88 Q3 ALM_L1 RX89 Q3 ALM_L1 RX80 Q3 AL	pooluon		02: A.M		poolition		02: A.M			
RX67 Unused RX69 Unused RX70 Unused RX70 Unused RX70 Unused RX70 Unused RX71 Unused RX71 Unused RX72 02-ALM_L1 RX73 02-ALM_L1 RX74 02-ALM_L1 RX75 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 02-ALM_L1 RX76 03-ALM_L1 RX77 02-ALM_L1 UT35A/UT32A: RX77 Unused Unused RX77 Unused RX77 Unused Unused RX77 Unused Unused RX77 Unused Unused Unused RX77 Unused Unused Unused RX77 Unused Unused Unused RX77 Unused Un		RX65	02: R.L_L1			RY65	02: R.L_L1			
RX68 Unused RX70 Unused RX77 Unused RX71 Unused RX71 Unused RX71 Unused RX71 Unused RX71 Unused RX73 Unused RX74 Unused RX75 Unused RX76 Unused RX77 Unused Unused RX77 Unused Unused RX77 Unused RX77 Unused RX77 Unused Unused RX79 Unused Unused RX79 Unused Unused RX79 Unused Unused RX79 Unused Unused RX79 Unused Unused Unus		RX66	02: S.R			RY66	02: S.R			
RX99 Unused)		RX67	,			RY67	,			
RX70 (Unused) RX71 (Unused) RX71 (Unused) RX72 (02: ALM1_L1 RX73 02: ALM2_L1 RX73 02: ALM2_L1 RX74 02: ALM3_L1 RX75 02: ALM3_L1 RX76 02: ALM3_L1 RX76 02: ALM3_L1 RX77 02: ALM3_L1 UT35A/UT32A: U			,							
RX71 (Unused)			,				,			
RX72 02 - ALM L1			,							
RX73 Q2 ALM2 L1 RX74 Q2 ALM2 L1 RX75 Q2 ALM3 L1 RX75 Q2 ALM3 L1 RX76 Q2 ALM3 L1 RX76 Q2 ALM3 L1 RX77 Q2 ALM6 L1 RX77 Q2 ALM6 L1 RX78 Q2 ALM3 L1 UT35A/UT32A: unused RX78 Q2 ALM3 L1 RX80 Q3 ALM RX81 Q3 RL L1 RX82 Q3 S. R RX83 (Unused) RX86 Q3 S. R RX83 (Unused) RX86 Q1 S. R RX86 (Unused) RX86 Q1 S. R RX86 (Unused) RX87 (Unused) RX87 (Unused) RX87 (Unused) RX87 (Unused) RX87 (Unused) RX87 (Unused) RX87 (Unused) RX87 (Unused) RX88 (Unused) RX88 (Unused) RX87 (Unused) RX87 (Unused) RX88 (Unused) RX88 (Unused) RX88 (Unused) RX88 (Unused) RX89 (Un			, ,				,			
RX74 02: ALM3_L1 RX75 02: ALM4_L1 RX76 02: ALM4_L1 RX77 02: ALM6_L1 RX77 02: ALM6_L1 RX77 02: ALM6_L1 RX79 02: ALM6_L1 RX79 02: ALM6_L1 RX79 02: ALM8_L1 Unused RX79 02: ALM8_L1 RX79 02: ALM8_L1 Unused RX79 02: ALM8_L1 RX79 02: ALM8_L1 RX79 02: ALM8_L1 RX79 02: ALM8_L1 RX78 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 03: A.M RX81 RX81 03: A.M RX81 RX81 RX81 03: A.M RX81 RX8							7			
RX75 02: ALMS_L1										
RX76							,			
RX77 02: ALM6_L1							,			
RX78 02: ALM7_L1				UT35A/UT32A			,			
RX79 02: ALM8_L1 RX80 03: A.M RX81 03: R.L_1 RX82 03: S.R RX83 Unused) RX85 Unused) RX85 Unused) RX85 Unused) RX85 Unused) RX85 Unused) RX85 Unused) RX86 Unused) RX87 Unused) RX87 Unused) RX88 03: ALM1_L1 RX89 03: ALM2_L1 RX89 03: ALM3_L1 UT35AUT32A: Unused RX89 Unused) Unused)							,			
RX80 03: A.M RX81 03: R.L.L1 RX82 03: S.R RX83 (Unused) RX84 (Unused) RX85 (Unused) RX86 (Unused) RX86 (Unused) RX86 (Unused) RX87 (Unused) RX88 (Unused) RX88 (Unused) RX89 (Un							,			
RX81 03: R.L_1 RX82 03: S.R RX83 (Unused) RX84 (Unused) RX85 (Unused) RX85 (Unused) RX85 (Unused) RX86 (Unused) RX86 (Unused) RX87 (Unused) RX87 (Unused) RX88 03: ALM1_L1 RX89 03: ALM1_L1 RX89 03: ALM2_L1 RX89 03: ALM3_L1 RX89 (Unused) RX89 (Unuse										
RX82 Q3: S.R RX83 (Unused) RX84 (Unused) RX85 (Unused) RX85 (Unused) RX86 (Unused) RX86 (Unused) RX87 (Unused) RX88 Q3: ALM1_L1 RX89 Q3: ALM1_L1 RX89 Q3: ALM1_L1 RX89 Q3: ALM1_L1 RX90 Q3: ALM3_L1 RX91 Q3: ALM3_L1 RX93 Q3: ALM3_L1 RX93 Q3: ALM3_L1 RX93 Q3: ALM3_L1 RX93 Q3: ALM3_L1 RX93 Q3: ALM3_L1 RX96 (Unused) RX97 (Unused) RX97 (Unused										
RX83 (Unused) RX84 (Unused) RX85 (Unused) RX85 (Unused) RX86 (Unused) RX86 (Unused) RX87 (Unused) RX88 RX89			_							
RX84 (Unused) RX85 (Unused) RX85 (Unused) RX86 (Unused) RX87 (Unused) RX87 (Unused) RX88 (03. ALM_L1 RX89 (03. ALM_L1 RX89 (03. ALM_L1 RX99 (03. ALM_L1 RX91 (03. ALM_L1 RX91 (03. ALM_L1 RX92 (03. ALM_L1 RX93 (03. ALM_L1 RX93 (03. ALM_L1 RX94 (03. ALM_L1 RX95 (03. ALM_L1 RX95 (03. ALM_L1 RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX97 (Un										
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RX86 (Unused) RX97 (Unused) RX97 (Unused) RX98 (Unused) RX98 (Unused) RX98 (Unused) RX99 (Unused			,				,			
RX88 03: ALM1_L1 RX99 03: ALM2_L1 RX99 03: ALM2_L1 RX90 03: ALM3_L1 RX91 03: ALM4_L1 RX92 03: ALM4_L1 RX93 03: ALM4_L1 RX93 03: ALM6_L1 UT35A/UT32A: RX94 03: ALM7_L1 Unused RX95 Unused RX96 Unused RX96 Unused RX96 Unused RX96 Unused RX96 Unused RX96 Unused RX91 Unused RX96 Unused RX96 Unused RX96 Unused RX91 Unused RX96 Unused RX96 Unused RX96 Unused RX97 Unused RX98 Unused RX98 Unused RX98 Unused RX99 Un		RX86	,			RY86				
RX89 03: ALM2_L1 RX90 03: ALM3_L1 RX91 03: ALM4_L1 RX91 03: ALM4_L1 RX92 03: ALM5_L1 RX93 03: ALM5_L1 RX94 03: ALM5_L1 RX95 03: ALM5_L1 RX95 03: ALM5_L1 RX96 RX97 03: ALM5_L1 RX96 RX97 03: ALM5_L1 RX97 03: ALM5_L1 RX98		RX87	(Unused)			RY87	(Unused)			
RX90 03: ALM3_L1		RX88	03: ALM1_L1			RY88	(Unused)			
RX91 03: ALM4_L1 RX92 03: ALM5_L1 RX93 03: ALM6_L1 Unused RX94 RX94 03: ALM5_L1 Unused RX95 RX94 03: ALM7_L1 Unused RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX96 (Unused) RX97 (Unused) RX98 (Unused) RX99 (RX89	03: ALM2_L1			RY89	(Unused)			
RX92 03: ALM5_L1 UT35A/UT32A:		RX90	03: ALM3_L1			RY90	(Unused)			
RX93 03: ALM6_L1 UT35A/UT32A: unused R34 (Unused) R496 (Unused) R596 (Unused) R796		RX91	03: ALM4_L1			RY91	(Unused)			
RX94 03: ALM7_L1		RX92	03: ALM5_L1			RY92	(Unused)			
RX95 03: ALM8_L1 RX96 (Unused) RX96 (Unused) RY96 (Unused) RY96 (Unused) RY96 (Unused) RY911 (Unused) RY9112 (Reserved) RY112 (Reserved) RX112 (Reserved) RX123 Remote Ready RX123 Remote Ready RX127 (Reserved) RX127 (Reserved) RX127 (Reserved) RX127 (Reserved) RX127 (Reserved) RX127 (Reserved) RX127 (Reserved) RX127 (Reserved) RX127 (Reserved) RX127 (Reserved) RX127 (RESERVED)		RX93	03: ALM6_L1			RY93	(Unused)			
RX96 (Unused)		RX94	03: ALM7_L1	unused		R94	(Unused)			
RX111 (Unused) RX112 (Reserved) RY112 (RESERVED) RY112 (RESERV			03: ALM8_L1			RY95	(Unused)			
RX111 (Unused) RY112 (Reserved) RY123 (Reserved) RY123 (Reserved) RY127 (RESERVED) RY127 (RESERV		RX96	(Unused)			RY96	(Unused)			
RX112 (Reserved) RX123 (Reserved) RX123 (Reserved) RX123 (Reserved) RX127 (Rese		:				:				
RX112 (Reserved) RX123 (Reserved) RX123 (Reserved) RX123 (Reserved) RX127 (Rese		RX111	(Unused)			RY111	(Unused)			
RX123 Remote Ready RY123 (Reserved)										
RX123 Remote Ready RY123 (Reserved) RX127 (Reserved) RX127 (Reserved) RY127 (Ruw) (Unused) RWw3 (Unused) RWw3 (Unused) RWw3 (Unused) RWw4 O1: SP_L1_1 RWw6 O2: SP_L1_1 RWw6 O2: SP_L1_1 RWw6 O2: SP_L1_1 RWw6 O2: SP_L1_1 RWw6 O2: MOUT_L1 RWw11 O2: MOUT_L1 RWw11 O2: MOUT_L1 RWw11 O2: MOUT_L1 RWw12 O3: MOUT_L1 RWw13 O1: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw14 O2: MOUT_L1 RWw15 O3: MOUT_L1 RWw15 O3: MOUT_L1 RWw16 O3: MOUT_L1 RWw16 O3: MOUT_L1 RWw16 O3: MOUT_L1 RWw16 O3: MOUT_L1 RWw16										
RWr0 Current page RWr1 01: PV_L1 RWr2 02: PV_L1 RWr3 03: PV_L1 RWr4 01: CSP_L1 RWr5 02: CSP_L1 RWr6 03: CSP_L1 RWr6 03: CSP_L1 RWr8 02: OUT_L1 RWr9 03: OUT_L1 RWr9 03: OUT_L1 RWr10 01: H.OUT_L1 RWr11 02: H.OUT_L1 RWr12 03: H.OUT_L1 RWr12 03: H.OUT_L1 RWr13 01: C.OUT_L1 RWr14 02: C.OUT_L1 RWr14 02: C.OUT_L1 RWr14 02: C.OUT_L1 RWw14 01: MOUT_CL1 RWw15 02: MOUT_CL1 RWw16 03: MOUT_L1 RWw17 03: MOUT_L1 RWw18 03: MOUT_L1 RWw19 03: MOUT_L1 RWw10 01: MOUT_L1 RWw11 02: MOUT_L1 RWw11 02: MOUT_L1 RWw11 02: MOUT_L1 RWw12 03: MOUT_L1 RWw13 01: C.OUT_L1 RWw13 01: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 02: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWw14 03: MOUT_CL1 RWW14 03: MOUT_			Remote Ready				(Pasaryad)			
RWr0 Current page RWr1 01: PV_L1 RWr2 02: PV_L1 RWr3 03: PV_L1 RWr4 01: CSP_L1 RWr5 02: CSP_L1 RWr6 03: CSP_L1 RWr7 01: OUT_L1 RWr8 02: OUT_L1 RWr9 03: OUT_L1 RWr10 01: H.OUT_L1 RWr11 02: H.OUT_L1 RWr12 03: H.OUT_L1 RWr12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWr14 02: C.OUT_L1 RWr14 02: C.OUT_L1 RWr14 02: C.OUT_L1 RWr14 02: MOUT_CL1 RWw14 01: MOUT_CL1 RWw16 RWw16 RWw17 RWw17 RWw17 RWw18 RWw18 RWw19 RWw19 RWw19 RWw19 RWw19 RWw19 RWw19 RWw19 RWw4 RWw4 RWw40 RW			Tremote rready				((Neserved)			
RWr0 Current page RWw0 Page change request RWr1 01: PV_L1 RWw1 (Unused) RWr2 02: PV_L1 RWw2 (Unused) RWr3 03: PV_L1 RWw3 (Unused) RWr4 01: CSP_L1 RWw4 01: SP_L1_1 RWr5 02: CSP_L1 RWw5 02: SP_L1_1 RWr6 03: CSP_L1 RWw6 03: SP_L1_1 RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr9 03: MOUT_L1 RWw9 03: MOUT_L1 RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr12 03: H.OUT_L1 RWw11 02: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUT_C_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUT_C_L1			(D 1)				(D			
RWr1 01: PV_L1 RWw2 (Unused) RWr2 02: PV_L1 RWw2 (Unused) RWr3 03: PV_L1 RWw3 (Unused) RWr4 01: CSP_L1 RWw4 01: SP_L1_1 RWr5 02: CSP_L1 RWw5 02: SP_L1_1 RWr6 03: CSP_L1 RWw6 03: SP_L1_1 RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr10 01: H.OUT_L1 RWw9 03: MOUT_L1 RWr11 02: H.OUT_L1 RWw10 01: MOUT_L1 RWr12 03: H.OUT_L1 RWw11 02: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUT_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUT_L1		RX127	(reserved)			RY127	[(Reserved)			
RWr1 01: PV_L1 RWw2 (Unused) RWr2 02: PV_L1 RWw2 (Unused) RWr3 03: PV_L1 RWw3 (Unused) RWr4 01: CSP_L1 RWw4 01: SP_L1_1 RWr5 02: CSP_L1 RWw5 02: SP_L1_1 RWr6 03: CSP_L1 RWw6 03: SP_L1_1 RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr10 01: H.OUT_L1 RWw9 03: MOUT_L1 RWr11 02: H.OUT_L1 RWw10 01: MOUT_L1 RWr12 03: H.OUT_L1 RWw11 02: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUT_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUT_L1	DMrO		Current page		D\\/\u\C		Page change request			
RWr2 02: PV_L1 RWw2 (Unused) RWr3 03: PV_L1 RWw3 (Unused) RWr4 01: CSP_L1 RWw4 01: SP_L1_1 RWr5 02: CSP_L1 RWw5 02: SP_L1_1 RWr6 03: CSP_L1 RWw6 03: SP_L1_1 RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr10 01: H.OUT_L1 RWw9 03: MOUT_L1 RWr11 02: H.OUT_L1 RWw10 01: MOUT_L1 RWw12 03: MOUT_L1 RWw11 02: MOUT_L1 RWw12 03: MOUT_L1 RWw12 03: MOUT_L1 RWw13 01: C.OUT_L1 RWw13 01: MOUTC_L1 RWw14 02: C.OUT_L1 RWw14 02: MOUTC_L1							0 0			
RWr3 03: PV_L1 RWw3 (Unused) RWr4 01: CSP_L1 RWw4 01: SP_L1_1 RWr5 02: CSP_L1 RWw5 02: SP_L1_1 RWr6 03: CSP_L1 RWw6 03: SP_L1_1 RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr9 03: OUT_L1 RWw9 03: MOUT_L1 RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWw13 01: C.OUT_L1 RWw13 01: MOUT_C_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUT_C_L1	$\overline{}$,			
RWr4 01: CSP_L1 RWw4 01: SP_L1_1 RWr5 02: CSP_L1 RWw5 02: SP_L1_1 RWr6 03: CSP_L1 RWw6 03: SP_L1_1 RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr9 03: OUT_L1 RWw9 03: MOUT_L1 RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUT_C_L1 RWw14 02: MOUT_C_L1 RWw14 02: MOUT_C_L1	_						,			
RWr5 02: CSP_L1 RWw5 02: SP_L1_1 RWr6 03: CSP_L1 RWw6 03: SP_L1_1 RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr9 03: OUT_L1 RWw9 03: MOUT_L1 RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUT_C_L1 RWw14 02: MOUT_C_L1 RWw14 02: MOUT_C_L1	_				-		,			
RWr6 03: CSP_L1 RWw6 03: SP_L1_1 RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr9 03: OUT_L1 RWw9 03: MOUT_L1 RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUTC_L1 RWw14 02: MOUTC_L1 RWw14 02: MOUTC_L1										
RWr7 01: OUT_L1 RWw7 01: MOUT_L1 RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr9 03: OUT_L1 RWw9 03: MOUT_L1 RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUT_C_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUT_C_L1										
RWr8 02: OUT_L1 RWw8 02: MOUT_L1 RWr9 03: OUT_L1 RWw9 03: MOUT_L1 RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUTc_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUTc_L1	-		_							
RWr9 03: OUT_L1 RWw9 03: MOUT_L1 RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUTc_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUTc_L1	-						_			
RWr10 01: H.OUT_L1 RWw10 01: MOUT_L1 RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUTc_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUTc_L1	\vdash		_				_			
RWr11 02: H.OUT_L1 RWw11 02: MOUT_L1 RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUTc_L1 RWw14 02: C.OUT_L1 RWw14 02: MOUTc_L1	\vdash									
RWr12 03: H.OUT_L1 RWw12 03: MOUT_L1 RWr13 01: C.OUT_L1 RWw13 01: MOUTc_L1 RWw14 02: C.OUT_L1 RWw14 02: MOUTc_L1	-									
RWr13 01: C.OUT_L1 RWw13 01: MOUTc_L1 RWr14 02: C.OUT_L1 RWw14 02: MOUTc_L1										
RWr14 02: C.OUT_L1 RWw14 02: MOUTc_L1	\vdash									
RWr15 03: C.OUT_L1 RWw15 03: MOUTc_L1	_									
	RWr15		03: C.OUT_L1		RWw15		03: MOUTc_L1			

		IN area			e 2 (Ver.1.10, 4-station occupi
С	C-Link sl	ave (UTAdvanced) → CC-Link master	С	C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
		The fixed-part is omitted			The fixed-part is omitted
	•	(See profile number 0 on page 1)		•	(See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	:			:	
	RX123	Remote Ready			(Reserved)
	:			:	
	RX127	(Reserved)			(Reserved)
					,
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3		03: P_L1_1	RWw3		03: P_L1_1
RWr4		01: I_L1_1	RWw4		01: I_L1_1
RWr5		02: I_L1_1	RWw5		02: I_L1_1
RWr6		03: I_L1_1	RWw6		03: I_L1_1
RWr7		01: D_L1_1	RWw7		01: D_L1_1
RWr8		02: D_L1_1	RWw8		02: D_L1_1
RWr9		03: D_L1_1	RWw9		03: D_L1_1
RWr10		01: SPNO.	RWw10		01: SPNO.
RWr11		02: SPNO.	RWw11		02: SPNO.
RWr12		03: SPNO.	RWw12		03: SPNO.
RWr13		(Unused)	RWw13		(Unused)
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)

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Page 3

Pro	file num	per 1 (Simple PID control with 3 connect	ed contro	ollers	on pag	e 3 (Ver.1.10, 4-station occupied	
		IN area		-		OUT area	
C	C-Link sl	ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced			
Word position	Bit position	Contents of assignment		ord sition	Bit position	Contents of assignment	
	RX0	Receive data valid			RY0	Rescan request	
	RX1	During-write			RY1	(Reserved)	
	RX2	Write acknowledgement			RY2	Write request	
	RX3	(Reserved)			RY3	(Reserved)	
	RX4	(Reserved)			RY4	(Reserved)	
	RX5	(Reserved)			RY5	(Reserved)	
	RX6	(Reserved)			RY6	(Reserved)	
	RX7	(Reserved)			RY7	(Reserved)	
	•	The fixed-part is omitted			•	The fixed-part is omitted	
		(See profile number 0 on page 1)				(See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)	
	RX48	(Unused)			RY48	(Unused)	
	:				:		
	RX111	(Unused)	1		RY111	(Unused)	
	RX112	(Reserved)			RY112	(Reserved)	
	:				:		
	RX123	Remote Ready			RY123	(Reserved)	
	:				:		
	RX127	(Reserved)			RY127	(Reserved)	
RWr0		Current page	_	Ww0		Page change request	
RWr1		01: Pc_L1_1		Ww1		01: Pc_L1_1	
RWr2		02: Pc_L1_1		Ww2		02: Pc_L1_1	
RWr3		03: Pc_L1_1	4 1 1 1 1 1	Ww3		03: Pc_L1_1	
RWr4		01: lc_L1_1	4 <u>Fir</u>	Ww4		01: lc_L1_1	
RWr5		02: lc_L1_1	_ R\	Ww5		02: lc_L1_1	
RWr6		03: lc_L1_1		Ww6		03: lc_L1_1	
RWr7		01: Dc_L1_1	⊣	Ww7		01: Dc_L1_1	
RWr8		02: Dc_L1_1		8wW		02: Dc_L1_1	
RWr9		03: Dc_L1_1		Ww9		03: Dc_L1_1	
RWr10		01: SPNO.		Vw10		01: SPNO.	
RWr11		02: SPNO.	- <u> </u>	Vw11		02: SPNO.	
RWr12		03: SPNO.		Vw12		03: SPNO.	
RWr13		(Unused)		Vw13		(Unused)	
RWr14		(Unused)		Vw14		(Unused)	
RWr15		(Unused)	RV	Vw15		(Unused)	

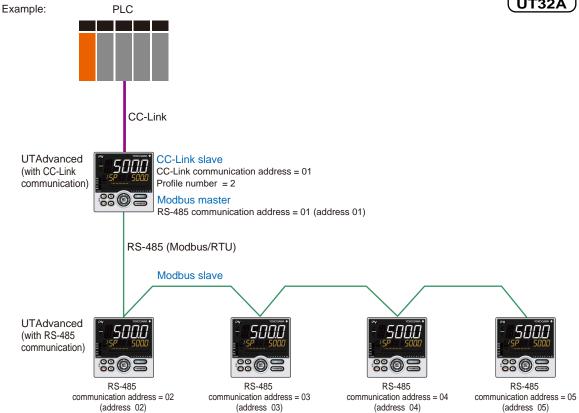
Pro	file num	ber 1 (Simple PID control with 3 connector	ed controllers) on pag	e 4 (Ver.1.10, 4-station occupied)		
		IN area		· · · ·	OUT area		
	C-Link sl	ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvance			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
	RX0	Receive data valid		RY0	Rescan request		
	RX1	During-write		RY1	(Reserved)		
	RX2	Write acknowledgement		RY2	Write request		
	RX3	(Reserved)		RY3	(Reserved)		
	RX4	(Reserved)		RY4	(Reserved)		
	RX5	(Reserved)		RY5	(Reserved)		
	RX6	(Reserved)		RY6	(Reserved)		
	RX7	(Reserved)		RY7	(Reserved)		
		The fixed-part is omitted			The fixed-part is omitted		
		(See profile number 0 on page 1)			(See profile number 0 on page 1)		
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)		
	RX48	(Unused)		RY48	(Unused)		
	:			:			
	RX111	(Unused)		RY111	(Unused)		
	RX112	(Reserved)		RY112	(Reserved)		
	•			:			
	RX123	Remote Ready		RY123	(Reserved)		
	:			:			
	RX127	(Reserved)		RY127	(Reserved)		
		,					
RWr0		Current page	RWw0		Page change request		
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1		
RWr2		02: A1_L1_1	RWw2		01: A1_L1_1		
RWr3		03: A1_L1_1	RWw3		03: A1_L1_1		
RWr4		01: A2_L1_1	RWw4		01: A2_L1_1		
RWr5		02: A2_L1_1	RWw5		02: A2_L1_1		
RWr6		03: A2_L1_1	RWw6		03: A2_L1_1		
RWr7		01: A3_L1_1	RWw7		01: A3_L1_1		
RWr8		02: A3_L1_1	RWw8		02: A3_L1_1		
RWr9		03: A3_L1_1	RWw9		03: A3_L1_1		
RWr10		01: A4_L1_1	RWw10		01: A4_L1_1		
RWr11		02: A4_L1_1	RWw11		02: A4_L1_1		
RWr12		03: A4_L1_1	RWw12		03: A4_L1_1		
RWr13		01: A5_L1_1	RWw13		01: A5_L1_1		
RWr14		02: A5_L1_1	RWw14		02: A5_L1_1 \rightarrow UT35A: unused		
RWr15		03: A5_L1_1	RWw15		03: A5_L1_1		

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Profile number 2 (Simple PID control with 5 connected controllers) (Ver.2.00, 1-station occupied x8 setting)





Page 1

Profile	number	2 (Simple PID cont	rol with 5 connected con	trol	lers) on p	page 1	(Ver.2.00, 1-station occupied x8 setting)	
		IN area			OUT area			
		ave (UTAdvanced)	→ CC-Link master		CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents	of assignment		Word position	Bit position	Contents of assignment	
	RX0	Receive data valid				RY0	Rescan request	
	RX1	During-write				RY1	(Reserved)	
	RX2	Write acknowledge	ment			RY2	Write request	
	RX3	(Reserved)				RY3	(Reserved)	
	RX4	(Reserved)				RY4	(Reserved)	
	RX5	(Reserved)				RY5	(Reserved)	
	RX6	(Reserved)				RY6	(Reserved)	
	RX7	(Reserved)				RY7	(Reserved)	
	•		d-part is omitted number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection	slave (address 32)			RY47	Batch write request (address 32)	
	RX48	01: A.M				RY48	01: A.M	
	RX49	01: R.L_L1				RY49	01: R.L_L1	
	RX50	01: S.R				RY50	01: S.R	
	RX51	01: ALM1_L1				RY51	(Unused)	
	RX52	01: ALM2_L1				RY52	(Unused)	
	RX53	01: ALM3_L1				RY53	(Unused)	
	RX54	01: ALM4_L1				RY54	(Unused)	
		01: ALM5_L1				RY55	(Unused)	
		01: ALM6_L1	UT35A/UT32A:			RY56	(Unused)	
		01: ALM7_L1	unused			RY57	(Unused)	
	RX58	01: ALM8_L1	J			RY58	(Unused)	
		02: A.M				RY59	02: A.M	
	RX60	02: R.L_L1				RY60	02: R.L_L1	
	_	02: S.R				RY61	02: S.R	
	RX62	02: ALM1_L1				RY62	(Unused)	
	RX63 02: ALM2_L1					RY63	(Unused)	

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CC-Link slave (UTAdvanced) → CC-Link master							
	Bit position	Conte	nts of assignment				
	RX64	02: ALM3_L1					
	RX65	02: ALM4_L1					
	RX66	02: ALM5_L1)				
	RX67	02: ALM6_L1	UT35A/UT32A:				
	RX68	02: ALM7_L1	unused				
	RX69	02: ALM8_L1					
	RX70	03: A.M	-				
	RX71	03: R.L L1					
	RX72	03: S.R					
	RX73	03: ALM1_L1					
	RX74	03: ALM2_L1					
	RX75	03: ALM3_L1					
_	RX76	03: ALM4 L1					
_	RX77	03: ALM5_L1)				
_	RX78	03: ALM6_L1	UT35A/UT32A:				
_	RX79	03: ALM7 L1	unused				
	RX80	03: ALM8 L1	- J				
	RX81	04: A.M					
	RX82	04: R.L L1					
	RX83	04: K.E_E1					
	RX84	04: ALM1 L1					
	RX85	04: ALM2 L1					
_	RX86	04: ALM3_L1					
_	RX87	04: ALM3_L1					
	RX88	04: ALM4_L1)				
	RX89	04: ALM6_L1	— UT35A/UT32A:				
_	RX90	04: ALM6_L1	- unused				
	RX91	04: ALM7_L1	_				
_	RX92	05: A.M					
	RX93	05: A.M 05: R.L L1					
	RX94	05: R.L L1					
	RX95	05: ALM1_L1					
_		05: ALM1_L1					
	RX96						
	RX97	05: ALM3_L1					
	RX98	05: ALM4_L1					
_	RX99	05: ALM5_L1	_ IIT25 \				
	-	05: ALM6_L1	UT35A/UT32A:				
	RX101	05: ALM7_L1	_ unuscu				
		05: ALM8_L1	J				
		(Unused)					
		(Unused)					
	1	(Unused)					
	i 	(Unused)					
	RX107	(Unused)					
	RX108	(Unused)					
	RX109	(Unused)					
	RX110	(Unused)					
	RX111	(Unused)					
	RX112	(Reserved)					
	:						
f	RX123	Remote Ready					
Ī	:	I					
	RX127						

Profile number 2 (Simple PID control with 5 connected controllers) on page 1

RWr0	Current page
RWr1	01: PV_L1
RWr2	02: PV_L1
RWr3	03: PV_L1
RWr4	04: PV_L1
RWr5	05: PV_L1
RWr6	01: CSP_L1
RWr7	02: CSP_L1
RWr8	03: CSP_L1

		OUT area
C	aster → CC-Link slave (UTAdvanced)	
Word	Bit	Contents of assignment
position	position	
	RY64	(Unused)
	RY65	(Unused)
	RY66	(Unused)
	RY67	(Unused)
	RY68	(Unused)
	RY69	(Unused)
	RY70	03: A.M
	RY71	03: R.L_L1
	RY72	03: S.R
	RY73	(Unused)
	RY74	(Unused)
	RY75	(Unused)
	RY76	(Unused)
	RY77	(Unused)
	RY78	(Unused)
	RY79	(Unused)
	RY80	(Unused)
	RY81	04: A.M
	RY82	04: R.L L1
	RY83	04: S.R
	RY84	(Unused)
	RY85	(Unused)
	RY86	(Unused)
	RY87	(Unused)
	RY88	(Unused)
	RY89	(Unused)
	RY90	(Unused)
	RY91	(Unused)
	RY92	05: A.M
	RY93	05: R.L_L1
	RY94	05: R.L_L1
	RY95	(Unused)
	RY96	(Unused)
	RY97	(Unused)
	RY98	(Unused)
	RY99	(Unused)
	RY100	(Unused)
	RY101	(Unused)
	RY102	(Unused)
		(Unused)
	RY104	(Unused)
	RY105	(Unused)
		(Unused)
	RY107	(Unused)
	RY108	(Unused)
	RY109	(Unused)
	RY1109	(Unused)
	RY111	(Unused)
	RY112	(Reserved)
	:	
	RY123	(Reserved)
	RY127	(Reserved)

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	(Unused)
RWw5	(Unused)
RWw6	01: SP_L1_1
RWw7	02: SP_L1_1
RWw8	03: SP_L1_1

Profile	number	2 (Simple PID control with 5 connected cor	itrol	lers) on p	page 1	(Ver.2.00, 1-station occupied x8 setting)		
		IN area				OUT area		
		ave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
RWr9		04: CSP_L1		RWw9		04: SP_L1_1		
RWr10		05: CSP_L1		RWw10		05: SP_L1_1		
RWr11		01: OUT_L1		RWw11		01: MOUT_L1		
RWr12		02: OUT_L1		RWw12		02: MOUT_L1		
RWr13		03: OUT_L1		RWw13		03: MOUT_L1		
RWr14		04: OUT_L1		RWw14		04: MOUT_L1		
RWr15		05: OUT_L1		RWw15		05: MOUT_L1		
RWr16		01: H.OUT_L1		RWw16		01: MOUT_L1		
RWr17		02: H.OUT_L1		RWw17		02: MOUT_L1		
RWr18		03: H.OUT_L1		RWw18		03: MOUT_L1		
RWr19		04: H.OUT_L1		RWw19		04: MOUT_L1		
RWr20		05: H.OUT_L1		RWw20		05: MOUT_L1		
RWr21		01: C.OUT_L1		RWw21		01: MOUTc_L1		
RWr22		02: C.OUT_L1		RWw22		02: MOUTc_L1		
RWr23		03: C.OUT_L1		RWw23		03: MOUTc_L1		
RWr24		04: C.OUT_L1		RWw24		04: MOUTc_L1		
RWr25		05: C.OUT_L1		RWw25		05: MOUTc_L1		
RWr26		(Unused)		RWw26		(Unused)		
RWr27		(Unused)		RWw27		(Unused)		
RWr28		(Unused)		RWw28		(Unused)		
RWr29		(Unused)		RWw29		(Unused)		
RW30		(Unused)		RWw30		(Unused)		
RWr31		(Unused)		RWw31		(Unused)		

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		IN area			OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master	l c	C-Link m	naster → CC-Link slave (UTAdvanced)
Word	Bit	,	Word	Bit	i i
osition	position	Contents of assignment	position	position	
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
		(chacca)	l		(Ondoca)
	:		l L	:	
		(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	RX123	Remote Ready		RY123	(Reserved)
	:			:	
	DV407	(Reserved)		DV407	(D = = = = = = = = = = = = = = = = = = =
	RX 127	(Reserved)		KT 127	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		Current page 01: P_L1_1	RWw1		01: P_L1_1
RWr2			RWw2		
RWr3		02: P_L1_1	1 -		02: P_L1_1
RWr4		03: P_L1_1	RWw3		03: P_L1_1 04: P_L1_1
		04: P_L1_1	1 -		
RWr5		05: P_L1_1	RWw5		05: P_L1_1
RWr6		01: I_L1_1	RWw6	-	01: I_L1_1
RWr7		02: I_L1_1	RWw7		02: I_L1_1
RWr8		03: I_L1_1	RWw8		03: I_L1_1
RWr9		04: I_L1_1	RWw9		04: I_L1_1
RWr10		05: I_L1_1	RWw10	-	05: I_L1_1
RWr11		01: D_L1_1	RWw11		01: D_L1_1
RWr12		02: D_L1_1	RWw12		02: D_L1_1
RWr13		03: D_L1_1	RWw13		03: D_L1_1
RWr14		04: D_L1_1	RWw14		04: D_L1_1
RWr15		05: D_L1_1	RWw15		05: D_L1_1
RWr16		01: SPNO.	RWw16		01: SPNO.
RWr17		02: SPNO.	RWw17	 	02: SPNO.
RWr18		03: SPNO.	RWw18	+	03: SPNO.
RWr19		04: SPNO.	RWw19	_	04: SPNO.
RWr20		05: SPNO.	RWw20		05: SPNO.
RWr21		(Unused)	RWw21	-	(Unused)
RWr22		(Unused)	RWw22		(Unused)
RWr23		(Unused)	RWw23		(Unused)
RWr24		(Unused)	RWw24		(Unused)
RWr25		(Unused)	RWw25		(Unused)
RWr26		(Unused)	RWw26		(Unused)
RWr27		(Unused)	RWw27		(Unused)
RWr28		(Unused)	RWw28		(Unused)
RWr29		(Unused)	RWw29		(Unused)
RW30		(Unused)	RWw30		(Unused)
RWr31		(Unused)	RWw31		(Unused)

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		IN area			OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master	C	C-Link m	aster → CC-Link slave (UTAdvanced)
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment
osition	position	Receive data valid	position	position RY0	
	RX0 RX1	During-write	+	RY1	Rescan request (Reserved)
	RX2	Write acknowledgement	1	RY2	Write request
	RX3	(Reserved)	1	RY3	(Reserved)
	RX4	(Reserved)	1	RY4	(Reserved)
	RX5	(Reserved)	1	RY5	(Reserved)
	RX6	(Reserved)	1	RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•			•	
		The fixed-part is omitted			The fixed-part is omitted
		(See profile number 0 on page 1)			(See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:	,	1	:	,
	- DV444	(/ / / / / / / / / / / / / / / / / / /	 	DV444	(1
	RX111	(Unused)	-	RY111	(Unused)
		(Reserved)		RY112	(Reserved)
	:			:	
	RX123	Remote Ready		RY123	(Reserved)
	:			:	
	RX127	(Reserved)		RY127	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1
RWr3		03: Pc_L1_1	RWw3		03: Pc_L1_1
RWr4		04: Pc_L1_1	RWw4		04: Pc_L1_1
RWr5		05: Pc_L1_1	RWw5		05: Pc_L1_1
RWr6		01: lc_L1_1	RWw6		01: lc_L1_1
RWr7		02: lc_L1_1	RWw7		02: lc_L1_1
RWr8		03: lc_L1_1	RWw8		03: lc_L1_1
RWr9		04: lc_L1_1	RWw9		04: lc_L1_1
RWr10		05: lc_L1_1	RWw10		05: lc_L1_1
RWr11		01: Dc_L1_1	RWw11		01: Dc_L1_1
RWr12		02: Dc_L1_1	RWw12		02: Dc_L1_1
RWr13		03: Dc_L1_1	RWw13		03: Dc_L1_1
RWr14		04: Dc_L1_1	RWw14		04: Dc_L1_1
RWr15		05: Dc_L1_1	RWw15		05: Dc_L1_1
RWr16		01: SPNO.	RWw16		01: SPNO.
RWr17		02: SPNO.	RWw17		02: SPNO.
RWr18		03: SPNO.	RWw18		03: SPNO.
RWr19		04: SPNO.	RWw19		04: SPNO.
RWr20		05: SPNO.	RWw20		05: SPNO.
RWr21		(Unused)	RWw21		(Unused)
RWr22		(Unused)	RWw22		(Unused)
RWr23		(Unused)	RWw23		(Unused)
RWr24		(Unused)	RWw24		(Unused)
RWr25		(Unused)	RWw25		(Unused)
RWr26		(Unused)	RWw26		(Unused)
RWr27		(Unused)	RWw27		(Unused)
RWr28		(Unused)	RWw28		(Unused)
RWr29		(Unused)	RWw29		(Unused)
RW30		(Unused)	RWw30		(Unused)
RWr31	<u> </u>	(Unused)	RWw31		(Unused)

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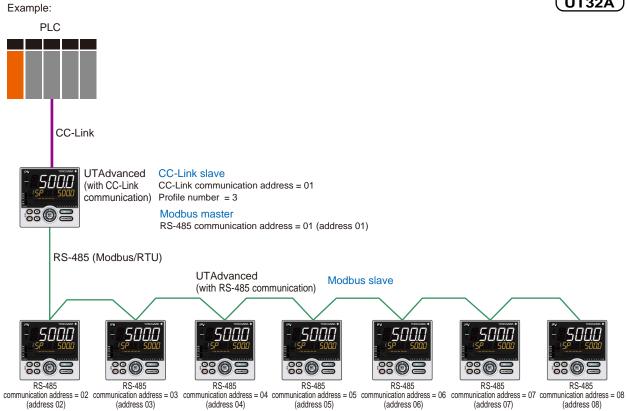
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		IN area			OUT area
		ave (UTAdvanced) → CC-Link master			aster → CC-Link slave (UTAdvanced)
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment
JSILIOII	RX0	Receive data valid	position	RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The Condensation of the d		•	The Control to collect
		The fixed-part is omitted			The fixed-part is omitted
		(See profile number 0 on page 1)			(See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
		(Upuped)	-	PV444	(Ulpused)
		(Unused)		RY111	(Unused)
	KX112	(Reserved)		RY112	(Reserved)
				:	
	RX123	Remote Ready		RY123	(Reserved)
	:			:	
	RX127	(Reserved)		RY127	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1
RWr2		02: A1_L1_1	RWw2		02: A1_L1_1
RWr3		03: A1_L1_1	RWw3		03: A1_L1_1
RWr4		04: A1_L1_1	RWw4		04: A1_L1_1
RWr5		05: A1_L1_1	RWw5		05: A1_L1_1
RWr6		01: A2_L1_1	RWw6		01: A2_L1_1
RWr7		02: A2_L1_1	RWw7		02: A2_L1_1
RWr8		03: A2_L1_1	RWw8		03: A2_L1_1
RWr9		04: A2_L1_1	RWw9		04: A2_L1_1
RWr10		05: A2_L1_1	RWw10		05: A2_L1_1
RWr11		01: A3_L1_1	RWw11		01: A3_L1_1
RWr12		02: A3_L1_1	RWw12		02: A3_L1_1
RWr13		03: A3_L1_1	RWw13		03: A3_L1_1
RWr14		04: A3_L1_1	RWw14		04: A3_L1_1
RWr15		05: A3_L1_1	RWw15		05: A3 L1 1
RWr16		01: A4_L1_1	RWw16		01: A4_L1_1
RWr17		02: A4_L1_1	RWw17		02: A4_L1_1
RWr18		03: A4_L1_1	RWw18		03: A4_L1_1
RWr19		04: A4_L1_1	RWw19		04: A4_L1_1
RWr20		05: A4_L1_1	RWw20		05: A4_L1_1
RWr21		01: A5_L1_1	RWw20		01: A5_L1_1
RWr22		02: A5_L1_1	RWw21		02: A5_L1_1
RWr23		03: A5_L1_1 UT35A/UT32A:	RWw22		03: A5_L1_1 UT35A/UT32A:
RWr24		03: A5_L1_1 unused	RWw23		04: A5_L1_1 unused
	-	05: A5 L1 1			
RWr25	-		RWw25 RWw26		05: A5_L1_1 J
RWr26		(Unused)	-		(Unused)
RWr27		(Unused)	RWw27		(Unused)
RWr28		(Unused)	RWw28		(Unused)
RWr29	-	(Unused)	RWw29		(Unused)
RW30		(Unused)	RWw30		(Unused)
RWr31	I	(Unused)	RWw31		(Unused)

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Profile number 3 (Simple PID control with 8 connected controllers) (Ver.2.00, 2-station occupied x8 setting)





Page 1

Profile	number	3 (Simple PID control with 8 connected cor	trol	lers) on p	page 1	(Ver.2.00, 2-station occupied x8 setting)
		IN area				OUT area
	CC-Link slave (UTAdvanced) → CC-Link master					aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	01: A.M			RY48	01: A.M
	RX49	01: R.L_L1			RY49	01: R.L_L1
	RX50	01: S.R			RY50	01: S.R
	RX51	(Unused)			RY51	(Unused)
	RX52	(Unused)			RY52	(Unused)
	RX53	(Unused)			RY53	(Unused)
	RX54	(Unused)			RY54	(Unused)
	RX55	(Unused)			RY55	(Unused)
	RX56	01: ALM1_L1			RY56	(Unused)
	RX57	01: ALM2_L1			RY57	(Unused)
	RX58	01: ALM3_L1			RY58	(Unused)
	RX59	01: ALM4_L1			RY59	(Unused)

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Profile	number	3 (Simple PID cont	rol with 8 connected cont	rollers) on	page 1	(Ver.2.00, 2-station occupied x8 setting)
		IN area				OUT area
Word	C-Link sl Bit	ave (UTAdvanced)		Word	C-Link m	aster → CC-Link slave (UTAdvanced)
	position		of assignment		position	
		01: ALM5_L1	UT35A/UT32A:		RY60	(Unused)
	RX61 RX62	01: ALM6_L1 01: ALM7_L1	unused		RY61 RY62	(Unused)
		01: ALM7_L1			RY63	(Unused)
	RX64	02: A.M			RY64	02: A.M
	RX65	02: R.L_L1			RY65	02: R.L_L1
	RX66	02: S.R			RY66	02: S.R
	RX67 RX68	(Unused)			RY67	(Unused)
	RX69	(Unused)			RY68 RY69	(Unused)
		(Unused)			RY70	(Unused)
	RX71	(Unused)			RY71	(Unused)
		02: ALM1_L1			RY72	(Unused)
	_	02: ALM2_L1			RY73	(Unused)
		02: ALM3_L1 02: ALM4_L1			RY74 RY75	(Unused)
		02: ALM4_L1	1		RY76	(Unused)
		02: ALM6_L1	UT35A/UT32A:		RY77	(Unused)
		02: ALM7_L1	unused		RY78	(Unused)
	RX79	02: ALM8_L1			RY79	(Unused)
		03: A.M			RY80	03: A.M
	RX81 RX82	03: R.L_L1 03: S.R			RY81 RY82	03: R.L_L1 03: S.R
	RX83	(Unused)			RY83	(Unused)
	RX84	(Unused)			RY84	(Unused)
	RX85	(Unused)			RY85	(Unused)
	RX86	(Unused)			RY86	(Unused)
	RX87	(Unused)			RY87	(Unused)
	RX88 RX89	03: ALM1_L1			RY88	(Unused)
	RX90	03: ALM2_L1 03: ALM3_L1			RY89 RY90	(Unused)
	RX91	03: ALM4_L1			RY91	(Unused)
	RX92	03: ALM5_L1			RY92	(Unused)
	RX93	03: ALM6_L1	UT35A/UT32A:		RY93	(Unused)
	RX94	03: ALM7_L1	unused		RY94	(Unused)
	RX95 RX96	03: ALM8_L1 04: A.M			RY95 RY96	(Unused) 04: A.M
	RX97	04: R.L_L1			RY97	04: R.L_L1
	RX98	04: S.R			RY98	04: S.R
	RX99	(Unused)			RY99	(Unused)
		(Unused)				(Unused)
		(Unused)			+	(Unused)
	_	(Unused)				(Unused)
		(Unused) 04: ALM1_L1			+	(Unused)
		04: ALM1_L1			+	(Unused)
	-	04: ALM3_L1			+	(Unused)
	-	04: ALM4_L1				(Unused)
		04: ALM5_L1			+	(Unused)
		04: ALM6_L1	UT35A/UT32A: unused		+	(Unused)
	-	04: ALM7_L1	unuseu			(Unused)
		04: ALM8_L1 05: A.M	,			(Unused) 05: A.M
		05: R.L_L1				05: R.L_L1
	_	05: S.R			_	05: S.R
	-	(Unused)				(Unused)
		(Unused)				(Unused)
	_	(Unused)			_	(Unused)
		(Unused)			+	(Unused)
		(Unused) 05: ALM1_L1			+	(Unused)
		05: ALM1_L1				(Unused)
		05: ALM3_L1			+	(Unused)
	-	05: ALM4_L1			+	(Unused)

Name	Profile	number	3 (Simple PID cont	rol with 8 connected cor	trollers) on page 1 (Ver.2.00, 2-station occupied x8 setting)				
Word Bit			<u> </u>			OUT area			
Docation Docation Contents of assignment			ave (UTAdvanced)	→ CC-Link master					
RX124 (0s.ALM.5.1)			Contents	of assignment			Contents of assignment		
RX126 0s. ALM7 1	poomon				position				
RY127 QS ALM8 L1 RY128 QG AM RY128 QG AM RY128 QG AM RY128 QG AM RY128 QG AM RY128 QG AM RY128 QG AM RY139 QG S RL L1 RY130 QG S RL L1 RY130 QG S RL RY131 QUussed RY132 QUussed RY132 QUussed RY132 Quussed RY132 Quussed RY133 Quussed RY133 Quussed RY133 Quussed RY134 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY136 Quussed RY147 Quussed RY147 Quussed RY147 Quussed RY148 Quussed RY149 Quussed		RX125	05: ALM6_L1	(:		RY125	(Unused)		
RX128 06: A.M RX139 06: S.R RX131 (Unused) RX131 (Unused) RX132 (Unused) RX133 (Unused) RX1440 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1450 (Unused) RX1550 (Unused				unused		+	,		
RX129 06: R. L. L1							/		
RX130 06: S. R RX131 (Unused) RX132 (Unused) RX133 (Unused) RX134 (Unused) RX145 (Unused) RX146 (Unused) RX146 (Unused) RX147 (Unused) RX148 (Unused) RX148 (Unused) RX149 (Unused) RX									
RX131 (Unused)						+			
RX132 (Unused)									
RX134 (Unused)			· /						
RX135 (Unused)		RX133	(Unused)			RY133	(Unused)		
RX136 Gis ALM 1.1 RX137 Gis ALM 2.1 RX138 Gis ALM 3.1 RX139 Gis ALM 3.1 RX139 Gis ALM 3.1 RX149 Gis ALM 4.1 RX1410 Gis ALM 6.1 RX1410 Gis ALM 6.1 UT35A/UT32A; Unused) RY143 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY144 (Unused) RY146 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX148 (Unused) RX151 (Unused) RX152 (Unused) RX153 (Unused) RX155 (Unused) RX155 (Unused) RX155 (Unused) RX158 (Unused) RX157 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX157 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused) RX158 (Unused)		RX134	(Unused)			RY134	(Unused)		
RX137 06. ALMZ_L1			,				,		
RX138 06: ALMS_L1						+	,		
RX139 06: ALM4_L1 RX140 06: ALM6_L1 UT35A/UT32A:						+			
RX140 06: ALM5_L1							(/		
RX141 06: ALM6_L1						+	,		
RX142 06: ALM7_L1				UT35A/UT32A:		+	` '		
RX144 07: A.M RY145 07: A.M RY146 07: A.M RY146 07: S.R RX147 (Unused) RX148 (Unused) RX148 (Unused) RX149 (Unused) RX149 (Unused) RX149 (Unused) RX149 (Unused) RX149 (Unused) RX151 (Unused) RX151 (Unused) RX152 07: ALM _ L1 RX153 07: ALM _ L1 RX154 07: ALM _ L1 RX155 07: ALM _ L1 RX156 07: ALM _ L1 RX157 07: ALM _ L1 Unused RX158 07: ALM _ L1 RX158 07: ALM _ L1 Unused RX159 07: ALM _ L1 RX159 07: ALM _ L1 Unused RX159 07: ALM _ L1 Unused RX159 07: ALM _ L1 Unused RX159 07: ALM _ L1 Unused RX159 07: ALM _ L1 Unused RX159 07: ALM _ L1 Unused RX159 07: ALM _ L1 Unused RX159 (Unused) RX1						+	,		
RX145 07: R.L_L1 RX146 07: S.R RX147 (Unused) RX148 (Unused) RX148 (Unused) RX149 (Unused) RX149 (Unused) RX149 (Unused) RX150 (Unused) RX151 (Unused) RX152 07: ALM1_L1 RX153 07: ALM2_L1 RX154 07: ALM3_L1 RX155 07: ALM3_L1 RX155 07: ALM3_L1 RX156 08: ALM3_L1 RX161 08: R.L_L1 RX162 08: S.R RX163 (Unused) RX164 (Unused) RX164 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX167 (Unused) RX167 (Unused) RX168 08: ALM1_L1 RX170 08: ALM3_L1 RX171 08: ALM3_L1 RX171 08: ALM3_L1 RX172 08: ALM3_L1 RX173 08: ALM3_L1 RX						RY143	(Unused)		
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RX147 (Unused)		RX145	07: R.L_L1			RY145	07: R.L_L1		
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RX150 (Unused)			,			_	,		
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RX153 07: ALM2_L1 RX154 07: ALM3_L1 RX155 07: ALM3_L1 RX155 07: ALM3_L1 RX155 07: ALM4_L1 RX155 07: ALM6_L1 RX156 07: ALM6_L1 RX159 07: ALM6_L1 RX159 07: ALM8_L1 RX160 08: A.M RX161 08: R.L_L1 RX162 08: S.R RX163 (Unused) RX163 (Unused) RX164 (Unused) RX165 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX168 08: ALM1_L1 RX169 08: ALM3_L1 RX170 08: ALM3_L1 RX171 08: ALM4_L1 RX171 (Unused) RX160 RX167 (Unused) RX168 (Unused) RX169 (Unused) RX169 (Unused) RX169 (Unused) RX169 (Unused) RX169 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX171 (Unused) RX172 (Unused) RX173 (Unused) RX174 (Unused) RX175 (Unused) RX175 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX1776 (Unused) RX176 (Unused) RX1776 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX17776 (Unused) RX17776 (Unused) RX17776 (Unused) RX177777777777777777777777777777			, ,			+	,		
RX155 07: ALM4_L1 RX156 07: ALM5_L1 RX157 07: ALM6_L1 Unused RX158 07: ALM5_L1 RX158 07: ALM7_L1 Unused RX159 07: ALM8_L1 Unused RX159 07: ALM8_L1 Unused RX159 07: ALM8_L1 RX160 08: A.M RX161 08: R.L L1 RX162 08: S.R RX163 (Unused) RX165 (Unused) RX165 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX166 (Unused) RX167 (Unused) RX168 08: ALM1_L1 RX169 08: ALM2_L1 RX170 08: ALM3_L1 RX171 08: ALM4_L1 RX171 08: ALM4_L1 RX172 08: ALM5_L1 RX173 08: ALM6_L1 Unused RX174 (Unused) RX175 (Unused) RX176 (Unused) RX177 (Unused)							,		
RX156 07: ALM5_L1 UT35A/UT32A: Unused RY157 (Unused) RY158 (Unused) RY158 (Unused) RY158 (Unused) RY158 (Unused) RY159 (Unused) RY159 (Unused) RY159 (Unused) RY159 (Unused) RY159 (Unused) RY159 (Unused) RY160 08: A.M RX161 08: R.L_L1 RX162 08: S.R RX163 (Unused) RX164 (Unused) RX165 (Unused) RX165 (Unused) RX165 (Unused) RX166 (Unused) RX166 (Unused) RX167 (Unused) RX168 (Unused) RX169 (Unused) RX169 (Unused) RX169 (Unused) RX169 (Unused) RX169 (Unused) RX170 (Unused) RX170 (Unused) RX171 (Unused) RX171 (Unused) RX172 (Unused) RX173 (Unused) RX174 (Unused) RX175 (Unused) RX176 (Unused) RX1772 (Unused) RX1773 (Unused) RX1773 (Unused) RX1774 (Unused) RX1775 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX1776 (Unused) RX1776 (Unused) RX1776 (Unused) RX17776 (Unused) RX177776 (Unused) RX1777777777777777777777777777777777777		RX154	07: ALM3_L1			RY154	(Unused)		
RX157 07: ALM6_L1 UT35A/UT32A: unused RY158 (Unused) RY159 (Unused) RY160 08: ALM RX161 08: R.L_L1 RX162 08: S.R RX163 (Unused) RY160 08: ALM RY160 08: ALM RY161 08: R.L_L1 RX162 08: S.R RX163 (Unused) RY165 (Unused) RY165 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY166 (Unused) RY167 (Unused) RY168 (Unused) RY169 (Unused) RY169 (Unused) RY170 (Unused) RY170 (Unused) RY170 (Unused) RY171 (Unused) RY171 (Unused) RY172 (Unused) RY173 (Unused) RY174 (Unused) RY175 (Unused) RY176 (Unused) RY1776 (Unused) RY1776 (Unused) RY17776 (Unused) RY1778 (Unused) RY1778 (Unused) RY1779 (Unused) (Unused) (Unused) (Unused) (Unused) (Unused) (Unused) (U		RX155	07: ALM4_L1			RY155	(Unused)		
RX158 07: ALM7_L1				LITO 5 A / LITO 5 A		+	,		
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RX160				unuscu		+	,		
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RX166 (Unused)		RX164	(Unused)			RY164	(Unused)		
RX167 (Unused)		RX165	(Unused)			RY165	(Unused)		
RX168 08: ALM1_L1 RX169 08: ALM2_L1 RX170 08: ALM3_L1 RX171 08: ALM4_L1 RX172 08: ALM4_L1 RX173 08: ALM6_L1 RX174 08: ALM7_L1 RX175 08: ALM8_L1 RX176 (Unused) (Unused) (, ,			+	` '		
RX169 08: ALM2_L1 RX170 08: ALM3_L1 RX171 08: ALM4_L1 RX172 08: ALM5_L1 RX173 08: ALM6_L1 RX174 08: ALM7_L1 RX175 08: ALM8_L1 RX176 (Unused) (Unused) (Unused			,			+	,		
RX170 08: ALM3_L1 RX171 08: ALM4_L1 RX172 08: ALM5_L1 RX173 08: ALM6_L1 RX174 08: ALM7_L1 RX175 08: ALM8_L1 RX176 (Unused) (Unused) RX176 (Unused)							` '		
RX171 08: ALM4_L1 RX172 08: ALM5_L1 RX173 08: ALM6_L1 RX174 08: ALM7_L1 RX175 08: ALM8_L1 RX176 (Unused) RY175 (Unused) RY175 (Unused) RY176 (Unused) RY176 (Unused) RY176 (Unused) RY176 (Unused) RY176 (Unused) RY176 (Unused) RY176 (Unused) RY176 (Unused) RY176 (Unused) RY176 (Unused) RY367 (Unused) RY368 (Reserved) RY368 (Reserved) RY379 (RY379 RY379 RY379 (RY379 RY379 RY379 RY379 (RY379 RY379					+	,			
RX172 08: ALM5_L1 UT35A/UT32A:			_			+	` '		
RX173						+			
RX175 08: ALM8_L1 RY175 (Unused) RX176 (Unused) RX176 (Unused) RX176 (Unused) RX367 (Unused) RX368 (Reserved) RX379 Remote Ready RX379 (Reserved) RX379 (Reser				UT35A/UT32A:		+	,		
RX176 (Unused) RY176 (Unused)		RX174	08: ALM7_L1	unused		RY174	(Unused)		
: : : : : : : : : : : : : : : : : : :									
RX367 (Unused) RY367 (Unused) RY368 (Reserved)			(Unused)			+	(Unused)		
RX368 (Reserved) RY368 (Reserved)		:				:			
RX379 Remote Ready RY379 (Reserved)		RX367	(Unused)			RY367	(Unused)		
RX379 Remote Ready : RY379 (Reserved) : :			(Reserved)			RY368	(Reserved)		
RX379 Remote Ready : RY379 (Reserved) : :		:				:			
		RX379	Remote Ready			RY379	(Reserved)		
RX383 (Reserved) RY383 (Reserved)						:			
		RX383	(Reserved)			RY383	(Reserved)		

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Profile r	number	3 (Simple PID control with 8 connected	controlle	rs) on p	page 1	(Ver.2.00, 2-station occupied x8 setting)			
		IN area				OUT area			
CC-Link slave (UTAdvanced) → CC-Link master					CC-Link master → CC-Link slave (UTAdvanced)				
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment			
position	position			oosition	position				
RWr0		Current page		RWw0		Page change request			
RWr1		01: PV L1	_	RWw1		(Unused)			
RWr2		02: PV L1		RWw2		(Unused)			
RWr3		03: PV L1	— ⊢	RWw3		(Unused)			
RWr4		04: PV L1		RWw4		(Unused)			
RWr5		05: PV L1		RWw5		(Unused)			
RWr6		06: PV L1	—	RWw6		(Unused)			
RWr7		07: PV L1		RWw7		(Unused)			
RWr8		08: PV_L1		RWw8		(Unused)			
RWr9		01: CSP_L1		RWw9		01: SP_L1_1			
RWr10		02: CSP_L1		RWw10		02: SP_L1_1			
RWr11		03: CSP_L1	—	RWw11		03: SP_L1_1			
RWr12		04: CSP_L1		RWw12		04: SP_L1_1			
RWr13		05: CSP_L1		RWw13		05: SP_L1_1			
RWr14		06: CSP_L1		RWw14		06: SP_L1_1			
RWr15		07: CSP_L1	F	RWw15		07: SP_L1_1			
RWr16		08: CSP_L1		RWw16		08: SP_L1_1			
RWr17		01: OUT_L1	F	RWw17		01: MOUT_L1			
RWr18		02: OUT_L1	F	RWw18		02: MOUT_L1			
RWr19		03: OUT_L1	F	RWw19		03: MOUT_L1			
RWr20		04: OUT_L1	F	RWw20		04: MOUT_L1			
RWr21		05: OUT_L1	F	RWw21		05: MOUT_L1			
RWr22		06: OUT_L1	F	RWw22		06: MOUT_L1			
RWr23		07: OUT_L1		RWw23		07: MOUT_L1			
RWr24		08: OUT_L1	F	RWw24		08: MOUT_L1			
RWr25		01: H.OUT_L1	F	RWw25		01: MOUT_L1			
RWr26		02: H.OUT_L1	F	RWw26		02: MOUT_L1			
RWr27		03: H.OUT_L1	F	RWw27		03: MOUT_L1			
RWr28		04: H.OUT_L1	F	RWw28		04: MOUT_L1			
RWr29		05: H.OUT_L1	F	RWw29		05: MOUT_L1			
RWr30		06: H.OUT_L1	F	RWw30		06: MOUT_L1			
RWr31		07: H.OUT_L1	F	RWw31		07: MOUT_L1			
RWr32		08: H.OUT_L1	F	RWw32		08: MOUT_L1			
RWr33		01: C.OUT_L1	[F	RWw33		01: MOUTc_L1			
RWr34		02: C.OUT_L1		RWw34		02: MOUTc_L1			
RWr35		03: C.OUT_L1	[RWw35		03: MOUTc_L1			
RWr36		04: C.OUT_L1		RWw36		04: MOUTc_L1			
RWr37		05: C.OUT_L1		RWw37		05: MOUTc_L1			
RWr38		06: C.OUT_L1	<u>[</u>	RWw38		06: MOUTc_L1			
RWr39		07: C.OUT_L1		RWw39		07: MOUTc_L1			
RWr40		08: C.OUT_L1	— ⊢	RWw40		08: MOUTc_L1			
RWr41		(Unused)	[F	RWw41		(Unused)			
:				<u>:</u>					
RWr63		(Unused)	F	RWw63		(Unused)			

Profile	number	3 (Simple PID control with 8 connected cor	trollers) on	page 2	(Ver.2.00, 2-station occupied x8 setting)
C	C-Link el	IN area ave (UTAdvanced) → CC-Link master		C-l ink m	OUT area aster → CC-Link slave (UTAdvanced)
Word	Bit	,	Word	Bit	·
position	position		position	position	
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2 RX3	Write acknowledgement (Reserved)		RY2 RY3	Write request (Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	:	The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Ratch write request (address 32)
	RX48	Normal connection slave (address 32) (Unused)		RY48	Batch write request (address 32) (Unused)
	•	(Onuseu)		_	(Onuseu)
	:			:	
		(Unused)		RY367	,
	RX368	(Reserved)		RY368	(Reserved)
	:				
	RX379	Remote Ready		RY379	(Reserved)
	:			:	
	RX383	(Reserved)		RY383	(Reserved)
		I -		,	I= .
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1	1	01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3 RWr4		03: P_L1_1	RWw3		03: P_L1_1
RWr5		04: P_L1_1 05: P_L1_1	RWw5	+	04: P_L1_1 05: P_L1_1
RWr6		06: P_L1_1	RWw6		06: P_L1_1
RWr7		07: P_L1_1	RWw7		07: P_L1_1
RWr8		08: P_L1_1	RWw8		08: P_L1_1
RWr9		01: I_L1_1	RWw9		01: I_L1_1
RWr10		02: I_L1_1	RWw10)	02: I_L1_1
RWr11		03: I_L1_1	RWw11		03: I_L1_1
RWr12		04: I_L1_1	RWw12	:	04: I_L1_1
RWr13		05: I_L1_1	RWw13		05: I_L1_1
RWr14		06: I_L1_1	RWw14		06: I_L1_1
RWr15		07: I_L1_1	RWw15	5	07: I_L1_1
RWr16		08: I_L1_1	RWw16	+	08: I_L1_1
RWr17		01: D_L1_1	RWw17	+	01: D_L1_1
RWr18		02: D_L1_1	RWw18		02: D_L1_1
RWr19		03: D_L1_1	RWw19	1	03: D_L1_1
RWr20		04: D_L1_1	RWw20	+	04: D_L1_1
RWr21		05: D_L1_1	RWw21	_	05: D_L1_1
RWr22		06: D_L1_1	RWw22	_	06: D_L1_1
RWr23	-	07: D_L1_1	RWw23	_	07: D_L1_1
RWr24	1	08: D_L1_1	RWw24	_	08: D_L1_1
RWr25 RWr26	-	01: SPNO. 02: SPNO.	RWw25	+	01: SPNO. 02: SPNO.
RWr27	+	03: SPNO.	RWw26	_	03: SPNO.
RWr28	-	04: SPNO.	RWw27	+	04: SPNO.
RWr29	+	05: SPNO.	RWw29	_	05: SPNO.
RWr30	+	06: SPNO.	RWw30	+	06: SPNO.
RWr31		07: SPNO.	RWw31	_	07: SPNO.
RWr32	1	08: SPNO.	RWw32	+	08: SPNO.
RWr33	<u> </u>	(Unused)	RWw33	_	(Unused)
:		1,	:		(
:	-		:	1	
RWr63		(Unused)	RWw63		(Unused)

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Page 3

_		IN area		OUT area				
		ave (UTAdvanced) → CC-Link master			aster → CC-Link slave (UTAdvanced)			
Word osition	Bit position	Contents of assignment	Word	Bit position	Contents of assignment			
	RX0	Receive data valid		RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	(Unused)		RY48	(Unused)			
	:			:				
	RX367	(Unused)		RY367	(Unused)			
		(Reserved)		RY368	(Reserved)			
	:	(1.000.400)		:	(1.0001V0u)			
	DV070	Davis at a David to		D)/0=0	(Danamara)			
	RX379	Remote Ready		RY379	(Reserved)			
	RX383	(Reserved)		RY383	(Reserved)			
		-			I			
RWr0		Current page	RWw0		Page change request			
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1			
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1			
RWr3		03: Pc_L1_1	RWw3		03: Pc_L1_1			
RWr4		04: Pc_L1_1	RWw4		04: Pc_L1_1			
RWr5		05: Pc_L1_1	RWw5		05: Pc_L1_1			
RWr6	-	06: Pc_L1_1	RWw6	-	06: Pc_L1_1			
RWr7		07: Pc_L1_1	RWw7		07: Pc_L1_1			
RWr8	-	08: Pc_L1_1	RWw8	-	08: Pc_L1_1			
RWr9		01: lc_L1_1	RWw9		01: lc_L1_1			
RWr10	-	02: lc_L1_1	RWw10	-	02: lc_L1_1			
RWr11		03: lc_L1_1	RWw11		03: lc_L1_1			
RWr12		04: lc_L1_1	RWw12	-	04: lc_L1_1			
RWr13		05: lc_L1_1	RWw13		05: lc_L1_1			
RWr14		06: lc_L1_1	RWw14	1	06: lc_L1_1			
RWr15		07: lc_L1_1	RWw15		07: lc_L1_1			
RWr16		08: lc_L1_1	RWw16		08: lc_L1_1			
RWr17	-	01: Dc_L1_1	RWw17	-	01: Dc_L1_1			
RWr18		02: Dc_L1_1	RWw18	 	02: Dc_L1_1			
RWr19	-	03: Dc_L1_1	RWw19		03: Dc_L1_1			
RWr20		04: Dc_L1_1	RWw20		04: Dc_L1_1			
RWr21	-	05: Dc_L1_1	RWw21	-	05: Dc_L1_1			
RWr22		06: Dc_L1_1	RWw22	 	06: Dc_L1_1			
RWr23 RWr24	-	07: Dc_L1_1	RWw23 RWw24	+	07: Dc_L1_1			
	-	08: Dc_L1_1	· -	+	08: Dc_L1_1			
RWr25 RWr26	-	01: SPNO.	RWw25	1	01: SPNO.			
RWr27	-	02: SPNO. 03: SPNO.	RWw26	-	02: SPNO.			
	-		RWw27 RWw28	-	03: SPNO.			
RWr28	-	04: SPNO.	l	-	04: SPNO.			
RWr29	-	05: SPNO.	RWw29	-	05: SPNO.			
RWr30		06: SPNO.	RWw30	-	06: SPNO.			
RWr31	-	07: SPNO.	RWw31	-	07: SPNO.			
RWr32	-	08: SPNO.	RWw32	-	08: SPNO.			
RWr33	-	(Unused)	RWw33	-	(Unused)			
:			:					
RWr63	ľ	(Unused)	RWw63		(Unused)			

Profile	number	3 (Simple PID control with 8 connected con	trol	lers) on p	page 4	(Ver.2.00, 2-station occupied x8 setting)	
	IN area				OUT area		
	CC-Link slave (UTAdvanced) → CC-Link master					aster → CC-Link slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
	RX0	Receive data valid			RY0	Rescan request	
	RX1	During-write			RY1	(Reserved)	
	RX2	Write acknowledgement			RY2	Write request	
	RX3	(Reserved)			RY3	(Reserved)	
	RX4	(Reserved)			RY4	(Reserved)	
	RX5	(Reserved)			RY5	(Reserved)	
	RX6	(Reserved)			RY6	(Reserved)	
	RX7	(Reserved)			RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)	
	RX48	(Unused)			RY48	(Unused)	
	:				:		
	RX367	(Unused)			RY367	(Unused)	
	RX368	(Reserved)			RY368	(Reserved)	
	:						
	RX379	Remote Ready			RY379	(Reserved)	
	RX383	(Reserved)			RY383	(Reserved)	

RWr0	Current page
RWr1	01: A1_L1_1
RWr2	02: A1_L1_1
RWr3	03: A1_L1_1
RWr4	04: A1_L1_1
RWr5	05: A1_L1_1
RWr6	06: A1_L1_1
RWr7	07: A1_L1_1
RWr8	08: A1_L1_1
RWr9	01: A2_L1_1
RWr10	02: A2_L1_1
RWr11	03: A2_L1_1
RWr12	04: A2_L1_1
RWr13	05: A2_L1_1
RWr14	06: A2_L1_1
RWr15	07: A2_L1_1
RWr16	08: A2_L1_1
RWr17	01: A3_L1_1
RWr18	02: A3_L1_1
RWr19	03: A3_L1_1
RWr20	04: A3_L1_1
RWr21	05: A3_L1_1
RWr22	06: A3_L1_1
RWr23	07: A3_L1_1
RWr24	08: A3_L1_1
RWr25	01: A4_L1_1
RWr26	02: A4_L1_1
RWr27	03: A4_L1_1
RWr28	04: A4_L1_1
RWr29	05: A4_L1_1
RWr30	06: A4_L1_1
RWr31	07: A4_L1_1
RWr32	08: A4_L1_1

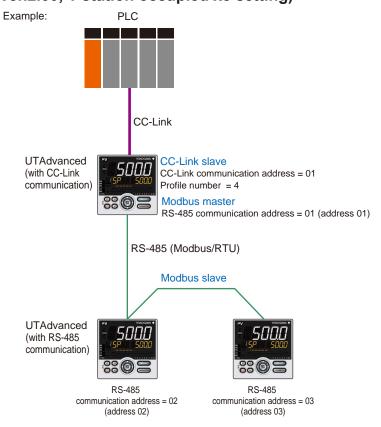
RWw0	Page change request
RWw1	01: A1_L1_1
RWw2	02: A1_L1_1
RWw3	03: A1_L1_1
RWw4	04: A1_L1_1
RWw5	05: A1_L1_1
RWw6	06: A1_L1_1
RWw7	07: A1_L1_1
RWw8	08: A1_L1_1
RWw9	01: A2_L1_1
RWw10	02: A2_L1_1
RWw11	03: A2_L1_1
RWw12	04: A2_L1_1
RWw13	05: A2_L1_1
RWw14	06: A2_L1_1
RWw15	07: A2_L1_1
RWw16	08: A2_L1_1
RWw17	01: A3_L1_1
RWw18	02: A3_L1_1
RWw19	03: A3_L1_1
RWw20	04: A3_L1_1
RWw21	05: A3_L1_1
RWw22	06: A3_L1_1
RWw23	07: A3_L1_1
RWw24	08: A3_L1_1
RWw25	01: A4_L1_1
RWw26	02: A4_L1_1
RWw27	03: A4_L1_1
RWw28	04: A4_L1_1
RWw29	05: A4_L1_1
RWw30	06: A4_L1_1
RWw31	07: A4_L1_1
RWw32	08: A4_L1_1

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Profile	number	3 (Simple PID conf	rol with 8 connected cor	itrol	lers) on p	page 4	(Ver.2.00, 2-stat	ion occupied x8 setting)
	IN area				OUT area			
C	C-Link sl	ave (UTAdvanced)	→ CC-Link master		C	C-Link m	aster → CC-Link s	lave (UTAdvanced)
Word position	Bit position	Contents	of assignment		Word position	Bit position	Content	s of assignment
RWr33		01: A5_L1_1			RWw33		01: A5_L1_1	
RWr34		02: A5_L1_1			RWw34		02: A5_L1_1	
RWr35		03: A5_L1_1			RWw35		03: A5_L1_1	
RWr36		04: A5_L1_1	UT35A/UT32A:		RWw36		04: A5_L1_1	UT35A/UT32A:
RWr37		05: A5_L1_1	unused		RWw37		05: A5_L1_1	unused
RWr38		06: A5_L1_1			RWw38		06: A5_L1_1	
RWr39		07: A5_L1_1			RWw39		07: A5_L1_1	
RWr40		08: A5_L1_1			RWw40		08: A5_L1_1	J
RWr41		(Unused)			RWw41		(Unused)	
:					:			
RWr63		(Unused)			RWw63		(Unused)	

Profile number 4 (Cascade control with 3 connected controllers) (Ver.2.00, 1-station occupied x8 setting)





Page 1

Profile	number	4 (Cascade control with 3 connected control	llers)	on pag	je 1	(Ver.2.00, 1-station occupied x8 setting)		
		IN area		OUT area				
C	C-Link sl	ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		Word osition	Bit position	Contents of assignment		
	RX0	Receive data valid			RY0	Rescan request		
	RX1	During-write			RY1	(Unused)		
	RX2	Write acknowledgement			RY2	Write request		
	RX3	(Reserved)			RY3	(Reserved)		
	RX4	(Reserved)			RY4	(Reserved)		
	RX5	(Reserved)			RY5	(Reserved)		
	RX6	(Reserved)			RY6	(Reserved)		
	RX7	(Reserved)			RY7	(Reserved)		
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)		
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)		
	RX48	01: R.L L1			RY48	01: R.L L1		
	RX49	01: S.R			RY49	01: S.R		
	RX50	01: ALM1_L1			RY50	(Unused)		
	RX51	01: ALM2_L1			RY51	(Unused)		
	RX52	01: ALM3_L1			RY52	(Unused)		
	RX53	01: ALM4_L1			RY53	(Unused)		
	RX54	01: ALM5_L1			RY54	(Unused)		
	RX55	01: ALM6_L1			RY55	(Unused)		
	RX56	01: ALM7_L1			RY56	(Unused)		
	RX57	01: ALM8_L1			RY57	(Unused)		
	RX58	01: ALM1_L2			RY58	(Unused)		
	RX59	01: ALM2_L2			RY59	(Unused)		
	RX60	01: ALM3_L2			RY60	(Unused)		
	RX61	01: ALM4_L2			RY61	(Unused)		
	RX62	01: ALM5_L2			RY62	(Unused)		
	RX63	01: ALM6_L2			RY63	(Unused)		

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(Ver.2.00, 1-station occupied x8 setting)

	IN area CC-Link slave (UTAdvanced) → CC-Link master					
Word	Bit					
	position	Contents of assignment				
	RX64	01: ALM7_L2				
	RX65	01: ALM8_L2				
	RX66	02: R.L_L1				
	RX67	02: S.R				
	RX68	02: ALM1_L1				
	RX69	02: ALM2_L1				
	RX70	02: ALM3_L1				
	RX71	02: ALM4_L1				
	RX72	02: ALM5_L1				
	RX73	02: ALM6_L1				
	RX74	02: ALM7_L1				
	RX75	02: ALM8_L1				
	RX76	02: ALM1_L2				
	RX77	02: ALM2_L2				
	RX78	02: ALM3_L2				
	RX79	02: ALM4_L2				
	RX80	02: ALM5_L2				
	RX81	02: ALM6_L2				
	RX82	02: ALM7_L2				
	RX83	02: ALM8_L2				
	RX84	03: R.L_L1				
	RX85	03: S.R				
	RX86	03: ALM1_L1				
	RX87	03: ALM2_L1				
	RX88	03: ALM3_L1				
	RX89	03: ALM4_L1				
	RX90	03: ALM5_L1				
	RX91	03: ALM6_L1				
	RX92	03: ALM7_L1				
	RX93	03: ALM8_L1				
	RX94	03: ALM1_L2				
	RX95	03: ALM2_L2				
	RX96	03: ALM3_L2				
	RX97	03: ALM4_L2				
	RX98	03: ALM5_L2				
	RX99	03: ALM6_L2				
	RX100	03: ALM7_L2				
	RX101	03: ALM8_L2				
	RX102	(Unused)				
	RX103	(Unused)				
	RX104	(Unused)				
	RX105	(Unused)				
	RX106	(Unused)				
	RX107	(Unused)				
	RX108	(Unused)				
	RX109	(Unused)				
	RX110	(Unused)				
	RX111	(Unused)				
	RX112	(Reserved)				
	:					
	RX123	Remote Ready				
		Tromote freauy				
	:	(5)				
	RX127	(Reserved)				

Profile number 4 (Cascade control with 3 connected controllers) on page 1

		OUT area
Word	C-Link m Bit	aster → CC-Link slave (UTAdvanced)
position	-	Contents of assignment
	RY64	(Unused)
	RY65	(Unused)
	RY66	02: R.L_L1
	RY67	02: S.R
	RY68	(Unused)
	RY69	(Unused)
	RY70	(Unused)
	RY71	(Unused)
	RY72	(Unused)
	RY73	(Unused)
	RY74	(Unused)
	RY75	(Unused)
	RY76	(Unused)
	RY77	(Unused)
	RY78	(Unused)
	RY79	(Unused)
	RY80	(Unused)
	RY81	(Unused)
	RY82	(Unused)
	RY83	(Unused)
	RY84	03: R.L_L1
	RY85	03: S.R
	RY86	(Unused)
	RY87	(Unused)
	RY88	(Unused)
	RY89	(Unused)
	RY90	(Unused)
	RY91	(Unused)
	RY92	(Unused)
	RY93	(Unused)
	R94	(Unused)
	RY95 RY96	(Unused)
		(Unused)
	RY97 RY98	(Unused)
	RY99	(Unused)
	RY100	(Unused)
	RY101	(Unused)
	RY102	(Unused)
	RY103	(Unused)
	RY104	(Unused)
		(Unused)
	RY106	(Unused)
	RY107	(Unused)
	RY108	(Unused)
	RY109	(Unused)
	RY110	(Unused)
	RY111	(Unused)
	RY112	(Reserved)
	:	
	RY123	(Reserved)
		(Inceserved)
	: :	(D)
	RY127	(Reserved)

RWr0	Current page
RWr1	01: PV_L1
RWr2	02: PV_L1
RWr3	03: PV_L1
RWr4	01: PV_L2
RWr5	02: PV_L2
RWr6	03: PV_L2
RWr7	01: CSP_L1
RWr8	02: CSP_L1

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	(Unused)
RWw5	(Unused)
RWw6	(Unused)
RWw7	01: SP_L1_1
RWw8	02: SP_L1_1

Profile	Profile number 4 (Cascade control with 3 connected control			s) on pag	ge 1	(Ver.2.00, 1-station occupied x8 setting)		
		IN area		OUT area				
	CC-Link slave (UTAdvanced) → CC-Link master				CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
RWr9		03: CSP_L1		RWw9		03: SP_L1_1		
RWr10		01: CSP_L2		RWw10		01: SP_L2_1		
RWr11		02: CSP_L2		RWw11		02: SP_L2_1		
RWr12		03: CSP_L2		RWw12		03: SP_L2_1		
RWr13		01: C.A.M		RWw13		01: C.A.M		
RWr14		02: C.A.M		RWw14		02: C.A.M		
RWr15		03: C.A.M		RWw15		03: C.A.M		
RWr16		01: OUT_L2		RWw16		01: MOUT_L2		
RWr17		02: OUT_L2		RWw17		02: MOUT_L2		
RWr18		03: OUT_L2		RWw18		03: MOUT_L2		
RWr19		01: H.OUT_L2		RWw19		01: MOUT_L2		
RWr20		02: H.OUT_L2		RWw20		02: MOUT_L2		
RWr21		03: H.OUT_L2		RWw21		03: MOUT_L2		
RWr22		01: C.OUT_L2		RWw22		01: MOUTc_L2		
RWr23		02: C.OUT_L2		RWw23		02: MOUTc_L2		
RWr24		03: C.OUT_L2		RWw24		03: MOUTc_L2		
RWr25		(Unused)		RWw25		(Unused)		
RWr26		(Unused)		RWw26		(Unused)		
RWr27		(Unused)		RWw27		(Unused)		
RWr28		(Unused)		RWw28		(Unused)		
RWr29		(Unused)		RWw29		(Unused)		
RW30		(Unused)		RWw30		(Unused)		
RWr31		(Unused)		RWw31		(Unused)		

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Page 2

		4 (Cascade control with 3 connected contr	1 1 1 1 1 1	J	(Ver.2.00, 1-station occupied x8 setting
C	C I interel	IN area		C Link m	001 4.04
Word	Bit	ave (UTAdvanced) → CC-Link master	Word	Bit	aster → CC-Link slave (UTAdvanced)
	position	Contents of assignment		position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted		•	The fixed-part is omitted
	•	(See profile number 0 on page 1)		•	(See profile number 0 on page 1)
	•			•	
		Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX111	(Unused)	1	RY111	(Unused)
		(Reserved)		RY112	(Reserved)
	:			:	
	PV123	Remote Ready	+	PV122	(Reserved)
	•	Nemote Ready		•	(Neserveu)
	:			:	
	RX127	(Reserved)		RY127	(Reserved)
=	1			1	-
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3		03: P_L1_1	RWw3		03: P_L1_1
RWr4		01: I_L1_1	RWw4		01: I_L1_1
RWr5		02: I_L1_1	RWw5		02: I_L1_1
RWr6		03: I_L1_1	RWw6		03: I_L1_1
RWr7		01: D_L1_1	RWw7		01: D_L1_1
RWr8		02: D_L1_1	RWw8		02: D_L1_1
RWr9		03: D_L1_1	RWw9		03: D_L1_1
RWr10		01: SPNO.	RWw10		01: SPNO.
RWr11		02: SPNO.	RWw11		02: SPNO.
RWr12		03: SPNO.	RWw12		03: SPNO.
RWr13		01: P_L2_1	RWw13		01: P_L2_1
RWr14		02: P_L2_1	RWw14		02: P_L2_1
RWr15		03: P_L2_1	RWw15		03: P_L2_1
RWr16		01: I_L2_1	RWw16		01: I_L2_1
RWr17		02: I_L2_1	RWw17		02: I_L2_1
RWr18		03: I_L2_1	RWw18		03: I_L2_1
RWr19		01: D_L2_1	RWw19		01: D_L2_1
RWr20		02: D_L2_1	RWw20		02: D_L2_1
RWr21		03: D_L2_1	RWw21		03: D_L2_1
RWr22		(Unused)	RWw22		(Unused)
RWr23		(Unused)	RWw23		(Unused)
RWr24		(Unused)	RWw24		(Unused)
RWr25		(Unused)	RWw25		(Unused)
RWr26		(Unused)	RWw26		(Unused)
RWr27		(Unused)	RWw27	İ	(Unused)
RWr28		(Unused)	RWw28		(Unused)
RWr29		(Unused)	RWw29	İ	(Unused)
RW30		(Unused)	RWw30		(Unused)
RWr31		(Unused)	RWw31		(Unused)

		IN area			OUT area	
		ave (UTAdvanced) → CC-Link master			aster → CC-Link slave (UTAdvanced)	
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
	RX0	Receive data valid		RY0	Rescan request	
	RX1	During-write		RY1	(Reserved)	
	RX2	Write acknowledgement		RY2	Write request	
	RX3	(Reserved)		RY3	(Reserved)	
	RX4	(Reserved)		RY4	(Reserved)	
	RX5	(Reserved)		RY5	(Reserved)	
	RX6	(Reserved)		RY6	(Reserved)	
	RX7	(Reserved)		RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)	
	RX48	(Unused)		RY48	(Unused)	
	•	(Onuseu)		_	(Onuseu)	
	:			:		
	RX111	(Unused)		RY111	(Unused)	
	RX112	(Reserved)		RY112	(Reserved)	
				:		
	RX123	Remote Ready		RY123	(Reserved)	
	•	Tromoto rroday		:	(110001100)	
	:	(5)		:	-	
	RX127	(Reserved)		RY127	(Reserved)	
RWr0		Current page	RWw0		Page change request	
RWr1		(Unused)	RWw1		(Unused)	
RWr2		(Unused)	RWw2		(Unused)	
RWr3		(Unused)	RWw3		(Unused)	
RWr4		(Unused)	RWw4		(Unused)	
RWr5		(Unused)	RWw5		(Unused)	
RWr6		(Unused)	RWw6		(Unused)	
RWr7		(Unused)	RWw7		(Unused)	
RWr8		(Unused)	RWw8		(Unused)	
RWr9		(Unused)	RWw9		(Unused)	
RWr10		01: SPNO.	RWw10		,	
RWr11			RWw10		01: SPNO.	
		02: SPNO.	· -		02: SPNO.	
RWr12		03: SPNO.	RWw12		03: SPNO.	
RWr13		01: Pc_L2_1	RWw13		01: Pc_L2_1	
RWr14		02: Pc_L2_1	RWw14		02: Pc_L2_1	
RWr15		03: Pc_L2_1	RWw15	-	03: Pc_L2_1	
RWr16		01: lc_L2_1	RWw16	+	01: lc_L2_1	
RWr17		02: lc_L2_1	RWw17	-	02: Ic_L2_1	
RWr18		03: lc_L2_1	RWw18	+	03: lc_L2_1	
RWr19		01: Dc_L2_1	RWw19	_	01: Dc_L2_1	
RWr20		02: Dc_L2_1	RWw20		02: Dc_L2_1	
RWr21		03: Dc_L2_1	RWw21		03: Dc_L2_1	
RWr22		(Unused)	RWw22		(Unused)	
RWr23		(Unused)	RWw23	_	(Unused)	
RWr24		(Unused)	RWw24		(Unused)	
RWr25		(Unused)	RWw25		(Unused)	
RWr26		(Unused)	RWw26		(Unused)	
RWr27		(Unused)	RWw27		(Unused)	
RWr28		(Unused)	RWw28		(Unused)	
RWr29		(Unused)	RWw29		(Unused)	
RW30		(Unused)	RWw30		(Unused)	
RWr31		(Unused)	RWw31		(Unused)	

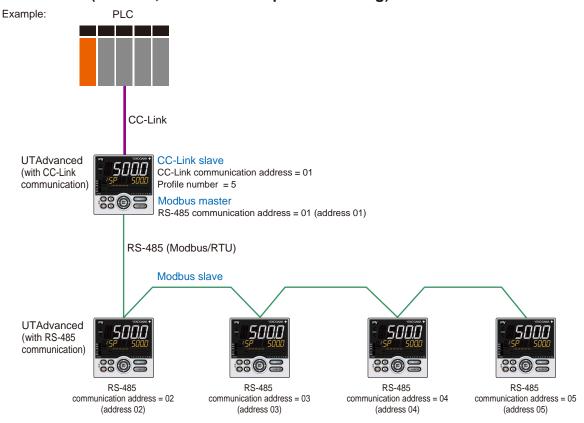
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Profile number 4 (Cascade control with 3 connected control IN area				-	OUT area			
C	C-I ink sl	ave (UTAdvanced) → CC-Link master	C	CC-Link master → CC-Link slave (UTAdvanced)				
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment			
position	RX0	Receive data valid	position	RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)	1	RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
				•				
		The fixed-part is omitted			The fixed-part is omitted			
		(See profile number 0 on page 1)			(See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	(Unused)		RY48	(Unused)			
	:			:				
	DV444			D)////				
		(Unused)		RY111	(Unused)			
	RX112	(Reserved)		RY112	(Reserved)			
	:			:				
	RX123	Remote Ready		RY123	(Reserved)			
	:			:				
	RX127	(Reserved)		RY127	(Reserved)			
	TOTIL	((toosived)		101127	(110001100)			
RWr0		Current page	RWw0		Page change request			
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1			
RWr2		02: A1_L1_1	RWw2		02: A1_L1_1			
RWr3		03: A1_L1_1	RWw3		03: A1_L1_1			
RWr4		01: A2_L1_1	RWw4		01: A2_L1_1			
RWr5		02: A2_L1_1	RWw5		02: A2_L1_1			
RWr6		03: A2_L1_1	RWw6		03: A2_L1_1			
RWr7		01: A3_L1_1	RWw7		01: A3_L1_1			
RWr8		02: A3_L1_1	RWw8		02: A3_L1_1			
RWr9		03: A3_L1_1	RWw9		03: A3_L1_1			
RWr10		01: A4_L1_1	RWw10		01: A4_L1_1			
RWr11		02: A4_L1_1	RWw10		02: A4_L1_1			
RWr12		03: A4_L1_1	RWw12		03: A4_L1_1			
RWr13		01: A5_L1_1	RWw13		01: A5_L1_1			
RWr14		02: A5_L1_1	RWw14		02: A5_L1_1			
RWr15		03: A5_L1_1	RWw15		03: A5_L1_1			
RWr16		01: A1_L2_1			01: A1 L2 1			
RWr17	-	02: A1_L2_1	RWw16 RWw17	-	02: A1_L2_1			
RWr18			RWw17		03: A1_L2_1			
	_	03: A1_L2_1		 	01: A2_L2_1			
RWr19	-	01: A2_L2_1	RWw19	-				
RWr20	-	02: A2_L2_1	RWw20	-	02: A2_L2_1			
RWr21	-	03: A2_L2_1	RWw21		03: A2_L2_1			
RWr22	-	01: A3_L2_1	RWw22		01: A3_L2_1			
RWr23	-	02: A3_L2_1	RWw23		02: A3_L2_1			
RWr24		03: A3_L2_1	RWw24	-	03: A3_L2_1			
RWr25		01: A4_L2_1	RWw25		01: A4_L2_1			
RWr26		03: A5_L2_1	RWw26		02: A4_L2_1			
RWr27		03: A4_L2_1	RWw27		03: A4_L2_1			
RWr28		01: A5_L2_1	RWw28		01: A5_L2_1			
RWr29		02: A5_L2_1	RWw29		02: A5_L2_1			
RW30		03: A5_L2_1	RWw30		03: A5_L2_1			
RWr31	1	(Reserved)	RWw31		(Reserved)			

Profile number 5 (Cascade control with 5 connected controllers) (Ver.2.00, 2-station occupied x8 setting)





Page 1

rionie	Profile number 5 (Cascade control with 5 connected control IN area				ollers) on page 1 (Ver.2.00, 2-station occupied x8 setting)				
0	C I ink al			001 000					
CC-Link slave (UTAdvanced) → CC-Link master Word Bit			-	CC-Link master → CC-Link slave (UTAdvanced) Word Bit					
	position	Contents of assignment			position	Contents of assignment			
	RX0	Receive data valid			RY0	Rescan request			
	RX1	During-write			RY1	(Unused)			
	RX2	Write acknowledgement			RY2	Write request			
	RX3	(Reserved)			RY3	(Reserved)			
	RX4	(Reserved)			RY4	(Reserved)			
	RX5	(Reserved)			RY5	(Reserved)			
	RX6	(Reserved)			RY6	(Reserved)			
	RX7	(Reserved)			RY7	(Reserved)			
	•	The fixed-part is omitted			•	The fixed-part is omitted			
	•	(See profile number 0 on page 1)			•	(See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)			
		(Unused)			RY48	(Unused)			
	RX49	01: R.L L1			RY49	01: R.L L1			
		01: S.R			RY50	01: S.R			
	RX51	(Unused)			RY51	(Unused)			
	_	(Unused)			RY52	(Unused)			
	RX53	(Unused)			RY53	(Unused)			
	RX54	(Unused)			RY54	(Unused)			
	RX55	(Unused)			RY55	(Unused)			
	RX56	01: ALM1_L1	İ		RY56	(Unused)			
	RX57	01: ALM2_L1	ĺ		RY57	(Unused)			
	RX58	01: ALM3_L1	1		RY58	(Unused)			
	RX59	01: ALM4_L1			RY59	(Unused)			
	RX60	01: ALM5_L1	1		RY60	(Unused)			
	RX61	01: ALM6_L1	1		RY61	(Unused)			
	RX62	01: ALM7_L1	1		RY62	(Unused)			
	RX63	01: ALM8_L1	1		RY63	(Unused)			

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Profile	number	5 (Cascade control with 5 connected control	oller	s) on pag	ge 1	(Ver.2.00, 2-station occupied x8 setting)
		IN area				OUT area
Word	C-Link sla	ave (UTAdvanced) → CC-Link master		Word	C-Link m Bit	aster → CC-Link slave (UTAdvanced)
	position	Contents of assignment			position	Contents of assignment
		(Unused)			RY64 RY65	(Unused)
		(Unused)				(Unused)
		(Unused)			RY67	(Unused)
		(Unused)			RY68	(Unused)
		(Unused)			RY69 RY70	(Unused)
		(Unused)			RY71	(Unused)
	RX72	01: ALM1_L2			RY72	(Unused)
		01: ALM2_L2			RY73	(Unused)
		01: ALM3_L2 01: ALM4 L2			RY75	(Unused)
		01: ALM5_L2			RY76	(Unused)
		01: ALM6_L2			RY77	(Unused)
		01: ALM7_L2			RY78	(Unused)
		01: ALM8_L2			RY79	(Unused)
		(Unused) 02: R.L_L1			RY80 RY81	(Unused) 02: R.L L1
		02: S.R			RY82	02: S.R
		(Unused)			RY83	(Unused)
	RX84	(Unused)			RY84	(Unused)
		(Unused)			RY85	(Unused)
		(Unused)			RY86	(Unused)
		(Unused) 02: ALM1 L1			RY87 RY88	(Unused)
		02: ALM2 L1			RY89	(Unused)
	_	02: ALM3_L1			RY90	(Unused)
	RX91	02: ALM4_L1			RY91	(Unused)
		02: ALM5_L1			RY92	(Unused)
		02: ALM6_L1			RY93	(Unused)
		02: ALM7_L1 02: ALM8 L1			RY94 RY95	(Unused)
		(Unused)			RY96	(Unused)
	RX97	(Unused)			RY97	(Unused)
		(Unused)			RY98	(Unused)
		(Unused)			RY99	(Unused)
		(Unused)				(Unused)
		(Unused)				(Unused)
		(Unused)				(Unused)
	RX104	02: ALM1_L2				(Unused)
		02: ALM2_L2				(Unused)
		02: ALM3_L2				(Unused)
		02: ALM4_L2 02: ALM5 L2				(Unused)
		02: ALM6_L2				(Unused)
		02: ALM7_L2				(Unused)
		02: ALM8_L2				(Unused)
		(Unused)				(Unused)
		03: R.L_L1				03: R.L_L1
		03: S.R (Unused)				03: S.R (Unused)
		(Unused)				(Unused)
		(Unused)				(Unused)
		(Unused)				(Unused)
		(Unused)				(Unused)
		03: ALM1_L1				(Unused)
		03: ALM2_L1 03: ALM3_L1				(Unused)
		03: ALM3_L1 03: ALM4_L1				(Unused)
		03: ALM5_L1				(Unused)
		03: ALM6_L1				(Unused)
		03: ALM7_L1				(Unused)
	RX127	03: ALM8_L1			RY127	(Unused)

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Profile	number	5 (Cascade control with 5 connected control	oller	s) on pag	ge 1	(Ver.2.00, 2-station occupied x8 setting)
		IN area			-	OUT area
С	C-Link sl	ave (UTAdvanced) → CC-Link master		С	C-Link m	aster → CC-Link slave (UTAdvanced)
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment
position	position	(Unused)		position	position	(Unused)
		(Unused)			_	(Unused)
		(Unused)			-	(Unused)
	+	(Unused)			_	(Unused)
		(Unused)			-	(Unused)
	_	(Unused)			-	(Unused)
		(Unused)			-	(Unused)
		(Unused)			RY135	(Unused)
	RX136	03: ALM1_L2			RY136	(Unused)
	RX137	03: ALM2_L2			RY137	(Unused)
	RX138	03: ALM3_L2			RY138	(Unused)
	RX139	03: ALM4_L2			RY139	(Unused)
	RX140	03: ALM5_L2			RY140	(Unused)
	RX141	03: ALM6_L2			RY141	(Unused)
	RX142	03: ALM7_L2			RY142	(Unused)
	RX143	03: ALM8_L2			RY143	(Unused)
	RX144	(Unused)			RY144	(Unused)
	+	04: R.L_L1				04: R.L_L1
		04: S.R			-	04: S.R
	RX147	(Unused)				(Unused)
	RX148	(Unused)				(Unused)
	RX149	(Unused)			RY149	(Unused)
	RX150	(Unused)			-	(Unused)
	_	(Unused)			_	(Unused)
		04: ALM1_L1			-	(Unused)
		04: ALM2_L1			_	(Unused)
	+	04: ALM3_L1			-	(Unused)
		04: ALM4_L1				(Unused)
	+	04: ALM5_L1			<u> </u>	(Unused)
	_	04: ALM6_L1			-	(Unused)
	+	04: ALM7_L1			_	(Unused)
		04: ALM8_L1				(Unused)
		(Unused)				(Unused)
	+	(Unused)			-	(Unused)
	_	(Unused)			_	(Unused)
		(Unused)			-	(Unused)
		(Unused)			_	(Unused)
		(Unused)			-	(Unused)
	_	(Unused)			-	(Unused)
		04: ALM1_L2				(Unused)
	+	04: ALM2 L2			†	(Unused)
	+	04: ALM3_L2				(Unused)
	_	04: ALM4_L2				(Unused)
	+	04: ALM5_L2				(Unused)
		04: ALM6_L2				(Unused)
	†	04: ALM7_L2			_	(Unused)
	+	04: ALM8_L2				(Unused)
		(Unused)				(Unused)
	+	05: R.L_L1			-	05: R.L_L1
	RX178	05: S.R				05: S.R
	+	(Unused)			-	(Unused)
	RX180	(Unused)			RY180	(Unused)
	RX181	(Unused)			RY181	(Unused)
	RX182	(Unused)			RY182	(Unused)
	RX183	(Unused)			RY183	(Unused)
	RX184	05: ALM1_L1			RY184	(Unused)
		05: ALM2_L1			RY185	(Unused)
	RX186	05: ALM3_L1			RY186	(Unused)
	RX187	05: ALM4_L1			RY187	(Unused)
	RX188	05: ALM5_L1			RY188	(Unused)
	RX189	05: ALM6_L1			RY189	(Unused)
	RX190	05: ALM7_L1			RY190	(Unused)
	RX191	05: ALM8_L1			RY191	(Unused)

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(Ver.2.00, 2-station occupied x8 setting)

	IN area						
		ave (UTAdvanced) → CC-Link master					
Word	Bit position	Contents of assignment					
pecinen		(Unused)					
	RX193	(Unused)					
	RX194	(Unused)					
	RX195	(Unused)					
	RX196	(Unused)					
	RX197	(Unused)					
	RX198	(Unused)					
	RX199	(Unused)					
	RX200	05: ALM1_L2					
	RX201	05: ALM2_L2					
	RX202	05: ALM3_L2					
	RX203	05: ALM4_L2					
	RX204	05: ALM5_L2					
	RX205	05: ALM6_L2					
	RX206	05: ALM7_L2					
	RX207	05: ALM8_L2					
	RX208	(Unused)					
	:						
	RX367	(Unused)					
	RX368	(Reserved)					
	:						
	RX379	Remote Ready					
	:						
	RX383	(Reserved)					

Profile number 5 (Cascade control with 5 connected controllers) on page 1

	OUT area							
		aster → CC-Link slave (UTAdvanced)						
Word	Bit	Contents of assignment						
position								
	RY192	(Unused)						
\vdash	RY193	(Unused)						
	RY194	(Unused)						
	RY195	(Unused)						
	RY196	(Unused)						
	RY197	(Unused)						
	RY198	(Unused)						
	RY199	(Unused)						
	RY200	(Unused)						
	RY201	(Unused)						
	RY202	(Unused)						
	RY203	(Unused)						
	RY204	(Unused)						
	RY205	(Unused)						
	RY206	(Unused)						
	RY207	(Unused)						
	RY208	(Unused)						
	:							
	RY367	(Unused)						
	RY368	(Reserved)						
	:							
	RY379	(Reserved)						
	:							
	RY383	(Reserved)						

RWr0	Current page
RWr1	01: PV_L1
RWr2	02: PV_L1
RWr3	03: PV_L1
RWr4	04: PV_L1
RWr5	05: PV_L1
RWr6	01: PV_L2
RWr7	02: PV_L2
RWr8	03: PV_L2
RWr9	04: PV_L2
RWr10	05: PV_L2
RWr11	01: CSP_L1
RWr12	02: CSP_L1
RWr13	03: CSP_L1
RWr14	04: CSP_L1
RWr15	05: CSP_L1
RWr16	01: CSP_L2
RWr17	02: CSP_L2
RWr18	03: CSP_L2
RWr19	04: CSP_L2
RWr20	05: CSP_L2
RWr21	01: C.A.M
RWr22	02: C.A.M
RWr23	03: C.A.M
RWr24	04: C.A.M
RWr25	05: C.A.M
RWr26	01: OUT_L2
RWr27	02: OUT_L2
RWr28	03: OUT_L2
RWr29	04: OUT_L2
RWr30	05: OUT_L2
RWr31	01: H.OUT_L2
RWr32	02: H.OUT_L2
RWr33	03: H.OUT_L2
RWr34	04: H.OUT_L2
RWr35	05: H.OUT_L2
RWr36	01: C.OUT_L2
RWr37	02: C.OUT_L2

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	(Unused)
RWw5	(Unused)
RWw6	(Unused)
RWw7	(Unused)
RWw8	(Unused)
RWw9	(Unused)
RWw10	(Unused)
RWw11	01: SP_L1_1
RWw12	02: SP_L1_1
RWw13	03: SP_L1_1
RWw14	04: SP_L1_1
RWw15	05: SP_L1_1
RWw16	01: SP_L2_1
RWw17	02: SP_L2_1
RWw18	03: SP_L2_1
RWw19	04: SP_L2_1
RWw20	05: SP_L2_1
RWw21	01: C.A.M
RWw22	02: C.A.M
RWw23	03: C.A.M
RWw24	04: C.A.M
RWw25	05: C.A.M
RWw26	01: MOUT_L2
RWw27	02: MOUT_L2
RWw28	03: MOUT_L2
RWw29	04: MOUT_L2
RWw30	05: MOUT_L2
RWw31	01: MOUT_L2
RWw32	02: MOUT_L2
RWw33	03: MOUT_L2
RWw34	04: MOUT_L2
RWw35	05: MOUT_L2
RWw36	01: MOUTc_L2
RWw37	02: MOUTc_L2

Profile number 5 (Cascade control with 5 connected contro			oller	s) on pag	ge 1	(Ver.2.00, 2-station occupied x8 setting)	
IN area			OUT area				
CC-Link slave (UTAdvanced) → CC-Link master			C	CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
RWr38		03: C.OUT_L2		RWw38		03: MOUTc_L2	
RWr39		04: C.OUT_L2		RWw39		04: MOUTc_L2	
RWr40		05: C.OUT_L2		RWw40		05: MOUTc_L2	
RWr41		(Unused)		RWw41		(Unused)	
:				:			
RWr63		(Unused)		RWw63		(Unused)	

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Page 2

Profile number 5 (Cascade control with 5 connected contro				illers) on page 2 (Ver.2.00, 2-station occupied x8 set				
C	C-I ink sl	ave (UTAdvanced) → CC-Link master	C	OUT area CC-Link master → CC-Link slave (UTAdvanced				
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment			
osition	position		position	position				
	RX0	Receive data valid		RY0	Rescan request			
	RX1 RX2	During-write	1	RY1 RY2	(Reserved)			
	RX2	Write acknowledgement (Reserved)	-	RY3	Write request			
	RX4	(Reserved)	+	RY4	(Reserved)			
	RX5		1	RY5	(Reserved)			
	RX6	(Reserved)	1	RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1			
	• DV47	Normal connection along (address 22)	-	• DV47	Datab write request (address 22)			
		Normal connection slave (address 32)	-	RY47	Batch write request (address 32)			
	RX48	(Unused)	1	RY48	(Unused)			
	:		J L	:				
	RX367	(Unused)		RY367	(Unused)			
	RX368	(Reserved)		RY368	(Reserved)			
	:			:				
	RX379	Remote Ready		RY379	(Reserved)			
	:			:				
	•	(Reserved)		PV292	(Reserved)			
	KV303	((Reserved)		K1303	(Keserveu)			
RWr0		Current page	RWw0		Page change request			
RWr1		01: P_L1_1	RWw1		01: P_L1_1			
RWr2		02: P_L1_1	RWw2		02: P_L1_1			
RWr3		03: P_L1_1	RWw3		03: P_L1_1			
RWr4		04: P_L1_1	RWw4		04: P_L1_1			
RWr5		05: P_L1_1	RWw5		05: P_L1_1			
RWr6		01: I_L1_1	RWw6		01: I_L1_1			
RWr7		02: I_L1_1	RWw7		02: I_L1_1			
RWr8		03: I_L1_1	RWw8		03: I_L1_1			
RWr9		04: I_L1_1	RWw9		04: I_L1_1			
RWr10		05: I_L1_1	RWw10		05: I_L1_1			
RWr11		01: D_L1_1	RWw11		01: D_L1_1			
RWr12		02: D_L1_1	RWw12		02: D_L1_1			
RWr13		03: D L1 1	RWw13		03: D_L1_1			
RWr14		04: D L1 1	RWw14		04: D_L1_1			
RWr15		05: D_L1_1	RWw15		05: D_L1_1			
RWr16		01: SPNO.	RWw16		01: SPNO.			
RWr17		02: SPNO.	RWw17		02: SPNO.			
RWr18		03: SPNO.	RWw18		03: SPNO.			
RWr19		04: SPNO.	RWw19		04: SPNO.			
RWr20		05: SPNO.	RWw20		05: SPNO.			
RWr21		01: P L2 1	RWw21		01: P_L2_1			
RWr22		02: P_L2_1	RWw22		02: P_L2_1			
RWr23		03: P_L2_1	RWw23		03: P_L2_1			
RWr24		04: P_L2_1	RWw24		04: P_L2_1			
RWr25		05: P_L2_1	RWw25		05: P_L2_1			
RWr26		01: I L2 1	RWw26		01: I L2 1			
RWr27		02: I_L2_1	RWw27		02: I_L2_1			
RWr28		03: I_L2_1	RWw28	1	03: I_L2_1			
RWr29		04: I_L2_1	RWw29		04: I_L2_1			
RWr30		05: I_L2_1	RWw30		05: I_L2_1			
RWr31		01: D_L2_1	RWw31		01: D_L2_1			
RWr32		02: D_L2_1	RWw32		02: D_L2_1			
RWr33		03: D_L2_1	RWw33	<u> </u>	03: D_L2_1			
RWr34		04: D_L2_1	RWw34	<u> </u>	04: D_L2_1			
RWr35		05: D_L2_1	RWw35	+	05: D_L2_1			
RWr36		(Unused)	RWw36	+	(Unused)			
	1	[(0114004)	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1	[(0)14004)			
:								

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		IN area			OUT area
C	C I intent	IN area		C I ink m	
Word	Bit	ave (UTAdvanced) → CC-Link master	Word	Bit	aster → CC-Link slave (UTAdvanced)
	position	Contents of assignment		position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)	-	RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX367	(Unused)	┨ ├──	RY367	(Unused)
		(Reserved)			(Reserved)
	*	(1.0301VBU)		:	(NOSCIVEU)
	: D)(0==			: D) (0==	(5)
	RX379	Remote Ready		RY379	(Reserved)
	:				
	RX383	(Reserved)		RY383	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		(Unused)	RWw1		(Unused)
RWr2		(Unused)	RWw2		(Unused)
RWr3		(Unused)	RWw3		(Unused)
RWr4		(Unused)	RWw4		(Unused)
RWr5		(Unused)	RWw5		(Unused)
RWr6		(Unused)	RWw6		(Unused)
RWr7		(Unused)	RWw7		(Unused)
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)
RWr12		(Unused)	RWw12	-	(Unused)
RWr13		(Unused)	RWw13		(Unused)
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)
RWr16 RWr17		01: SPNO. 02: SPNO.	RWw16	-	01: SPNO. 02: SPNO.
		i	RWw17		
RWr18 RWr19		03: SPNO. 04: SPNO.	RWw18 RWw19		03: SPNO. 04: SPNO.
RWr20		05: SPNO.	RWw19		05: SPNO.
RWr21		01: Pc_L2_1	RWw20	-	01: Pc_L2_1
RWr22		02: Pc_L2_1	RWw21	<u> </u>	02: Pc_L2_1
RWr23		03: Pc_L2_1	RWw23		03: Pc_L2_1
RWr24		04: Pc_L2_1	RWw24		04: Pc_L2_1
RWr25		05: Pc_L2_1	RWw25	<u> </u>	05: Pc_L2_1
RWr26		01: lc_L2_1	RWw26		01: lc_L2_1
RWr27		02: lc_L2_1	RWw26		02: lc_L2_1
RWr28		03: lc_L2_1	RWw28		03: lc_L2_1
RWr29		04: lc_L2_1	RWw29	+	04: lc_L2_1
RWr30		05: lc_L2_1	RWw30	 	05: lc_L2_1
RWr31		01: Dc_L2_1	RWw30	 	01: Dc_L2_1
RWr32		02: Dc_L2_1	RWw31		02: Dc_L2_1
RWr33		03: Dc_L2_1	RWw32		03: Dc_L2_1
RWr34		04: Dc_L2_1	RWw34	1	04: Dc_L2_1
RWr35		05: Dc_L2_1	RWw34		05: Dc_L2_1
RWr36		(Unused)	RWw36		(Unused)
•		(0.114304)	•		(01,0000)
:			:		
RWr63	l	(Unused)	RWw63		(Unused)

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Page 4

		IN area			OUT area	
$\textbf{CC-Link slave (UTAdvanced)} \rightarrow \textbf{CC-Link master}$			с	C-Link m	aster → CC-Link slave (UTAdvanced)	
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment	
osition	position RX0	Receive data valid	position	position RY0	_	
	RX1	During-write		RY1	Rescan request (Reserved)	
	RX2	Write acknowledgement		RY2	Write request	
	RX3	(Reserved)		RY3	(Reserved)	
	RX4	(Reserved)		RY4	(Reserved)	
	RX5	(Reserved)		RY5	(Reserved)	
	RX6	(Reserved)		RY6	(Reserved)	
	RX7	(Reserved)		RY7	(Reserved)	
	•			•		
		The fixed-part is omitted			The fixed-part is omitted	
		(See profile number 0 on page 1)			(See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)	
	RX48	(Unused)		RY48	(Unused)	
	:			:		
	DV007			D)(007		
		(Unused)		RY367	(Unused)	
	1	(Reserved)		RY368	(Reserved)	
	:			:		
	RX379	Remote Ready		RY379	(Reserved)	
	:			:		
	RX383	(Reserved)		RY383	(Reserved)	
RWr0		Current page	RWw0		Page change request	
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1	
RWr2		02: A1_L1_1	RWw2		02: A1_L1_1	
RWr3		03: A1_L1_1	RWw3		03: A1_L1_1	
RWr4		04: A1_L1_1	RWw4		04: A1_L1_1	
RWr5		05: A1_L1_1	RWw5		05: A1_L1_1	
RWr6		01: A2_L1_1	RWw6		01: A2_L1_1	
RWr7		02: A2_L1_1	RWw7		02: A2_L1_1	
RWr8		03: A2_L1_1	RWw8	+	03: A2_L1_1	
RWr9	1	03. A2_L1_1 04: A2_L1_1	RWw9	+	04: A2_L1_1	
RWr10		05: A2_L1_1	RWw10		05: A2_L1_1	
RWr11		01: A3_L1_1	RWw10		01: A3_L1_1	
RWr12		02: A3_L1_1	RWw12		02: A3_L1_1	
RWr13		03: A3_L1_1	RWw13		03: A3_L1_1	
RWr14			RWw14		04: A3_L1_1	
RWr15		04: A3_L1_1 05: A3_L1_1	RWw15		05: A3_L1_1	
			RWw16			
RWr16		01: A4_L1_1 02: A4_L1_1	-	-	01: A4_L1_1	
RWr17			RWw17	i	02: A4_L1_1	
RWr18		03: A4_L1_1	RWw18		03: A4_L1_1	
RWr19		04: A4_L1_1	RWw19		04: A4_L1_1	
RWr20	-	05: A4_L1_1	RWw20		05: A4_L1_1	
RWr21	-	01: A5_L1_1	RWw21	_	01: A5_L1_1	
Wr22		02: A5_L1_1	RWw22		02: A5_L1_1	
RWr23	-	03: A5_L1_1	RWw23	_	03: A5_L1_1	
RWr24		04: A5_L1_1	RWw24		04: A5_L1_1	
RWr25	-	05: A5_L1_1	RWw25	_	05: A5_L1_1	
RWr26		01: A1_L2_1	RWw26		01: A1_L2_1	
RWr27		02: A1_L2_1	RWw27	-	02: A1_L2_1	
Wr28		03: A1_L2_1	RWw28		03: A1_L2_1	
Wr29		04: A1_L2_1	RWw29		04: A1_L2_1	
RWr30		05: A1_L2_1	RWw30		05: A1_L2_1	
RWr31		01: A2_L2_1	RWw31		01: A2_L2_1	
RWr32		02: A2_L2_1	RWw32		02: A2_L2_1	
RWr33		03: A2_L2_1	RWw33		03: A2_L2_1	
RWr34		04: A2_L2_1	RWw34		04: A2_L2_1	
RWr35		05: A2_L2_1	RWw35		05: A2_L2_1	
RWr36		01: A3_L2_1	RWw36		01: A3_L2_1	
RWr37		02: A3_L2_1	RWw37		02: A3_L2_1	
RWr38		03: A3_L2_1	RWw38		03: A3_L2_1	
RWr39		04: A3_L2_1	RWw39		04: A3_L2_1	
CVVIOS					. – –	

4.9 Profile List

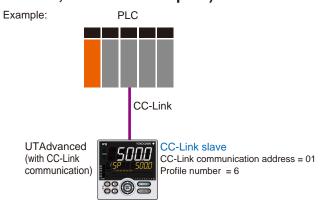
Profile	Profile number 5 (Cascade control with 5 connected controllers) on page 4 (Ver.2.00, 2-station occupied x8 setting)								
		IN area			OUT area				
C	C-Link sla	ave (UTAdvanced) → CC-Link master		C	C-Link m	aster → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment			
RWr41		01: A4_L2_1		RWw41		01: A4_L2_1			
RWr42		02: A4_L2_1		RWw42		02: A4_L2_1			
RWr43		03: A4_L2_1		RWw43		03: A4_L2_1			
RWr44		04: A4_L2_1		RWw44		04: A4_L2_1			
RWr45		05: A4_L2_1		RWw45		05: A4_L2_1			
RWr46		01: A5_L2_1		RWw46		01: A5_L2_1			
RWr47		02: A5_L2_1		RWw47		02: A5_L2_1			
RWr48		03: A5_L2_1		RWw48		03: A5_L2_1			
RWr49		04: A5_L2_1		RWw49		04: A5_L2_1			
RWr50		05: A5_L2_1		RWw50		05: A5_L2_1			
RWr51		(Unused)		RWw51		(Unused)			
:				:					
RWr63		(Unused)		RWw63		(Unused)			

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Intentionally blank

Profile number 6 (Simple PID control with 1 connected controllers) (Ver.1.10, 3-station occupied)





Page 1

Dro	filo num	har 6 (Simple DID	control with 1 connected	l controllors	l on nad	je 1 (Ver.1.10, 3-station occupied)			
FIU	ille Ilulii		CONTROL MILLI I CONNECTED	Controllers) on pay	OUT area			
C	C Link el	IN area	→ CC-Link master	_	CC-Link master → CC-Link slave (UTAdvanced)				
Word	Bit	· · · · · · · · ·		Word	Bit				
position		Contents	s of assignment		position	Contents of assignment			
	RX0	Receive data valid			RY0	Rescan request			
	RX1	During-write			RY1	(Reserved)			
	RX2	Write acknowledge	ement		RY2	Write request			
	RX3	(Reserved)			RY3	(Reserved)			
	RX4	(Reserved)			RY4	(Reserved)			
	RX5	(Reserved)			RY5	(Reserved)			
	RX6	(Reserved)			RY6	(Reserved)			
	RX7	(Reserved)			RY7	(Reserved)			
	•	The five	d-part is omitted		•	The fixed-part is omitted			
	•		number 0 on page 1)		•	(See profile number 0 on page 1)			
	•	(See profile i	iumber o on page 1)		•	(See profile flumber of our page 1)			
	RX47		slave (address 32)		RY47	Batch write request (address 32)			
	RX48	01: A.M			RY48	01: A.M			
	RX49	01: R.L_L1			RY49	01: R.L_L1			
	RX50	01: S.R			RY50	01: S.R			
	RX51	(Unused)			RY51	(Unused)			
	RX52	(Unused)			RY52	(Unused)			
	RX53	(Unused)			RY53	(Unused)			
	RX54	(Unused)			RY54	(Unused)			
	RX55	(Unused)			RY55	(Unused)			
	RX56	01: ALM1_L1			RY56	(Unused)			
	RX57	01: ALM2_L1			RY57	(Unused)			
	RX58	01: ALM3_L1			RY58	(Unused)			
	RX59	01: ALM4_L1			RY59	(Unused)			
	RX60	01: ALM5_L1			RY60	(Unused)			
	RX61	01: ALM6_L1	UT35A/UT32A:		RY61	(Unused)			
	RX62	01: ALM7_L1	unused		RY62	(Unused)			
	RX63	01: ALM8_L1	J		RY63	(Unused)			
	RX64	(Unused)			RY64	02: A.M			
	RX65	(Unused)			RY65	02: R.L_L1			
	RX66	(Unused)			RY66	02: S.R			
	RX67	(Unused)			RY67	(Unused)			
	RX68	(Unused)			RY68	(Unused)			
	RX69	(Unused)			RY69	(Unused)			
	RX70	(Unused)			RY70	(Unused)			
	RX71	(Unused)			RY71	(Unused)			
	RX72	(Unused)			RY72	(Unused)			
	RX73	(Unused)			RY73	(Unused)			
	RX74	(Unused)			RY74	(Unused)			
	RX75	(Unused)			RY75	(Unused)			
	RX76	(Unused)			RY76	(Unused)			
	RX77	(Unused)			RY77	(Unused)			
	RX78	(Unused)			RY78	(Unused)			
7	RX79	(Unused)			RY79	(Unused)			

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Pro	file numl	ber 6 (Simple PID control with 1 connected	d controllers) on page 1			e 1 (Ver.1.10, 3-station occupied)		
	IN area				OUT area			
C	CC-Link slave (UTAdvanced) → CC-Link master			С	C-Link m	aster → CC-Link slave (UTAdvanced)		
Word position			Word position	Bit position	Contents of assignment			
	RX80	(Reserved)			RY80	(Reserved)		
	:				:			
	RX91	Remote Ready			RY91	(Reserved)		
	:				:			
	RX95	(Reserved)			RY95	(Reserved)		

RWr0	Current page
RWr1	01: PV_L1
RWr2	01: CSP_L1
RWr3	01: OUT_L1
RWr4	01: H.OUT_L1
RWr5	01: C.OUT_L1
RWr6	
RWr7	
RWr8	
RWr9	
RWr10	
RWr11	

RWw0	Page change request
RWw1	(Unused)
RWw2	01: SP_L1_1
RWw3	01: MOUT_L1
RWw4	01: MOUT_L1
RWw5	01: MOUTc_L1
RWw6	
RWw7	
RWw8	
RWw9	
RWw10	
RWw11	

		ber 6 (Simple PID control with 1 connecte			,				
C	C-Link sl	IN area ave (UTAdvanced) → CC-Link master		OUT area CC-Link master → CC-Link slave (UTAdvanced)					
Word	Bit position	Contents of accimumant		Word	Bit position	Contents of assignment			
	RX0	Receive data valid		•	RY0	Rescan request			
	RX1	During-write	1 i		RY1	(Reserved)			
	RX2	Write acknowledgement	T i		RY2	Write request			
	RX3	(Reserved)	1		RY3	(Reserved)			
	RX4	(Reserved)	1 i		RY4	(Reserved)			
	RX5	(Reserved)	T i		RY5	(Reserved)			
	RX6	(Reserved)	1 i		RY6	(Reserved)			
	RX7	(Reserved)	1		RY7	(Reserved)			
	•	The fixed-part is omitted			•	The fixed-part is omitted			
		(See profile number 0 on page 1)				(See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)	T i		RY47	Batch write request (address 32)			
	RX48	(Unused)	7		RY48	(Unused)			
	:		1		:				
	RX79	(Unused)	1		RY79	(Unused)			
	RX80	(Reserved)	1		RY80	(Reserved)			
	:		7 i		:				
	RX91	Remote Ready			RY91	(Reserved)			
	:		1 i		:				
	RX95	(Reserved)	1 1		RY95	(Reserved)			
	,								
RWr0		Current page		RWw0		Page change request			
RWr1		01: P_L1_1	7	RWw1		01: P_L1_1			
RWr2		01: I_L1_1	7	RWw2		01: I_L1_1			
RWr3		01: D_L1_1	7	RWw3		01: D_L1_1			
RWr4		01: SPNO.	7	RWw4		01: SPNO.			
RWr5		(Unused)	7	RWw5		(Unused)			
RWr6		(Unused)	7	RWw6		(Unused)			
RWr7		(Unused)	7	RWw7		(Unused)			
RWr8		(Unused)	7	RWw8		(Unused)			
RWr9		(Unused)	7	RWw9		(Unused)			
RWr10		(Unused)	7	RWw10		(Unused)			
RWr11		(Unused)	7	RWw11		(Unused)			
RWr12		(Unused)	7	RWw12		(Unused)			
RWr13		(Unused)	7	RWw13		(Unused)			
RWr14		(Unused)	1	RWw14		(Unused)			
RWr15		(Unused)	1 1	RWw15		(Unused)			

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IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)				
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment			
300111011	RX0	Receive data valid	Podition	RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement	1	RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)	1	RY6	(Reserved)			
	RX7	(Reserved)	1	RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)			
	•	(See profile fluffiber 0 off page 1)		•	(See profile flumber of on page 1)			
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	(Unused)		RY48	(Unused)			
	:			:				
	RX79	(Unused)	1	RY79	(Unused)			
	RX80	(Reserved)		RY80	(Reserved)			
	:			:				
	RX91	Remote Ready		RY91	(Reserved)			
	:			:				
	RX95	(Reserved)		RY95	(Reserved)			
RWr0		Current page	RWw0		Page change request			
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1			
RWr2		01: lc_L1_1	RWw2		01: lc_L1_1			
RWr3		01: Dc_L1_1	RWw3		01: Dc_L1_1			
RWr4		01: SPNO.	RWw4		01: SPNO.			
RWr5		(Unused)	RWw5		(Unused)			
RWr6		(Unused)	RWw6		(Unused)			
RWr7		(Unused)	RWw7		(Unused)			
RWr8		(Unused)	RWw8		(Unused)			
RWr9		(Unused)	RWw9		(Unused)			
RWr10		(Unused)	RWw10		(Unused)			
RWr11		(Unused)	RWw11		(Unused)			
RWr12		(Unused)	RWw12		(Unused)			
RWr13		(Unused)	RWw13		(Unused)			
RWr14		(Unused)	RWw14		(Unused)			
RWr15		(Unused)	RWw15		(Unused)			

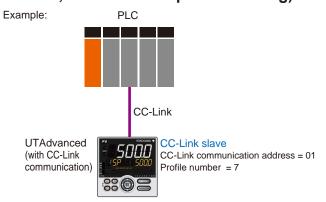
		IN area			OUT area			
C	CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
	RX0	Receive data valid		RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	:	The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)			
	•	, , , , , , , , , , , , , , , , , , ,		•				
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	(Unused)		RY48	(Unused)			
	:			:				
	RX79	(Unused)		RY79	(Unused)			
	RX80	(Reserved)		RY80	(Reserved)			
	:			:				
	RX91	Remote Ready		RY91	(Reserved)			
	:			:				
	RX95	(Reserved)		RY95	(Reserved)			
RWr0		Current page	RWw0		Page change request			
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1			
RWr2		01: A2_L1_1	RWw2		01: A1_L1_1			
RWr3		01: A3_L1_1	RWw3		03: A1_L1_1			
RWr4		01: A4_L1_1	RWw4		01: A2_L1_1			
RWr5		01: A5_L1_1 (UT35A/UT32A: unused)	RWw5		02: A2_L1_1 (UT35A/UT32A: unused)			
RWr6		(Unused)	RWw6		(Unused)			
RWr7		(Unused)	RWw7		(Unused)			
RWr8		(Unused)	RWw8		(Unused)			
RWr9		(Unused)	RWw9		(Unused)			
RWr10		(Unused)	RWw10		(Unused)			
RWr11		(Unused)	RWw11		(Unused)			
RWr12		(Unused)	RWw12		(Unused)			
RWr13		(Unused)	RWw13		(Unused)			
RWr14		(Unused)	RWw14		(Unused)			
RWr15		(Unused)	RWw15		(Unused)			

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Profile number 7 (Cascade control with 1 connected controllers) (Ver.2.00, 1-station occupied x8 setting)





Page 1

Profile	number	7 (Cascade control with 1 connected controlle	ers) on pa	ge 1	(Ver.2.00, 1-station occupied x8 setting)			
		IN area		OUT area				
C	C-Link sl	ave (UTAdvanced) → CC-Link master	С	CC-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
	RX0	Receive data valid		RY0	Rescan request			
	RX1	During-write		RY1	(Unused)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	01: R.L_L1		RY48	01: R.L_L1			
	RX49	01: S.R		RY49	01: S.R			
	RX50	01: ALM1_BIT_L1		RY50	(Unused)			
	RX51	01: ALM2_BIT_L1		RY51	(Unused)			
	RX52	01: ALM3_BIT_L1		RY52	(Unused)			
	RX53	01: ALM4_BIT_L1		RY53	(Unused)			
	RX54	01: ALM5_BIT_L1		RY54	(Unused)			
	RX55	01: ALM6_BIT_L1		RY55	(Unused)			
	RX56	01: ALM7_BIT_L1		RY56	(Unused)			
	RX57	01: ALM8_BIT_L1		RY57	(Unused)			
	RX58	01: ALM1_BIT_L2		RY58	(Unused)			
	RX59	01: ALM2_BIT_L2		RY59	(Unused)			
	RX60	01: ALM3_BIT_L2		RY60	(Unused)			
	RX61	01: ALM4_BIT_L2		RY61	(Unused)			
	RX62	01: ALM5_BIT_L2		RY62	(Unused)			
	RX63	01: ALM6_BIT_L2		RY63	(Unused)			
	RX64	01: ALM7_BIT_L2		RY64	(Unused)			
	RX65	01: ALM8_BIT_L2		RY65	(Unused)			
	RX66	(Unused)		RY66	(Unused)			
	:			:				
		(Unused)		RY111	(Unused)			
		(Reserved)			(Reserved)			
	:			:				
	RX123	Remote Ready		RY123	(Reserved)			
	:			:	,			
	RX127	(Reserved)		RY127	(Reserved)			

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Profile number 7 (Cascade conti	ol with 1 connected controlle	ers) on pag	ge 1	(Ver.2.00, 1-station occupied x8 setting)	
IN area			OUT area		
CC-Link slave (UTAdvanced	I) → CC-Link master	C	CC-Link master → CC-Link slave (UTAdvanced)		
Word Bit Content	ts of assignment	Word position	Bit position	Contents of assignment	

RWr0		Current page	RWw0		Page change request
RWr1		01: PV_L1	RWw1		(Unused)
RWr2		01: PV_L2	RWw2		(Unused)
RWr3		01: CSP_L1	RWw3		01: SP_L1_1
RWr4		01: CSP_L2	RWw4		01: SP_L2_1
RWr5		01: C.A.M	RWw5		01: C.A.M
RWr6		01: OUT_L2	RWw6		01: MOUT_2
RWr7		01: H.OUT_L2	RWw7		01: MOUT_2
RWr8		01: C.OUT_L2	RWw8		01: MOUTc_2
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)
RWr12		(Unused)	RWw12		(Unused)
RWr13		(Unused)	RWw13		(Unused)
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)
	:			:	
RWr31		(Unused)	RWw31		(Unused)

Profile	number	7 (Cascade control with 1 connected control	ollers) on pa	ge 2	(Ver.2.00, 1-station occupied x8 setting)
		IN area		-	OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master	С	C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	:				
	RX123	Remote Ready		RY123	(Reserved)
	:			:	
	RX127	(Reserved)		RY127	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		01: I_L1_1	RWw2		01: I_L1_1
RWr3		01: D_L1_1	RWw3		01: D_L1_1
RWr4		01: SPNO.	RWw4		01: SPNO.
RWr5		01: P_L2_1	RWw5		01: P_L2_1
RWr6		01: I_L2_1	RWw6		01: I_L2_1
RWr7		01: D_L2_1	RWw7		01: D_L2_1
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)
RWr12		(Unused)	RWw12		(Unused)
RWr13		(Unused)	RWw13		(Unused)
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)
	:			:	
RWr31		(Unused)	RWw31		(Unused)

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		IN area			OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master	С	C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	:				
	RX123	Remote Ready		RY123	(Reserved)
	:			:	
	RX127	(Reserved)		RY127	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		(Unused)	RWw1		(Unused)
RWr2		(Unused)	RWw2		(Unused)
RWr3		(Unused)	RWw3		(Unused)
RWr4		(Unused)	RWw4		(Unused)
RWr5		(Unused)	RWw5		(Unused)
RWr6		(Unused)	RWw6		(Unused)
RWr7		(Unused)	RWw7		(Unused)
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		01: SPNO.	RWw10		01: SPNO.
RWr11		01: Pc_L2_1	RWw11		01: Pc_L2_1
RWr12		01: lc_L2_1	RWw12		01: lc_L2_1
RWr13		01: Dc_L2_1	RWw13		01: Dc_L2_1
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)
	:			:	
RWr31		(Unused)	RWw31		(Unused)

Profile	number	7 (Cascade control with 1 connected control	ollers) on pa	ge 4	(Ver.2.00, 1-station occupied x8 setting)
		IN area		J -	OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master	C	C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	:				
	RX123	Remote Ready		RY123	(Reserved)
	:			:	
	RX127	(Reserved)		RY127	(Reserved)
				,	
RWr0		Current page	RWw0		Page change request
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1
RWr2		01: A2_L1_1	RWw2		01: A2_L1_1
RWr3		01: A3_L1_1	RWw3		01: A3_L1_1
RWr4		01: A4_L1_1	RWw4		01: A4_L1_1
RWr5		01: A5_L1_1	RWw5		01: A5_L1_1
RWr6		01: A1_L2_1	RWw6		01: A1_L2_1
RWr7		01: A2_L2_1	RWw7		01: A2_L2_1
RWr8		01: A3_L2_1	RWw8		01: A3_L2_1
RWr9		01: A4_L2_1	RWw9		01: A4_L2_1
RWr10		01: A5_L2_1	RWw10		01: A5_L2_1
RWr11		(Unused)	RWw11		(Unused)
RWr12		(Unused)	RWw12		(Unused)
RWr13		(Unused)	RWw13		(Unused)
RWr14		(Unused)	RWw14		(Unused)
RWr15		(Unused)	RWw15		(Unused)
	:			:	
RWr31		(Unused)	RWw31		(Unused)

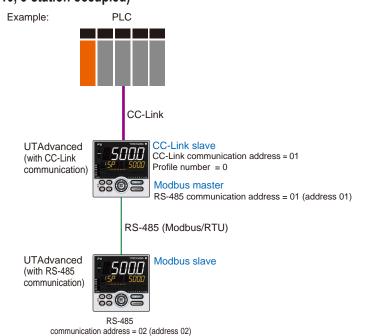
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4.9.2 Profile List for UP55A/UP35A/UP32A



Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) (Ver.1.10, 3-station occupied)



Page 1

TOTHE I	iuilibei V	(User profile [initial value: simple PID control w	THE COMMECTE	u control				
_		IN area		OUT area				
		ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced)				
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
	RX0	Receive data valid		RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	RX8	(Reserved)		RY8	(Reserved)			
	RX9	(Reserved)		RY9	(Reserved)			
	RX10	(Reserved)		RY10	(Reserved)			
	RX10	(Reserved)		RY11	(Reserved)			
		((/			
	RX12	(Reserved)		RY12	(Reserved)			
	RX13	(Reserved)		RY13	(Reserved)			
	RX14	(Reserved)		RY14	(Reserved)			
		(Reserved)		RY15	(Reserved)			
		Normal connection slave (address 01)		RY16	Batch write request (address 01)			
	RX17	Normal connection slave (address 02)		RY17	Batch write request (address 02)			
	RX18	Normal connection slave (address 03)		RY18	Batch write request (address 03)			
	RX19	Normal connection slave (address 04)		RY19	Batch write request (address 04)			
	RX20 RX21	Normal connection slave (address 05)		RY20 RY21	Batch write request (address 05)			
	RX22	Normal connection slave (address 06) Normal connection slave (address 07)		RY22	Batch write request (address 06) Batch write request (address 07)			
	RX23	Normal connection slave (address 07)		RY23	Batch write request (address 07)			
		Normal connection slave (address 09)		RY24	Batch write request (address 09)			
	RX25	Normal connection slave (address 10)		RY25	Batch write request (address 10)			
	RX26	Normal connection slave (address 11)		RY26	Batch write request (address 11)			
	RX27	Normal connection slave (address 12)		RY27	Batch write request (address 12)			
	RX28	Normal connection slave (address 13)		RY28	Batch write request (address 13)			
	RX29	Normal connection slave (address 14)		RY29	Batch write request (address 14)			
	RX30	Normal connection slave (address 15)		RY30	Batch write request (address 15)			
	RX31	Normal connection slave (address 16)		RY31	Batch write request (address 16)			
	RX32	Normal connection slave (address 17)		RY32	Batch write request (address 17)			
	RX33	Normal connection slave (address 18)		RY33	Batch write request (address 18)			
	RX34	Normal connection slave (address 19)		RY34	Batch write request (address 19)			

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1 TOTHE I	ullibei v	IN area	value. Simple 1 ID Control with	L COMMECIE	u control	OUT area
00	0.1.5-11		00 Link mastan		0.1.51	
Word	Bit	ave (UTAdvanced)	→ CC-Link master	Word	Bit	aster → CC-Link slave (UTAdvanced)
position	_	Contents	of assignment		position	Contents of assignment
podition		Normal connection	slave (address 20)	podition		Batch write request (address 20)
	RX36	Normal connection	slave (address 21)		RY36	Batch write request (address 21)
		Normal connection			RY37	Batch write request (address 22)
		Normal connection	()		RY38	Batch write request (address 23)
		Normal connection	, ,		RY39	Batch write request (address 24)
		Normal connection			RY40	Batch write request (address 25)
		Normal connection			RY41 RY42	Batch write request (address 26) Batch write request (address 27)
		Normal connection			RY43	Batch write request (address 27)
		Normal connection			RY44	Batch write request (address 29)
		Normal connection	, ,		RY45	Batch write request (address 30)
	RX46	Normal connection	slave (address 31)		RY46	Batch write request (address 31)
		Normal connection	slave (address 32)			Batch write request (address 32)
		01: RST_ON			RY48	01: RST_ON
	RX49	01: PRG_ON			RY49	01: PRG_ON
	RX50	01: LOC_ON			RY50	01: LOC_ON
	RX51	01: HOLD			RY51	01: HOLD
		(Unused)			RY52	01: ADV
	RX53	01: A.M_L1			RY53	01: A.M_L1
	RX54	01: PV_EV1			RY54	(Unused)
		01: PV_EV2			RY55	(Unused)
	RX56	01: TIME_EV1			RY56	(Unused)
	RX57	01: TIME_EV2			RY57	(Unused)
	RX58	01: TIME_EV3			RY58	(Unused)
	RX59	01: TIME_EV4			RY59	(Unused)
	RX60	01: TIME_EV5	UP35A/UP32A:		RY60	(Unused)
	RX61	01: TIME_EV6	unused		RY61	(Unused)
	RX62	01: TIME_EV7			RY62	(Unused)
	RX63	01: TIME_EV8			RY63	(Unused)
		02: RST_ON			RY64	02: RST ON
		02: PRG_ON			RY65	02: PRG ON
		02: LOC_ON			RY66	02: LOC_ON
		02: HOLD			RY67	02: HOLD
	RX68	(Unused)			RY68	02: ADV
	RX69	02: A.M_L1			RY69	02: A.M_L1
	RX70	02: PV EV1			RY70	(Unused)
	RX71	02: PV EV2			RY71	(Unused)
	RX72	02: TIME EV1			RY72	(Unused)
	RX73	02: TIME EV2			RY73	(Unused)
	RX74	02: TIME_EV3			RY74	(Unused)
		02: TIME EV4			RY75	(Unused)
		02: TIME EV5	UP35A/UP32A:		RY76	(Unused)
		02: TIME_EV6	unused			(Unused)
		02: TIME_EV7				(Unused)
		02: TIME EV8			RY79	(Unused)
		(Reserved)			RY80	(Reserved)
	:				:	
	PY01	Pomoto Poodu				(Pasaryad)
		Remote Ready			RY91	(Reserved)
	:				:	
	RX95	(Reserved)			RY95	(Reserved)
RWr0		Current page		RWw0		Page change request
RWr1		01: PV_L1		RWw1		01: H.TSP_L1
RWr2		01: CSP_L1		RWw2		01: H.SP_L1
RWr3		01: SEG_RTIME		RWw3		01: H.TM_L1
RWr4		02: PV_L1		RWw4		02: H.TSP_L1
RWr5		02: CSP_L1		RWw5		02: H.SP_L1
RWr6		02: SEG_RTIME		RWw6		02: H.TM_L1
RWr7		(Unused)		RWw7		(Unused)
RWr8		(Unused)		RWw8		(Unused)
RWr9		(Unused)		RWw9		(Unused)
RWr10		(Unused)		RWw10		(Unused)
RWr11		(Unused)		RWw11		(Unused)
		1,,		1		17

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1 (Ver.1.10, 3-station occupied)

		IN area		OUT area				
		ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced				
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
	RX0	Receive data valid		RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	•	The fixed-part is omitted		•	The fixed-part is omitted			
	•	(See profile number 0 on page 1)			(See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	(Unused)			(Unused)			
	:			:				
	RX79	(Unused)			(Unused)			
	RX80	(Reserved)		RY80	(Reserved)			
	:			:				
	RX91	Remote Ready		RY91	(Reserved)			
	:			:				
	RX95	(Reserved)		RY95	(Reserved)			
RWr0		Current page	RWw0		Page change request			
RWr1		01: P_L1_1	RWw1		01: P_L1_1			
RWr2		01: I_L1_1	RWw2		01: I_L1_1			
RWr3		01: D_L1_1	RWw3		01: D_L1_1			
RWr4		01 L.PID	RWw4		01: L.PID			
RWr5		01: C.PTNO.	RWw5		01: PTNO.			
RWr6		01: SEG.N	RWw6		01: SST			
RWr7		(Unused)	RWw7		(Unused)			
RWr8		(Unused)	RWw8		(Unused)			
RWr9		(Unused)	RWw9		(Unused)			
RWr10		(Unused)	RWw10		(Unused)			
RWr11		(Unused)	RWw11		(Unused)			

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		IN area			OUT area		
C	C-Link sl	ave (UTAdvanced) → CC-Link master	с	CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
	RX0	Receive data valid		RY0	Rescan request		
	RX1	During-write		RY1	(Reserved)		
	RX2	Write acknowledgement		RY2	Write request		
	RX3	(Reserved)		RY3	(Reserved)		
	RX4	(Reserved)		RY4	(Reserved)		
	RX5	(Reserved)		RY5	(Reserved)		
	RX6	(Reserved)		RY6	(Reserved)		
	RX7	(Reserved)		RY7	(Reserved)		
		The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)		
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)		
	RX48	(Unused)			(Unused)		
	:			:			
	RX79	(Unused)			(Unused)		
	RX80	(Reserved)		RY80	(Reserved)		
	:			:	,		
	RX91	Remote Ready		RY91	(Reserved)		
	:			:			
	RX95	(Reserved)		RY95	(Reserved)		
				1	-		
RWr0		Current page	RWw0		Page change request		
RWr1		02: P_L1_1	RWw1		02: P_L1_1		
RWr2		02: I_L1_1	RWw2		02: I_L1_1		
RWr3		02: D_L1_1	RWw3		02: D_L1_1		
RWr4		02: L.PID	RWw4		02: L.PID		
RWr5		02: C.PTNO.	RWw5		02: PTNO.		
RWr6		02: SEG.N	RWw6		02: SST		
RWr7		(Unused)	RWw7		(Unused)		
RWr8		(Unused)	RWw8		(Unused)		
RWr9		(Unused)	RWw9		(Unused)		
RWr10		(Unused)	RWw10		(Unused)		
RWr11		(Unused)	RWw11		(Unused)		

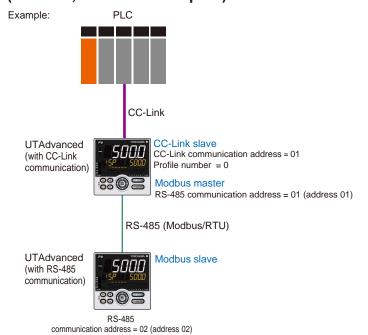
Profile r	number 0	(User profile [initial value: simple PID control w	ith 2 connect	ed control	
		IN area			OUT area
	C-Link sl	ave (UTAdvanced) → CC-Link master			naster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
		The fixed-part is omitted			The fixed-part is omitted
		(See profile number 0 on page 1)			(See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)			(Unused)
	:			:	
	RX79	(Unused)	1		(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	:			:	
	RX91	Remote Ready		RY91	(Reserved)
	:			:	
	RX95	(Reserved)		RY95	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: L.TY1	RWw1		01: L.TY1
RWr2		01: L.EV1	RWw2		01: L.EV1
RWr3		01: L.TY2	RWw3		01: L.TY2
RWr4		01: L.EV2	RWw4		01: L.EV2
RWr5		02: L.TY1	RWw5		02: L.TY1
RWr6		02: L.EV1	RWw6		02: L.EV1
RWr7		02: L.TY2	RWw7		02: L.TY2
RWr8		02: L.EV2	RWw8		02: L.EV2
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

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Profile number 11 (Simple PID control with 2 connected controllers) (Ver.1.10, 4-station occupied)





Page 1

Prof	file numl	ber 11 (Simple P	ID control with 2 connect	ed co	ontroller	s) on pa	ge 1 (Ver.1.10, 4-station occupie	
		IN area					OUT area	
C	C-Link sl	ave (UTAdvance	d) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Conter	its of assignment		Word position	Bit position	Contents of assignment	
	RX0	Receive data val	id			RY0	Rescan request	
	RX1	During-write				RY1	(Reserved)	
	RX2	Write acknowled	gement			RY2	Write request	
	RX3	(Reserved)				RY3	(Reserved)	
	RX4	(Reserved)				RY4	(Reserved)	
	RX5	(Reserved)				RY5	(Reserved)	
	RX6	(Reserved)				RY6	(Reserved)	
	RX7	(Reserved)		i i		RY7	(Reserved)	
	•		e number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection	on slave (address 32)			RY47	Batch write request (address 32)	
	RX48	01: RST_ON				RY48	01: RST_ON	
	RX49	01: PRG_ON] [RY49	01: PRG_ON	
	RX50	01: LOC_ON		7 I		RY50	01: LOC_ON	
	RX51	01: HOLD		1 i		RY51	01: HOLD	
	RX52	(Unused)] [RY52	01: ADV	
	RX53	01: A.M_L1		7 [RY53	01: A.M_L1	
	RX54	01: ALM1_L1] [RY54	(Unused)	
	RX55	01: ALM2_L1		1 i		RY55	(Unused)	
	RX56	01: PV_EV1		1 1		RY56	(Unused)	
	RX57	01: PV_EV2		1 [RY57	(Unused)	
	RX58	01: PV_EV3		1		RY58	(Unused)	
	RX59	01: PV_EV4	_			RY59	(Unused)	
	RX60	01: PV_EV5	UP35A/UP32A:			RY60	(Unused)	
		01: PV_EV6	unused			RY61	(Unused)	
		01: PV EV7	-			RY62	(Unused)	
		01: PV_EV8	- J			RY63	(Unused)	
		01: TIME EV1		1 1		RY64	(Unused)	
		01: TIME EV2		1		RY65	(Unused)	
		01: TIME EV3		1		RY66	(Unused)	
		01: TIME EV4		1		RY67	(Unused)	
		01: TIME EV5	UP35A: unused	1		RY68	(Unused)	
		01: TIME EV6	UP35A: unused	1		RY69	(Unused)	
		01: TIME EV7	UP35A: unused	1 1		RY70	(Unused)	

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Prof	file numl	<u>·</u> _	control with 2 connected	ed controller					
C	}_l ink el	IN area	→ CC-Link master		OUT area CC-Link master → CC-Link slave (UTAdvanced)				
Word	Bit		s of assignment	Word	Bit	Contents of assignment			
position	position PY71	01: TIME EV8		position	position RY71	(Unused)			
		01: TIME_EV9			RY72	(Unused)			
		01: TIME_EV10			RY73	(Unused)			
	RX74	01: TIME_EV11			RY74	(Unused)			
		01: TIME_EV12	UP35A: unused		RY75	(Unused)			
		01: TIME_EV13			RY76	(Unused)			
		01: TIME_EV14			RY77	(Unused)			
		01: TIME_EV15			RY78	(Unused)			
		01: TIME_EV16 02: RST_ON)		RY79 RY80	(Unused) 02: RST_ON			
		02: PRG_ON				02: PRG_ON			
		02: LOC_ON				02: LOC_ON			
	RX83	02: HOLD			RY83	02: HOLD			
		(Unused)			-	02: ADV			
		02: A.M_L1			RY85	02: A.M_L1			
		02: ALM1_L1			RY86	(Unused)			
		02: ALM2_L1 02: PV EV1			RY87 RY88	(Unused)			
		02: PV_EV1 02: PV_EV2			RY88 RY89	(Unused)			
		02: PV_LV2)		RY90	(Unused)			
		02: PV_EV4			RY91	(Unused)			
	RX92	02: PV_EV5	UP35A/UP32A:		RY92	(Unused)			
	RX93	02: PV_EV6	unused		RY93	(Unused)			
		02: PV_EV7			R94	(Unused)			
		02: PV_EV8	J		RY95	(Unused)			
		02: TIME_EV1			RY96	(Unused)			
		02: TIME_EV2 02: TIME_EV3			RY97 RY98	(Unused)			
		02: TIME_EV3			RY99	(Unused)			
		02: TIME_EV5)		_	(Unused)			
		02: TIME_EV6				(Unused)			
	RX102	02: TIME_EV7			RY102	(Unused)			
		02: TIME_EV8				(Unused)			
		02: TIME_EV9				(Unused)			
		02: TIME_EV10	UP35A/UP32A:			(Unused)			
		02: TIME_EV11 02: TIME_EV12	unused			(Unused)			
		02: TIME_EV12				(Unused)			
		02: TIME_EV14				(Unused)			
		02: TIME EV15			-	(Unused)			
	RX111	02: TIME_EV16	J		RY111	(Unused)			
	RX112	(Reserved)				(Reserved)			
	:				:				
	RX123	Remote Ready				(Reserved)			
	:				:				
	RX127	(Reserved)			RY127	(Reserved)			
RWr0		Current page		RWw0		Page change request			
RWr1		01: PV_L1		RWw1		01: H.TSP_L1			
RWr2 RWr3		01: CSP_L1 01: SEG_RTIME		RWw2 RWw3		01: H.SP_L1 01: H.TM_L1			
RWr4		01: SEG_RTIME 01: LSP_L1		RWw4		01: LSP_L1			
RWr5		01: OUT_L1		RWw5		01: MOUT_L1			
RWr6		01: C.PTNO.		RWw6		01: PTNO.			
RWr7		01: SEG.N		RWw7		01: SST			
RWr8		(Unused)		RWw8		(Unused)			
RWr9		02: PV_L1		RWw9		02: H.TSP_L1			
RWr10		02: CSP_L1		RWw10		02: H.SP_L1			
RWr11		02: SEG_RTIME		RWw11		02: H.TM_L1			
RWr12 RWr13		02: LSP_L1 02: OUT_L1		RWw12 RWw13		02: LSP_L1 02: MOUT_L1			
RWr14		02: OUT_L1 02: C.PTNO.		RWw13		02: PTNO.			
RWr15		02: SEG.N		RWw15		02: SST			

Pro	file num	ber 11 (Simple PID control with 2 connected	controller	s) on pa	ge 2 (Ver.1.10, 4-station occupied)
		IN area			OUT area
		ave (UTAdvanced) → CC-Link master			aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	:			:	
	_	Remote Ready		RY123	(Reserved)
	:				
	RX127	(Reserved)		RY127	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		01: I_L1_1	RWw2		01: I_L1_1
RWr3		01: D_L1_1	RWw3		01: D_L1_1
RWr4		01: L.PID	RWw4		01: L.PID
RWr5		01: A1_L1_1	RWw5		01: A1_L1_1
RWr6		01: A2_L1_1	RWw6		01: A2_L1_1
RWr7		01: A3_L1_1	RWw7		01: A3_L1_1
RWr8		(Unused)	RWw8		(Unused)
RWr9		02: P_L1_1	RWw9		02: P_L1_1
RWr10		02: I_L1_1	RWw10		02: I_L1_1
RWr11		02: D_L1_1	RWw11		02: D_L1_1
RWr12		02: L.PID	RWw12		02: L.PID
RWr13		02: A1_L1_1	RWw13		02: A1_L1_1
RWr14		02: A2_L1_1	RWw14		02: A2_L1_1
RWr15		02: A3_L1_1	RWw15		02: A3_L1_1

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		IN area	ntrol with 2 connected	a controller	o, on pa	OUT area	, 4-station occupie		
C	C-Link sl	ave (UTAdvanced) → C	C-Link master	С	CC-Link master → CC-Link slave (UTAdvanced)				
Word position	sition position Contents of		assignment	Word position	Bit position	Contents of	assignment		
	RX0	Receive data valid			RY0	Rescan request			
	RX1	During-write			RY1 RY2	(Reserved)			
	RX2		Write acknowledgement			Write request			
	RX3	(Reserved)			RY3	(Reserved)			
	RX4	(Reserved)			RY4	(Reserved)			
	RX5	(Reserved)			RY5	(Reserved)			
	RX6 RX7	(Reserved)			RY6 RY7	(Reserved)			
	RA/	(Reserved)			K17	(Reserved)			
		The fixed-par	t is omitted			The fixed-pa	art is omitted		
		(See profile numb	er 0 on page 1)			(See profile num	nber 0 on page 1)		
	RX47	Normal connection slav	re (address 32)		RY47	Batch write request (a	ddress 32)		
	RX48	(Unused)	· ·		RY48	(Unused)	•		
	:				:				
	RX111	(Unused)			RY111	(Unused)			
	RX112	(Reserved)	,		RY112	(Reserved)			
	:				:				
	RX123	Remote Ready			RY123	(Reserved)			
	:				:				
	RX127	(Reserved)			RY127	(Reserved)			
RWr0		Current page		RWw0		Page change request			
RWr1		01: L.TY1		RWw1		01: L.TY1			
RWr2		01: L.EV1		RWw2		01: L.EV1			
RWr3		01: L.TY2		RWw3		01: L.TY2			
RWr4		01: L.EV2		RWw4		01: L.EV2			
RWr5		01: L.TY3)	RWw5		01: L.TY3)		
RWr6		01: L.EV3		RWw6		01: L.EV3	-		
RWr7		01: L.TY4		RWw7		01: L.TY4	-		
RWr8		01: L.EV4		RWw8		01: L.EV4	-		
RWr9		01: L.TY5	UP35A/UP32A:	RWw9		01: L.TY5	UP35A/UP32A:		
RWr10		01: L.EV5	unused	RWw10		01: L.EV5.	unused		
RWr11		01: L.TY6		RWw11		01: L.TY6	_		
RWr12		01: L.EV6		RWw12		01: L.EV6	-		
RWr13		01: L.TY7		RWw13		01: L.TY7	_		
RWr14		01: L.EV7	J	RWw14		01: L.EV7	J		
RWr15		(Unused)		RWw15		(Unused)			

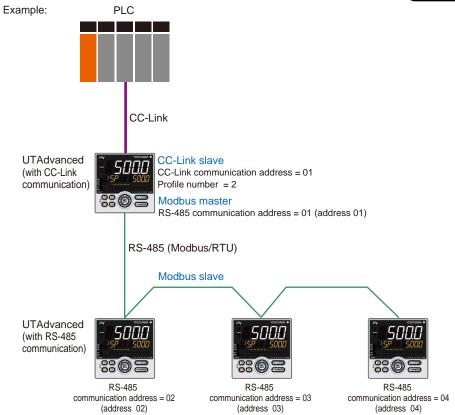
		IN area ave (UTAdvanced) \rightarrow C	C-Link master		OUT area CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of	assignment	Word position	Bit position			
	RX0	Receive data valid			RY0	Rescan request		
	RX1 RX2	During-write			RY1 RY2	(Reserved)		
	RX3	Write acknowledgemer (Reserved)	ıı		RY3	Write request (Reserved)		
	RX4	(Reserved)			RY4	(Reserved)		
	RX5	(Reserved)			RY5	(Reserved)		
	RX6	(Reserved)			RY6	(Reserved)		
	RX7	(Reserved)			RY7	(Reserved)		
	•	The fixed-pa (See profile numb			•		art is omitted ber 0 on page 1)	
	RX47	Normal connection slav	ve (address 32)		RY47	Batch write request (a	ddress 32)	
	RX48	(Unused)			RY48	(Unused)		
	:				:			
	RX111	(Unused)			RY111	(Unused)		
		(Reserved)			RY112	(Reserved)		
	:				:			
	RX123	Remote Ready			RY123	(Reserved)		
	:				:			
	RX127	(Reserved)			RY127	(Reserved)		
		I				Τ		
RWr0		Current page		RWw0		Page change request		
RWr1		02: L.TY1		RWw1		02: L.TY1		
RWr2		02: L.EV1		RWw2		02: L.EV1		
RWr3		02: L.TY2		RWw3		02: L.TY2		
RWr4		02: L.EV2		RWw4		02: L.EV2		
RWr5		02: L.TY3		RWw5		02: L.TY3]	
RWr6		02: L.EV3		RWw6		02: L.EV3		
RWr7		02: L.TY4		RWw7		02: L.TY4		
RWr8		02: L.EV4		RWw8		02: L.EV4	•	
RWr9		02: L.TY5	UP35A/UP32A:	RWw9		02: L.TY5	UP35A/UP32A	
RWr10		02: L.EV5.	unused	RWw10		02: L.EV5.	unused	
RWr11		02: L.TY6		RWw11		02: L.TY6	-	
RWr12		02: L.EV6		RWw12		02: L.EV6	-	
RWr13		02: L.TY7		RWw13		02: L.TY7	-	
RWr14		02: L.EV7]	RWw14		02: L.EV7	· J	
RWr15		(Unused)		RWw15		(Unused)		

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Profile number 12 (Simple PID control with 4 connected controllers) (Ver.2.00, 2-station occupied x4 setting)





Page 1

Profile	number	12 (Simple PID cor	ntrol with 4 connected co	ntro	ollers) on	page 1	(Ver.2.00, 2-station occupied x4 setting)	
		IN area			OUT area			
		ave (UTAdvanced)	→ CC-Link master		CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents	of assignment		Word position	Bit position	Contents of assignment	
	RX0	Receive data valid				RY0	Rescan request	
	RX1	During-write				RY1	(Reserved)	
	RX2	Write acknowledge	ment			RY2	Write request	
	RX3	(Reserved)				RY3	(Reserved)	
	RX4	(Reserved)				RY4	(Reserved)	
	RX5	(Reserved)				RY5	(Reserved)	
	RX6	(Reserved)				RY6	(Reserved)	
	RX7	(Reserved)				RY7	(Reserved)	
	•		d-part is omitted number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection	slave (address 32)			RY47	Batch write request (address 32)	
	RX48	01: RST_ON				RY48	01: RST_ON	
	RX49	01: PRG_ON				RY49	01: PRG_ON	
	RX50	01: LOC_ON				RY50	01: LOC_ON	
	RX51	01: HOLD				RY51	01: HOLD	
	RX52	(Unused)				RY52	01: ADV	
	RX53	01: A.M_L1				RY53	01: A.M_L1	
	RX54	01: ALM1_L1				RY54	(Unused)	
		01: ALM2_L1				RY55	(Unused)	
	RX56	01: PV_EV1				RY56	(Unused)	
	RX57	01: PV_EV2				RY57	(Unused)	
	RX58	01: PV_EV3				RY58	(Unused)	
		01: PV_EV4				RY59	(Unused)	
	RX60	01: PV_EV5	UP35A/UP32A:			RY60	(Unused)	
	RX61	01: PV_EV6	unused			RY61	(Unused)	
		01: PV_EV7				RY62	(Unused)	
	RX63	01: PV_EV8				RY63	(Unused)	

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Profile number	12 (Simple PID co	ntrol with 4 connected co	ntrollers) on	page 1	(Ver.2.00, 2-station occupied x4 setting)
	IN area				OUT area
CC-Link s	lave (UTAdvanced)	→ CC-Link master	Word	C-Link m	aster → CC-Link slave (UTAdvanced)
position position	Contents	s of assignment		position	Contents of assignment
	01: TIME_EV1			RY64	(Unused)
	01: TIME_EV2			RY65	(Unused)
RX66				RY66	(Unused)
RX67 RX68				RY67 RY68	(Unused)
RX69				RY69	(Unused)
RX70				RY70	(Unused)
RX71	01: TIME_EV8			RY71	(Unused)
RX72	01: TIME_EV9			RY72	(Unused)
	01: TIME_EV10	UP35A/UP32A:		RY73	(Unused)
RX74		unused		RY74	(Unused)
RX75				RY75	(Unused)
RX76				RY76 RY77	(Unused)
	01: TIME_EV15				(Unused)
RX79		J		RY79	(Unused)
RX80				RY80	02: RST_ON
RX81	02: PRG_ON			RY81	02: PRG_ON
RX82					02: LOC_ON
RX83				RY83	02: HOLD
RX84	1, ,			RY84	02: ADV
RX85	-			RY85	02: A.M_L1
RX86 RX87				RY86 RY87	(Unused)
RX88				RY88	(Unused)
RX89	-			RY89	(Unused)
RX90)		RY90	(Unused)
RX91	02: PV_EV4			RY91	(Unused)
RX92	02: PV_EV5	UP35A/UP32A: unused		RY92	(Unused)
RX93	02: PV_EV6			RY93	(Unused)
RX94				RY94	(Unused)
RX95)		RY95	(Unused)
RX96				RY96	(Unused)
RX97	-			RY97 RY98	(Unused)
RX99				RY99	(Unused)
L	02: TIME EV5)			(Unused)
RX101	02: TIME_EV6				(Unused)
RX102	02: TIME_EV7			RY102	(Unused)
	02: TIME_EV8				(Unused)
	02: TIME_EV9			 	(Unused)
	02: TIME_EV10	UP35A/UP32A:		+	(Unused)
	02: TIME_EV11 02: TIME_EV12	unused		+	(Unused)
	02: TIME EV13				(Unused)
	02: TIME_EV13				(Unused)
	02: TIME_EV15			+	(Unused)
	02: TIME_EV16	J		+	(Unused)
RX112	03: RST_ON				03: RST_ON
	03: PRG_ON				03: PRG_ON
	03: LOC_ON				03: LOC_ON
	03: HOLD			_	03: HOLD
	(Unused)			+	03: ADV
$\overline{}$	03: A.M_L1 03: ALM1_L1		-		03: A.M_L1 03: ALM1_L1
	03: ALM1_L1			+	(Unused)
	03: PV_EV1			_	(Unused)
	03: PV_EV2				(Unused)
	03: PV_EV3			_	(Unused)
	03: PV_EV4			_	(Unused)
	03: PV_EV5	UP35A/UP32A:		+	(Unused)
	03: PV_EV6	unused			(Unused)
	03: PV_EV7			+	(Unused)
KX12/	03: PV_EV8)		KA12/	(Unused)

Profile	number	12 (Simple PID cor	ntrol with 4 connected co	ntrollers) on	page 1	(Ver.2.00, 2-station occupied x4 setting)		
		IN area				OUT area		
Word	C-Link sl	ave (UTAdvanced)	→ CC-Link master	CC-Link master → CC-Link slave (UTAdvanced) Word Bit				
	position	Contents	s of assignment		position	Contents of assignment		
		03: TIME_EV1				(Unused)		
		03: TIME_EV2				(Unused)		
		03: TIME_EV3 03: TIME_EV4				(Unused)		
		03: TIME_EV4)		_	(Unused)		
		03: TIME_EV6				(Unused)		
	RX134	03: TIME_EV7			RY134	(Unused)		
		03: TIME_EV8				(Unused)		
		03: TIME_EV9				(Unused)		
		03: TIME_EV10	UP35A/UP32A: unused			(Unused)		
		03: TIME_EV11 03: TIME_EV12	unuseu			(Unused)		
		03: TIME_EV13				(Unused)		
		03: TIME_EV14				(Unused)		
	RX142	03: TIME_EV15			RY142	(Unused)		
		03: TIME_EV16	ı			(Unused)		
		04: RST_ON			_	04: RST_ON		
		04: PRG_ON				04: PRG_ON		
		04: LOC_ON		-		04: LOC_ON		
		(Unused)				04: HOLD 04: ADV		
		04: A.M_L1				04: A.M L1		
		04: ALM1_L1				(Unused)		
		04: ALM2_L1			_	(Unused)		
	RX152	04: PV_EV1			RY152	(Unused)		
	RX153	04: PV_EV2			RY153	(Unused)		
		04: PV_EV3				(Unused)		
		04: PV_EV4	UP35A/UP32A:			(Unused)		
		04: PV_EV5				(Unused)		
		04: PV_EV6 04: PV_EV7	unuseu			(Unused)		
		04: PV EV8			-	(Unused)		
		04: TIME EV1	,			(Unused)		
	-	04: TIME_EV2				(Unused)		
	RX162	04: TIME_EV3			RY162	(Unused)		
		04: TIME_EV4			_	(Unused)		
		04: TIME_EV5				(Unused)		
		04: TIME_EV6				(Unused)		
		04: TIME_EV7 04: TIME_EV8				(Unused)		
		04: TIME_EV9				(Unused)		
		04: TIME EV10	UP35A/UP32A:		_	(Unused)		
		04: TIME_EV11	unused			(Unused)		
	RX171	04: TIME_EV12				(Unused)		
		04: TIME_EV13				(Unused)		
		04: TIME_EV14			_	(Unused)		
		04: TIME_EV15				(Unused)		
		04: TIME_EV16				(Unused)		
		(Reserved)			*	(Reserved)		
	DV107	Romoto Doody			DV407	(Pasaniad)		
	*	Remote Ready				(Reserved)		
	PV101	(Peservod)			PV101	(Pasanyad)		
	KA 191	(Reserved)			K1191	(Reserved)		
RWr0		Current page		RWw0		Page change request		
RWr1		01: PV_L1		RWw1		01: H.TSP_L1		
RWr2		01: CSP_L1		RWw2		01: H.SP_L1		
RWr3		01: SEG_RTIME		RWw3		01: H.TM_L1		
RWr4		01: LSP_L1		RWw4		01: LSP_L1		
RWr5		01: OUT_L1		RWw5		01: MOUT_L1		
RWr6		01: C.PTNO.		RWw6		01: PTNO.		
RWr7 RWr8		(Unused)		RWw7 RWw8		(Unused)		
174410	L	(Ciluseu)		174440	l	(Ondoca)		

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Profile	Profile number 12 (Simple PID control with 4 connected controllers) on page 1 (Ver.2.00, 2-station occupied x4 setti							
		IN area				OUT area		
CC-Link slave (UTAdvanced) → CC-Link master				C-Link m	aster → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
RWr9		02: PV_L1		RWw9		02: H.TSP_L1		
RWr10		02: CSP_L1		RWw10		02: H.SP_L1		
RWr11		02: SEG_RTIME		RWw11		02: H.TM_L1		
RWr12		02: LSP_L1		RWw12		02: LSP_L1		
RWr13		02: OUT_L1		RWw13		02: MOUT_L1		
RWr14		02: C.PTNO.		RWw14		02: PTNO.		
RWr15		02: SEG.N		RWw15		02: SST		
RWr16		(Unused)		RWw16		(Unused)		
RWr17		03: PV_L1		RWw17		03: H.TSP_L1		
RWr18		03: CSP_L1		RWw18		03: H.SP_L1		
RWr19		03: SEG_RTIME		RWw19		03: H.TM_L1		
RWr20		03: LSP_L1		RWw20		03: LSP_L1		
RWr21		03: OUT_L1		RWw21		03: MOUT_L1		
RWr22		03: C.PTNO.		RWw22		03: PTNO.		
RWr23		03: SEG.N		RWw23		03: SST		
RWr24		(Unused)		RWw24		(Unused)		
RWr25		04: PV_L1		RWw25		04: H.TSP_L1		
RWr26		04: CSP_L1		RWw26		04: H.SP_L1		
RWr27		04: SEG_RTIME		RWw27		04: H.TM_L1		
RWr28		04: LSP_L1		RWw28		04: LSP_L1		
RWr29		04: OUT_L1		RWw29		04: MOUT_L1		
RW30		04: C.PTNO.		RWw30		04: PTNO.		
RWr31		04: SEG.N		RWw31		04: SST		

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area					
Word	C-Link sl Bit	ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced					
	position	Contents of assignment		position	Contents of assignment				
	RX0	Receive data valid		RY0	Rescan request				
	RX1	During-write		RY1	(Reserved)				
	RX2	Write acknowledgement		RY2	Write request				
	RX3	(Reserved)		RY3	(Reserved)				
	RX4	(Reserved)		RY4	(Reserved)				
	RX5	(Reserved)		RY5	(Reserved)				
	RX6	(Reserved)		RY6	(Reserved)				
	RX7	(Reserved)		RY7	(Reserved)				
	•	The fixed-part is omitted		•	The fixed-part is omitted				
	•	(See profile number 0 on page 1)		•	(See profile number 0 on page 1)				
	•	(See profile fluffiber of off page 1)		•	, , ,				
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)				
	RX48	(Unused)		RY48	(Unused)				
	:			:					
	RX175	(Unused)		RY175	(Unused)				
		(Reserved)		RY176	(Reserved)				
	:	(Neserved)		:	(Neserved)				
	•			•					
	RX187	Remote Ready		RY187	(Reserved)				
	:								
	RX191	(Reserved)		RY191	(Reserved)				
RWr0		Current page	RWw0		Page change request				
RWr1		01: P_L1_1	RWw1		01: P_L1_1				
RWr2		01: I_L1_1	RWw2		01: I_L1_1				
RWr3		01: D_L1_1	RWw3		01: D_L1_1				
RWr4		01: L.PID	RWw4		01: L.PID				
RWr5		01: A1_L1_1	RWw5		01: A1_L1_1				
RWr6		01: A2_L1_1	RWw6		01: A2_L1_1				
RWr7		01: A3_L1_1	RWw7		01: A3_L1_1				
RWr8		(Unused)	RWw8		(Unused)				
RWr9		02: P_L1_1	RWw9		02: P_L1_1				
RWr10		02: I_L1_1	RWw10		02: I_L1_1				
RWr11		02: D_L1_1	RWw11		02: D_L1_1				
RWr12		02: L.PID	RWw12		02: L.PID				
RWr13		02: A1_L1_1	RWw13		02: A1_L1_1				
RWr14		02: A2_L1_1	RWw14		02: A2_L1_1				
RWr15		02: A3_L1_1	RWw15		02: A3_L1_1				
RWr16		(Unused)	RWw16		(Unused)				
RWr17		03: P_L1_1	RWw17		03: P_L1_1				
RWr18		03: I_L1_1	RWw18		03: I_L1_1				
RWr19		03: D_L1_1	RWw19		03: D_L1_1				
RWr20		03: L.PID	RWw20		03: L.PID				
RWr21		03: A1_L1_1	RWw21		03: A1_L1_1				
RWr22		03: A2_L1_1	RWw22		03: A2_L1_1				
RWr23		03: A3_L1_1	RWw23		03: A3_L1_1				
RWr24		(Unused)	RWw24		(Unused)				
RWr25		04: P_L1_1	RWw25		04: P_L1_1				
RWr26		04: I_L1_1	RWw26		04: I_L1_1				
RWr27		04: D_L1_1	RWw27		04: D_L1_1				
RWr28		04: L.PID	RWw28		04: L.PID				
RWr29		04: A1 L1 1	RWw29		04: A1_L1_1				
RW30		04: A2_L1_1	RWw30		04: A2_L1_1				
RWr31		04: A3_L1_1	RWw31		04: A3_L1_1				

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Page 3

		12 (Simple PID control IN area				(Ver.2.00, 2-stati		
Word	C-Link sla Bit	ave (UTAdvanced) → C		Word	C-Link m Bit	master → CC-Link slave (UTAdvanced)		
	position	Contents of a	ssignment	position		Contents	of assignment	
		Receive data valid			RY0	Rescan request		
		During-write			RY1	(Reserved)		
	RX2 RX3	Write acknowledgemen (Reserved)	I .		RY2 RY3	Write request (Reserved)		
	RX4	(Reserved)			RY4	(Reserved)		
	RX5	(Reserved)			RY5	(Reserved)		
	RX6	(Reserved)			RY6	(Reserved)		
	RX7	(Reserved)			RY7	(Reserved)		
		The fixed-par (See profile numb			•		I-part is omitted umber 0 on page 1)	
	RX47	Normal connection slav	e (address 32)		RY47	Batch write request	(address 32)	
	RX48	(Unused)			RY48	(Unused)		
	:				:			
	RX175	(Unused)			RY175	(Unused)		
	RX176	(Reserved)			RY176	(Reserved)		
	:				•••			
	RX187	Remote Ready				(Reserved)		
	:				:			
	RX191	(Reserved)			RY191	(Reserved)		
RWr0		Current page		RWw0		Page change reque	oot	
RWr1		Current page 01: L.TY1		RWw1		01: L.TY1	:51	
RWr2		01: L.EV1		RWw2		01: L.EV1		
RWr3		01: L.TY2		RWw3		01: L.TY2		
RWr4		01: L.EV2		RWw4		01: L.EV2		
RWr5		01: L.TY3		RWw5		01: L.TY3	_]	
RWr6		01: L.EV3		RWw6		01: L.EV3		
RWr7		01: L.TY4		RWw7		01: L.TY4		
RWr8		01: L.EV4		RWw8		01: L.EV4		
RWr9		01: L.TY5	UP35A/UP32A:	RWw9		01: L.TY5	UP35A/UP32A	
RWr10		01: L.EV5.	unused	RWw10		01: L.EV5.	unused	
RWr11		01: L.TY6		RWw11		01: L.TY6	_	
RWr12		01: L.EV6		RWw12		01: L.EV6	—	
RWr13		01: L.TY7		RWw13		01: L.TY7	_	
RWr14		01: L.EV7		RWw14		01: L.EV7	— J	
RWr15		(Unused)		RWw15		(Unused)		
RWr16		(Unused)		RWw16		(Unused)		
RWr17		02: L.TY1		RWw17		02: L.TY1		
RWr18		02: L.EV1		RWw18		02: L.FV1		
RWr19		02: L.TY2		RWw19		02: L.TY2		
RWr20		02: L.EV2		RWw20		02: L.EV2		
RWr21		02: L.TY3		RWw21		02: L.TY3]	
RWr22		02: L.EV3		RWw22		02: L.EV3		
RWr23		02: L.TY4		RWw23		02: L.TY4		
RWr24		02: L.EV4		RWw24		02: L.EV4		
RWr25		02: L.TY5	UP35A/UP32A:	RWw25		02: L.TY5	 UP35A/UP32A	
RWr26		02: L.EV5.	unused	RWw26		02: L.EV5.	unused	
RWr27		02: L.TY6		RWw27		02: L.TY6	_	
RWr28		02: L.EV6		RWw28		02: L.EV6	—	
RWr29		02: L.TY7		RWw29		02: L.TY7		
RW30				RWw30			— J	
		02: L.EV7 (Unused)		RWw30		02: L.EV7 (Unused)		

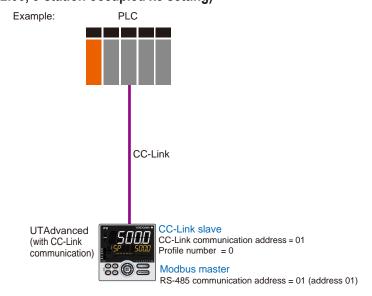
Profile	number	<u> </u>	ntrol with 4 connected co	ontrollers) on	page 4		tion occupied x4 settin	
C	C Link of	IN area	→ CC-Link master		OUT area CC-Link master → CC-Link slave (UTAdvanced)			
Word	Bit	· · · · · · · · · · · · · · · · · · ·		Word	Bit			
position	position		s of assignment	position	position		s or assignment	
	RX0 RX1	Receive data valid During-write			RY0 RY1	Rescan request (Reserved)		
	RX2	Write acknowledgement			RY2	Write request		
	RX3	(Reserved)			RY3	(Reserved)		
	RX4	(Reserved)			RY4	(Reserved)		
	RX5	(Reserved)			RY5	(Reserved)		
	RX6	(Reserved)			RY6	(Reserved)		
	RX7	(Reserved)			RY7	(Reserved)		
			d-part is omitted number 0 on page 1)				d-part is omitted number 0 on page 1)	
	RX47	Normal connection	slave (address 32)		RY47	Batch write reques	t (address 32)	
	RX48	(Unused)			RY48	(Unused)		
	:				:			
	RX175	(Unused)			RY175	(Unused)		
	RX176	(Reserved)			RY176	(Reserved)		
	:				:			
	<u> </u>	Remote Ready			RY187	(Reserved)		
	:				:			
	RX191	(Reserved)			RY191	(Reserved)		
RWr0	ĺ	Current page		RWw0		Page change requ	oct	
RWr1		Current page 03: L.TY1		RWw1		03: L.TY1	esi	
RWr2		03: L.EV1		RWw2		03: L.EV1		
RWr3		03: L.TY2		RWw3		03: L.TY2		
RWr4		03: L.EV2		RWw4		03: L.EV2		
RWr5		03: L.TY3)	RWw5		03: L.TY3)	
RWr6		03: L.FV3		RWw6		03: L.TY3		
RWr7		03: L.EV3		RWw7		03: L.EV3		
RWr8				RWw8				
		03: L.EV4	LIBOTA / IBOOA			03: L.EV4	LIBOSA // IBOOA	
RWr9		03: L.TY5	UP35A/UP32A: unused	RWw9		03: L.TY5	UP35A/UP32A: unused	
RWr10		03: L.EV5.	unuscu	RWw10		03: L.EV5.	unuocu	
RWr11		03: L.TY6		RWw11		03: L.TY6		
RWr12		03: L.EV6		RWw12		03: L.EV6		
RWr13		03: L.TY7		RWw13		03: L.TY7		
RWr14		03: L.EV7)	RWw14		03: L.EV7)	
RWr15		(Unused)		RWw15		(Unused)		
RWr16		(Unused)		RWw16		(Unused)		
RWr17		04: L.TY1		RWw17		04: L.TY1		
RWr18		04: L.EV1		RWw18		04: L.EV1		
RWr19		04: L.TY2		RWw19		04: L.TY2		
RWr20		04: L.EV2		RWw20		04: L.EV2		
RWr21		04: L.TY3		RWw21		04: L.TY3		
RWr22		04: L.EV3		RWw22		04: L.EV3		
RWr23		04: L.TY4		RWw23		04: L.TY4		
RWr24		04: L.EV4		RWw24		04: L.EV4		
RWr25		04: L.TY5	UP35A/UP32A:	RWw25		04: L.TY5	UP35A/UP32A:	
RWr26		04: L.EV5.	unused	RWw26		04: L.EV5.	unused	
RWr27		04: L.TY6		RWw27		04: L.TY6		
RWr28		04: L.EV6		RWw28		04: L.EV6		
RWr29	İ	04: L.TY7		RWw29		04: L.TY7		
RW30		04: L.EV7	J	RWw30		04: L.EV7	J	
RWr31		(Unused)		RWw31		(Unused)		

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Intentionally blank

Profile number 13 (Simple PID control with program pattern setting for 1 connected controller) (Ver.2.00, 3-station occupied x8 setting)





Page 1

	C-Link sl	IN area ave (UTAdvanced) → C	C-Link master		C-Link m	OUT area aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of a	ssignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgemen	t		RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	•	The fixed-par	t is omitted		•	The fixed-part is omitted
	•	(See profile numb			•	(See profile number 0 on page 1)
	•	` '	,		•	
	RX47	Normal connection slav	e (address 32)		RY47	Batch write request (address 32)
	RX48	01: RST_ON			RY48	01: RST_ON
	RX49	01: PRG_ON			RY49	01: PRG_ON
	-	01: LOC_ON			RY50	01: LOC_ON
	RX51	01: HOLD			RY51	01: HOLD
	RX52	(Unused)			RY52	01: ADV
	RX53	01: A.M_L1			RY53	01: A.M_L1
	RX54	(Unused)			RY54	(Unused)
	RX55	(Unused)			RY55	(Unused)
	RX56	(Unused)			RY56	(Unused)
	RX57	(Unused)			RY57	(Unused)
	RX58	(Unused)			RY58	(Unused)
	RX59	(Unused)			RY59	(Unused)
	RX60	(Unused)			RY60	(Unused)
	RX61	(Unused)			RY61	(Unused)
	RX62	(Unused)			RY62	(Unused)
	RX63	(Unused)			RY63	(Unused)
	RX64	01: PV_EV1			RY64	(Unused)
	RX65	01: PV_EV2			RY65	(Unused)
	RX66	01: PV_EV3			RY66	(Unused)
	RX67	01: PV_EV4			RY67	(Unused)
	RX68	01: PV_EV5	UP35A/UP32A:		RY68	(Unused)
	RX69	01: PV_EV6	unused		RY69	(Unused)
	RX70	01: PV_EV7			RY70	(Unused)
	RX71	01: PV_EV8			RY71	(Unused)
	RX72	02: ALM1_L1			RY72	(Unused)

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Profile n	umber 13 (Simple PID control with pro	gram pattern setting for 1	connected controller) on page 1 (Ver.2.00, 3-station occupied x8 setting)			
		IN area				OUT area	
	C-Link sl	ave (UTAdvanced) $ ightarrow$ C	C-Link master		C-Link m	aster → CC-Link sla	ve (UTAdvanced)
Word	Bit position	Contents of a	ssignment	Word	Bit position	Contents	of assignment
pooition	_	02: ALM2_L1		poolition	RY73	(Unused)	
	-		JP35A: unused		RY74	(Unused)	
	RX75	02: ALM4_L1 l	JP35A: unused		RY75	(Unused)	
	RX76	(Unused)			RY76	(Unused)	
	RX77	(Unused)			RY77	(Unused)	
	RX78	(Unused)			RY78	(Unused)	
	RX79	(Unused)			RY79	(Unused)	
	RX80	01: TIME_EV1			RY80	(Unused)	
	RX81	01: TIME_EV2			RY81	(Unused)	
		01: TIME_EV3			RY82	(Unused)	
	RX83	01: TIME_EV4			RY83	(Unused)	
		01: TIME_EV5			RY84	(Unused)	
		01: TIME_EV6			RY85	(Unused)	
		01: TIME_EV7			RY86	(Unused)	
		01: TIME_EV8			RY87	(Unused)	
	_	01: TIME_EV9			RY88	(Unused)	
	-	01: TIME_EV10	UP35A/UP32A: unused		RY89	(Unused)	
	_	01: TIME_EV11	unuseu		RY90	(Unused)	
		01: TIME_EV12			RY91	(Unused)	
		01: TIME_EV13			RY92	(Unused)	
		01: TIME_EV14			RY93	(Unused)	
	_	01: TIME_EV15			RY94	(Unused)	
		01: TIME_EV16	,		RY95	(Unused)	
		(Unused)			RY96	(Unused)	
	:				:		
		(Unused)				(Unused)	
		(Reserved)			-	(Reserved)	
	:				:		
	RX635	Remote Ready			RY635	(Reserved)	
	:						
	RX639	(Reserved)			RY639	(Reserved)	
RWr0		Current page		RWw0		Page change reques	st
RWr1		01: PV_L1		RWw1		01: H.TSP_L1	
RWr2		01: CSP_L1		RWw2		01: H.SP_L1	
RWr3		_	JP35A/UP32A: unused	RWw3		01: H.SP_L2	UP35A/UP32A: unused
RWr4		01: SEG.RTIME		RWw4		01: H.TM_L1	
RWr5		01: LSP_L1		RWw5		01: LSP_L1	
RWr6			JP35A/UP32A: unused	RWw6		01: LSP_L2	UP35A/UP32A: unused
RWr7		01: OUT_L1		RWw7		01: MOUT_L1	
RWr8		01: H.OUT_L1		RWw8		01: MOUT_L1	
RWr9		01: C.OUT_L1		RWw9		01: MOUTc_L1	
RWr10		01: C.PTNO.		RWw10		01: PTNO.	
RWr11		01: SEG.N		RWw11		01: SST	
RWr12		(Unused)		RWw12		(Unused)	
÷				:			
RWr95		(Unused)		RWw95		(Unused)	

Profile N	ulliber 13 (Simple PID control with pro	yranı pattern setting för 1	connected con	u oner) on	page 2 (ver.2.00, 3-sta OUT area	ation occupied x8 setting)
C	C_l ink el	IN area ave (UTAdvanced) → C	C-I ink master		C-I ink m	OUT area aster → CC-Link sla	ave (LITAdvanced)
Word	Bit			Word	Bit		· · · · · · · · · · · · · · · · · · ·
position	position	Contents of a	assignment	position	position		of assignment
	RX0	Receive data valid			RY0	Rescan request	
	RX1 RX2	During-write Write acknowledgemen	4		RY1 RY2	(Reserved)	
	RX3	(Reserved)	L .		RY3	Write request (Reserved)	
	RX4	(Reserved)			RY4	(Reserved)	
	RX5	(Reserved)			RY5	(Reserved)	
	RX6	(Reserved)			RY6	(Reserved)	
	RX7	(Reserved)			RY7	(Reserved)	
	•	The fixed-par	t is smitted		•	The fived	-part is omitted
		(See profile numb			•		umber 0 on page 1)
	•	` '			•		
	RX47	Normal connection slav	e (address 32)		RY47	Batch write request	(address 32)
	RX48	(Unused)			RY48	(Unused)	
	:				:		
	RX623	(Unused)			RY623	(Unused)	
	RX624	(Reserved)			RY624	(Reserved)	
	<u> </u>				<u>:</u>		
	RX635	Remote Ready			RY635	(Reserved)	
	:				:		
	RX639	(Reserved)			RY639	(Reserved)	
RWr0		Current page		RWw0		Page change reque	est
RWr1		01: P_L1_1		RWw1		01: P_L1_1	
RWr2		01: I_L1_1		RWw2		01: I_L1_1	
RWr3		01: D_L1_1		RWw3		01: D_L1_1	
RWr4	-	01: Pc_L1_1		RWw4		01: Pc_L1_1	
RWr5 RWr6		01: lc_L1_1 01: Dc_L1_1		RWw5 RWw6		01: lc_L1_1 01: Dc_L1_1	
RWr7		01: L.PID		RWw7		01: L.PID	
RWr8		01: A1_L1_1		RWw8		01: A1_L1_1	
RWr9		01: A2_L1_1		RWw9		01: A2_L1_1	
RWr10		01: A3_L1_1	JP35A/UP32A: unused	RWw10		01: A3_L1_1	UP35A/UP32A: unused
RWr11		01: A4_L1_1	JP35A/UP32A: unused	RWw11		01: A4_L1_1	UP35A/UP32A: unused
RWr12		(Unused)		RWw12		(Unused)	
:				:			
RWr20		(Unused)		RWw20		(Unused)	
RWr21		01: L.TY1		RWw21		01: L.TY1	
RWr22		01: L.EV1		RWw22		01: L.EV1	
RWr23	-	01: L.TY2		RWw23	1	01: L.TY2	
RWr24		01: L.EV2		RWw24		01: L.EV2	
RWr25	-	01: L.TY3		RWw25 RWw26	-	01: L.TY3	—)
RWr26 RWr27		01: L.EV3 01: L.TY4		RWw26		01: L.EV3 01: L.TY4	—
RWr28	-	01: L.EV4		RWw27	-	01: L.EV4	—
RWr29		01: L.TY5		RWw29		01: L.TY5	
RWr30		01: L.EV5.	UP35A/UP32A:	RWw30		01: L.EV5.	— UP35A/UP32A:
RWr31		01: L.TY6	unused	RWw31		01: L.TY6	unused
RWr32		01: L.EV6		RWw32		01: L.EV6	
RWr33		01: L.TY7		RWw33		01: L.TY7	
RWr34		01: L.EV7		RWw34		01: L.EV7	
RWr35		01: L.TY8		RWw35		01: L.TY8	
RWr36	-	01: L.EV8)	RWw36	-	01: L.EV8	J
RWr37		(Unused)		RWw37	-	(Unused)	
:	1				ļ		
RWr70	-	(Unused)		RWw70		(Unused)	
RWr71	-	01: CLR.P		RWw71		01: CLR.P	
RWr72	-	01: CLR.TRG		RWw72		01: CLR.TRG	
RWr73	-	(Upused)		RWw73	-	(Unused)	
RWr74	-	(Unused)		RWw74	-	(Unused)	
: DW/ 05		(1.1		DIA	-	(1.1	
RWr95		(Unused)		RWw95		(Unused)	

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Page 3

Profile n	umber 13 (Simple PID control with pro	gram pattern setting for 1 c	onnected con	troller) on	page 3 (Ver.2.00, 3-sta	tion occupied x8 setting)
		IN area				OUT area	
Word		ave (UTAdvanced) → C	C-Link master	Word		aster → CC-Link sla	ive (UTAdvanced)
	Bit position	Contents of	assignment		Bit position	Contents	of assignment
•	RX0	Receive data valid			RY0	Rescan request	
	RX1	During-write			RY1	(Reserved)	
	RX2	Write acknowledgemen	t		RY2	Write request	
	RX3 RX4	(Reserved)			RY3 RY4	(Reserved)	
	RX5	(Reserved)			RY5	(Reserved)	
	RX6	(Reserved)			RY6	(Reserved)	
	RX7	(Reserved)			RY7	(Reserved)	
		The fixed-pa (See profile numb					-part is omitted umber 0 on page 1)
	RX47	Normal connection slav	e (address 32)		RY47	Batch write request	(address 32)
	RX48	(Unused)			RY48	(Unused)	
	:				:		
	RX623	(Unused)			RY623	(Unused)	
	RX624	(Reserved)			RY624	(Reserved)	
	:				:		
	RX635	Remote Ready			RY635	(Reserved)	
	:				:		
	RX639	(Reserved)			RY639	(Reserved)	
	,	,				,	
RWr0		Current page		RWw0		Page change reque	st
RWr1		01: PTNOC		RWw1		01: PTNOC	
RWr2		01: SEGNOC		RWw2		01: SEGNOC	
RWr3		01: SSP_L1		RWw3		01: SSP_L1	
RWr4		_	JP35A/UP32A: unused	RWw4		01: SSP_L2	UP35A/UP32A: unused
RWr5 RWr6		01: STC 01: WT.SW1		RWw5 RWw6		01: STC 01: WT.SW1	
RWr7		01: WZ.UP1		RWw7		01: WZ.UP1	
RWr8		01: WZ.LO1		RWw8		01: WZ.LO1	
RWr9		01: WT.TM1		RWw9		01: WT.TM1	
RWr10		01: WT.SW2)	RWw10		01: WT.SW2)
RWr11		01: WZ.UP2		RWw11		01: WZ.UP2	_
RWr12		01: WZ.LO2		RWw12		01: WZ.LO2	
RWr13		01: WT.TM2		RWw13		01: WT.TM2	_
RWr14		01: WT.SW3		RWw14		01: WT.SW3	_
RWr15		01: WZ.UP3		RWw15		01: WZ.UP3	_
RWr16		01: WZ.LO3		RWw16		01: WZ.LO3	_
RWr17 RWr18		01: WT.TM3 01: WT.SW4	UP35A/UP32A:	RWw17 RWw18		01: WT.TM3 01: WT.SW4	— \UP35A/UP32A:
RWr19		01: WZ.UP4	unused	RWw19		01: WZ.UP4	— unused
RWr20		01: WZ.LO4		RWw20		01: WZ.LO4	-
RWr21		01: WT.TM4		RWw21		01: WT.TM4	_
RWr22		01: WT.SW5		RWw22		01: WT.SW5	_
RWr23		01: WZ.UP5		RWw23		01: WZ.UP5	_
RWr24		01: WZ.LO5		RWw24		01: WZ.LO5	
RWr25		01: WT.TM5	J	RWw25		01: WT.TM5	J
RWr26		01: R.CYCL		RWw26		01: R.CYCL	
RWr27		01: R.STRT		RWw27		01: R.STRT	
RWr28		01: R.END		RWw28		01: R.END	
RWr29		(Unused)		RWw29		(Unused)	
RWr30 RWr31		(Unused) 01: P.NAME		RWw30 RWw31	-	(Unused) 01: P.NAME	
RWr32		01: P.NAME		RWw31		01: P.NAME	
RWr33		01: P.NAME		RWw33		01: P.NAME	
RWr34		01: P.NAME		RWw34		01: P.NAME	
RWr35		01: P.NAME		RWw35		01: P.NAME	
RWr36		01: P.NAME		RWw36	İ	01: P.NAME	
RWr37		01: P.NAME		RWw37		01: P.NAME	
RWr38		01: P.NAME		RWw38		01: P.NAME	
RWr39		01: P.NAME		RWw39		01: P.NAME	
RWr40		01: P.NAME		RWw40		01: P.NAME	

Profile n	umber 13 (Simple PID control with program pattern setting for 1	page 3 (Ver.2.00, 3-station occupied x8 setting)				
	IN area			OUT area			
С	CC-Link slave (UTAdvanced) → CC-Link master			С	C-Link m	aster → CC-Link slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
RWr41		01: P.NAME		RWw41		01: P.NAME	
RWr42		01: PTN.ERR		RWw42		(Unused)	
RWr43		(Unused)		RWw43		(Unused)	
:				:			
RWr95		(Unused)		RWw95		(Unused)	
			-				

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Profile no	Profile number 13 (Simple PID control with program pattern setting for 1						
		IN area	Γ				
		ave (UTAdvanced) → CC-Link master	1				
Word position	Bit position	Contents of assignment					
	RX0	Receive data valid					
	RX1	During-write					
	RX2	Write acknowledgement	Vrite acknowledgement				
	RX3	(Reserved)	Reserved)				
	RX4	(Reserved)					
	RX5	(Reserved)					
	RX6	(Reserved)					
	RX7	(Reserved)					
	•	The fixed-part is omitted					
	•	(See profile number 0 on page 1)	l				
	•	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	1				
	RX47	Normal connection slave (address 32)	1				
	RX48	(Unused)					
	:						
	RX623	(Unused)					
	RX624	(Reserved)					
	:						
	RX635	Remote Ready					
	:						
	RX639	(Reserved)					

nected conf	OUT area						
C	C-Link m	aster → CC-Link slave (UTAdvanced)					
Word position	Bit	Contents of assignment					
position	RY0	Rescan request					
	RY1	(Reserved)					
	RY2	Write request					
	RY3	(Reserved)					
	RY4	(Reserved)					
	RY5	(Reserved)					
	RY6	(Reserved)					
	RY7	(Reserved)					
	•	The fixed-part is omitted (See profile number 0 on page 1)					
	•	· ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '					
	RY47	Batch write request (address 32)					
	RY48	(Unused)					
	:						
	RY623	(Unused)					
	RY624	(Reserved)					
	:						
	RY635	(Reserved)					
	:						
	RY639	(Reserved)					

RWr0	Current page
RWr1	01: PTNOC
RWr2	01: SEGNOC
RWr3	01: TSP_L1
RWr4	01: TSP_L2 UP35A/UP32A: unused
RWr5	01: TIME
RWr6	01: TM.RT
RWr7	01: S.PID
RWr8	01: JC
RWr9	01: PV.TY1
RWr10	01: PV.EV1
RWr11	01: PV.TY2
RWr12	01: PV.EV2
RWr13	01: PV.TY3
RWr14	01: PV.EV3
RWr15	01: PV.TY4
RWr16	01: PV.EV4
RWr17	01: PV.TY5
RWr18	01: PV.EV5 UP35A/UP32A:
RWr19	01: PV.TY6 unused
RWr20	01: PV.EV6
RWr21	01: PV.TY7
RWr22	01: PV.EV7
RWr23	01: PV.TY8
RWr24	01: PV.EV8
RWr25	01: TME1
RWr26	01: T.ON1
RWr27	01: T.OF1
RWr28	01: TME2
RWr29	01: T.ON2
RWr30	01: T.OF2
RWr31	01: TME3
RWr32	01: T.ON3
RWr33	01: T.OF3
RWr34	01: TME4
RWr35	01: T.ON4
RWr36	01: T.OF4

RWw0	F	Page change request	
RWw1	C	1: PTNOC	
RWw2	C	1: SEGNOC	
RWw3	C)1: TSP_L1	
RWw4	C)1: TSP_L2	UP35A/UP32A: unused
RWw5	C	1: TIME	
RWw6	C	1: TM.RT	
RWw7	C)1: S.PID	
RWw8	C)1: JC	
RWw9	C)1: PV.TY1	
RWw10	C)1: PV.EV1	
RWw11	C)1: PV.TY2	
RWw12	C)1: PV.EV2	
RWw13	C)1: PV.TY3	
RWw14	C)1: PV.EV3	
RWw15	C)1: PV.TY4	
RWw16	C)1: PV.EV4	
RWw17	C)1: PV.TY5	
RWw18	C)1: PV.EV5	UP35A/UP32A:
RWw19	C)1: PV.TY6	unused
RWw20	C)1: PV.EV6	
RWw21	C)1: PV.TY7	
RWw22	C)1: PV.EV7	
RWw23	C)1: PV.TY8	
RWw24	C)1: PV.EV8	J
RWw25	C)1: TME1	
RWw26	C)1: T.ON1	
RWw27	C)1: T.OF1	
RWw28	C)1: TME2	
RWw29	C)1: T.ON2	
RWw30	C)1: T.OF2	
RWw31	C	1: TME3	
RWw32	C	1: T.ON3	
RWw33	C	1: T.OF3	
RWw34	C)1: TME4	
RWw35	C	1: T.ON4	
RWw36	C	1: T.OF4	

4.9 Profile List

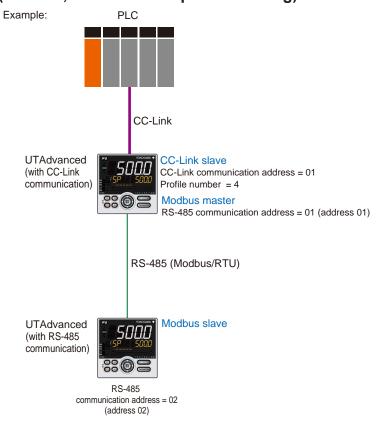
Profile n	umber 13 (Simple PID control with	program pattern setting for 1	connected con	troller) on	page 4 (Ver.2.00, 3-station	n occupied x8 setting
		IN area				OUT area	
		ave (UTAdvanced) -	→ CC-Link master			$aster \to CC\text{-}Link\;slave$	(UTAdvanced)
Word	Bit position	Contents	of assignment	Word	Bit position	Contents of	assignment
RWr37	position	01: TME5)	RWw37	position	01: TME5	`
RWr38		01: T.ON5		RWw38		01: T.ON5	
RWr39		01: T.OF5		RWw39		01: T.OF5	
RWr40		01: TME6		RWw40		01: TME6	
RWr41		01: T.ON6		RWw41		01: T.ON6	
RWr42		01: T.OF6		RWw42		01: T.OF6	
RWr43		01: TME7		RWw43		01: TME7	
RWr44		01: T.ON7		RWw44		01: T.ON7	
RWr45		01: T.OF7		RWw45		01: T.OF7	
RWr46		01: TME8		RWw46		01: TME8	
RWr47		01: T.ON8		RWw47		01: T.ON8	
RWr48		01: T.OF8		RWw48		01: T.OF8	
RWr49		01: TME9		RWw49		01: TME9	
RWr50		01: T.ON9		RWw50		01: T.ON9	
RWr51		01: T.OF9		RWw51		01: T.OF9	
RWr52		01: TME10		RWw52		01: TME10	
RWr53		01: T.ON10		RWw53		01: T.ON10	
RWr54		01: T.OF10	LIDOS A # IDOO A	RWw54		01: T.OF10	
RWr55		01: TME11	UP35A/UP32A:	RWw55		01: TME11	UP35A/UP32
RWr56		01: T.ON11	unused	RWw56		01: T.ON11	unused
RWr57		01: T.OF11		RWw57		01: T.OF11	
RWr58		01: TME12		RWw58		01: TME12	
RWr59		01: T.ON12		RWw59		01: T.ON12	
RWr60		01: T.OF12		RWw60		01: T.OF12	
RWr61		01: TME13		RWw61		01: TME13	
RWr62		01: T.ON13		RWw62		01: T.ON13	
RWr63		01: T.OF13		RWw63		01: T.OF13	
RWr64		01: TME14		RWw64		01: TME14	
RWr65		01: T.ON14		RWw65		01: T.ON14	
RWr66		01: T.OF14		RWw66		01: T.OF14	
RWr67	1	01: TME15		RWw67		01: TME15	
RWr68		01: T.ON15		RWw68		01: T.ON15	
RWr69		01: T.OF15		RWw69		01: T.OF15	
RWr70		01: TME16		RWw70		01: TME16	
RWr71		01: T.ON16		RWw71		01: T.ON16	
RWr72		01: T.OF16	J	RWw72		01: T.OF16	J
RWr73		01: PTN.ERR		RWw73		(Unused)	
RWr74		(Unused)		RWw74		(Unused)	
:				:			
RWr95		(Unused)		RWw95		(Unused)	

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Profile number 14 (Cascade control with 2 connected controllers) (Ver.2.00, 2-station occupied x4 setting)





Page 1

Profile	Profile number 14 (Cascade control with 2 connected control			on pa	age 1	(Ver.2.00, 2-station occupied x4 setting)	
		IN area		OUT area			
C	C-Link sl	ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced			
Word position	Bit position	Contents of assignment		ord sition	Bit position	Contents of assignment	
	RX0	Receive data valid			RY0	Rescan request	
	RX1	During-write			RY1	(Unused)	
	RX2	Write acknowledgement			RY2	Write request	
	RX3	(Reserved)			RY3	(Reserved)	
	RX4	(Reserved)			RY4	(Reserved)	
	RX5	(Reserved)			RY5	(Reserved)	
	RX6	(Reserved)			RY6	(Reserved)	
	RX7	(Reserved)			RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)	
	RX48	01: RST_ON			RY48	01: RST_ON	
	RX49	01: PRG_ON			RY49	01: PRG_ON	
	RX50	01: LOC_ON			RY50	01: LOC_ON	
	RX51	01: HOLD			RY51	01: HOLD	
	RX52	(Unused)			RY52	01: ADV	
	RX53	01: A.M_L2			RY53	01: A.M_L2	
	RX54	(Unused)			RY54	(Unused)	
	RX55	(Unused)			RY55	(Unused)	
	RX56	01: L.C			RY56	01: L.C	
	RX57	(Unused)			RY57	(Unused)	
	RX58	(Unused)			RY58	(Unused)	
	RX59	(Unused)			RY59	(Unused)	
	RX60	(Unused)			RY60	(Unused)	
	RX61	(Unused)			RY61	(Unused)	
	RX62	(Unused)			RY62	(Unused)	
	RX63	(Unused)			RY63	(Unused)	

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Profile	number	14 (Cascade control with 2 connected co	ntrolle	ers) on pa	ige 1	(Ver.2.00, 2-station occupied x4 setting	
C	C Link el	IN area ave (UTAdvanced) → CC-Link master		OUT area CC-Link master → CC-Link slave (UTAdvan			
Word	Bit	,		Word	Bit	1	
position	position	Contents of assignment		position	position		
		01: PV_EV1 01: PV EV2	-		RY64 RY65	(Unused)	
		01: PV_EV2 01: PV_EV3	\dashv			(Unused)	
		01: PV EV4	\dashv			(Unused)	
		01: PV_EV5	_		RY68	(Unused)	
	RX69	01: PV_EV6			RY69	(Unused)	
		01: PV_EV7			RY70	(Unused)	
		01: PV_EV8	_		RY71	(Unused)	
	-	01: ALM1_L1	_		RY72	(Unused)	
		01: ALM2_L1	4			(Unused)	
		01: ALM3_L1 01: ALM4_L1	-		RY74 RY75	(Unused)	
		(Unused)	\dashv		RY76	(Unused)	
		(Unused)			RY77	(Unused)	
		(Unused)	\dashv		RY78	(Unused)	
	_	(Unused)			RY79	(Unused)	
	RX80	01: TIME_EV1			RY80	(Unused)	
		01: TIME_EV2			RY81	(Unused)	
		01: TIME_EV3	_		RY82	(Unused)	
		01: TIME_EV4			RY83	(Unused)	
		01: TIME_EV5	4		RY84	(Unused)	
		01: TIME_EV6	_		RY85	(Unused)	
		01: TIME_EV7 01: TIME_EV8	\dashv		RY86 RY87	(Unused)	
		01: TIME_EV9	_		RY88	(Unused)	
		01: TIME_EV10	+		RY89	(Unused)	
		01: TIME EV11	1		RY90	(Unused)	
	RX91	01: TIME_EV12			RY91	(Unused)	
	RX92	01: TIME_EV13			RY92	(Unused)	
		01: TIME_EV14			RY93	(Unused)	
		01: TIME_EV15			R94	(Unused)	
		01: TIME_EV16	_		RY95	(Unused)	
	RX96	(Unused)	_		RY96	(Unused)	
	RX97	(Unused)	4		RY97	(Unused)	
	RX98 RX99	(Unused)	-		RY98 RY99	(Unused)	
		(Unused)	\dashv			(Unused)	
		(Unused)	-			(Unused)	
		(Unused)	1			(Unused)	
	RX103	(Unused)			RY103	(Unused)	
	RX104	(Unused)			RY104	(Unused)	
		(Unused)				(Unused)	
	i	(Unused)	_			(Unused)	
		(Unused)	_			(Unused)	
		(Unused)	\dashv			(Unused)	
		(Unused)	\dashv			(Unused)	
		(Unused)	\dashv			(Unused)	
		02: RST_ON	\dashv			02: RST_ON	
		02: PRG_ON	\dashv			02: PRG_ON	
		02: LOC_ON				02: LOC_ON	
		02: HOLD			RY115	02: HOLD	
	RX116	(Unused)			RY116	02: ADV	
		02: A.M_L2	_			02: A.M_L2	
	_	(Unused)	_			(Unused)	
		(Unused)	4			(Unused)	
	RX120	l .	4			02: L.C	
	-	(Unused)	\dashv			(Unused)	
		(Unused)	\dashv			(Unused)	
		(Unused)	\dashv			(Unused)	
		(Unused)	\dashv			(Unused)	
		(Unused)	\dashv			(Unused)	
		13 7		1		IN THE STATE OF TH	

Profile	number	14 (Cascade control with 2 connected co	ontrollers) on p	age 1	(Ver.2.00, 2-station occupied x4 setting)
		IN area			OUT area
Word		ave (UTAdvanced) → CC-Link master			naster → CC-Link slave (UTAdvanced)
	Bit position	Contents of assignment	Word	Bit position	Contents of assignment
		02: PV_EV1		RY128	(Unused)
		02: PV_EV2		+	(Unused)
		02: PV_EV3		+	(Unused)
	 	02: PV_EV4 02: PV EV5			(Unused)
		02: PV EV6			(Unused)
		02: PV EV7	\dashv	+	(Unused)
	RX135	02: PV_EV8		RY135	(Unused)
	RX136	02: ALM1_L1		RY136	(Unused)
		02: ALM2_L1	_	+	(Unused)
		02: ALM3_L1	_		(Unused)
		02: ALM4_L1 (Unused)	\dashv \vdash	_	(Unused)
		(Unused)		+	(Unused)
		(Unused)		_	(Unused)
	RX143	(Unused)		RY143	(Unused)
		02: TIME_EV1			(Unused)
		02: TIME_EV2		-	(Unused)
		02: TIME_EV3			(Unused)
		02: TIME_EV4		+	(Unused)
	-	02: TIME_EV5 02: TIME EV6	\dashv \vdash		(Unused)
		02: TIME_EV7		 	(Unused)
		02: TIME EV8		+	(Unused)
	RX152	02: TIME_EV9			(Unused)
	RX153	02: TIME_EV10		RY153	(Unused)
		02: TIME_EV11	_	_	(Unused)
		02: TIME_EV12	_	+	(Unused)
		02: TIME_EV13 02: TIME_EV14		RY156	(Unused)
	-	02: TIME_EV14 02: TIME EV15	_	+	(Unused)
		02: TIME EV16		_	(Unused)
	RX160	(Unused)		RY160	(Unused)
		(Unused)			(Unused)
	-	(Unused)	_		(Unused)
	-	(Unused)		+	(Unused)
		(Unused)	\dashv \vdash	+	(Unused)
		(Unused)	\dashv		(Unused)
	RX167	(Unused)			(Unused)
	RX168	(Unused)		RY168	(Unused)
		(Unused)	_		(Unused)
	-	(Unused)	_	+	(Unused)
		(Unused)	+		(Unused)
	-	(Unused)		+	(Unused)
		(Unused)	\dashv	_	(Unused)
		(Unused)		+	(Unused)
	RX176	(Reserved)			(Reserved)
	:			:	
	RX187	Remote Ready		RY187	(Reserved)
	:			:	
	RX191	(Reserved)		RY191	(Reserved)
DW 0		Current name	DW 0		Daws showed to
RWr0 RWr1		Current page 01: PV L1	RWw0 RWw1		Page change request 01: H.TSP_L1
RWr2	<u> </u>	01: PV_L1 01: CSP_L1	RWw1		01: H.SP_L1
RWr3		01: SEG_RTIME	RWw3		01: H.TM_L1
RWr4		01: LSP_L1	RWw4		01: LSP_L1
RWr5		01: OUT_L2	RWw5		01: MOUT_L2
RWr6		(Unused)	RWw6		(Unused)
RWr7		(Unused)	RWw7		(Unused)
RWr8		01: C.PTNO.	RWw8		01: PTNO.

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Profile	Profile number 14 (Cascade control with 2 connected controll				age 1	(Ver.2.00, 2-station occupied x4 setting)		
		IN area		OUT area				
	CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
RWr9		01: SEG.N		RWw9		01: SST		
RWr10		(Unused)		RWw10		(Unused)		
RWr11		01: PV_L2		RWw11		(Unused)		
RWr12		01: CSP_L2		RWw12		01: LSP_L2		
RWr13		01: OUT_L2		RWw13		01: MOUT_L2		
RWr14		(Unused)		RWw14		(Unused)		
RWr15		(Unused)		RWw15		(Unused)		
RWr16		(Unused)		RWw16		(Unused)		
RWr17		02: PV_L1		RWw17		02: H.TSP_L1		
RWr18		02: CSP_L1		RWw18		02: H.SP_L1		
RWr19		02: SEG_RTIME		RWw19		02: H.TM_L1		
RWr20		02: LSP_L1		RWw20		02: LSP_L1		
RWr21		02: OUT_L2		RWw21		02: MOUT_L2		
RWr22		(Unused)		RWw22		(Unused)		
RWr23		(Unused)		RWw23		(Unused)		
RWr24		02: C.PTNO.		RWw24		02: PTNO.		
RWr25		02: SEG.N		RWw25		02: SST		
RWr26		(Unused)		RWw26		(Unused)		
RWr27		02: PV_L2		RWw27		(Unused)		
RWr28		02: CSP_L2		RWw28		02: LSP_L2		
RWr29		02: OUT_L2		RWw29		02: MOUT_L2		
RW30		(Unused)		RWw30		(Unused)		
RWr31		(Unused)		RWw31		(Unused)		

IN area				OUT area					
CC-Link slave (UTAdvanced) → CC-Link master				CC-Link master → CC-Link slave (UTAdvane					
Word osition	Bit position	Contents of assignment		ord	Bit position	Contents of assignment			
	RX0	Receive data valid			RY0	Rescan request			
	RX1	During-write			RY1	(Reserved)			
	RX2	Write acknowledgement			RY2	Write request			
	RX3	(Reserved)			RY3	(Reserved)			
	RX4	(Reserved)			RY4	(Reserved)			
	RX5	(Reserved)			RY5	(Reserved)			
	RX6	(Reserved)			RY6	(Reserved)			
	RX7	(Reserved)			RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)			
	RX48	(Unused)			RY48	(Unused)			
	:				:				
		(Unused)	┨	-	RY175	(Unused)			
		(Reserved)			RY175	(Reserved)			
	:	(ITCOCI VEU)			:	(incocived)			
	-				•	(5)			
	KX187	Remote Ready			RY187	(Reserved)			
	:				:				
	RX191	(Reserved)			RY191	(Reserved)			
		-		1		-			
RWr0		Current page	_	Vw0		Page change request			
RWr1		01: P_L1_1		Vw1		01: P_L1_1			
RWr2		01: I_L1_1	-	Vw2		01: I_L1_1			
RWr3		01: D_L1_1	┥ ├──	Vw3		01: D_L1_1			
RWr4 RWr5		01: L.PID	-	Vw4 Vw5		01: L.PID			
RWr6		01: A1_L1_1		Vw5 Vw6		01: A1_L1_1			
RWr7		01: A2_L1_1 01: A3_L1_1	1 —	Vw7		01: A2_L1_1 01: A3_L1_1			
RWr8		01: A3_L1_1 01: A4_L1_1	-	Vw7 Vw8		01: A3_L1_1 01: A4_L1_1			
RWr9		01: P_L2_1	1 —	Vw9		01: P_L2_1			
RWr10		01: I_L2_1	+ -	/w10		01: I_L2_1			
RWr11		01: D_L2_1	1 —	/w11		01: D_L2_1			
Wr12		01: A1_L2_1	-	w112		01: A1 L2 1			
RWr13		01: A2_L2_1	1 —	w12		01: A2_L2_1			
RWr14		01: A3 L2 1		w14		01: A3 L2 1			
RWr15		01: A4_L2_1		w15		01: A4_L2_1			
RWr16		(Unused)	-	w16		(Unused)			
RWr17		02: P_L1_1	1 1	w17		02: P_L1_1			
RWr18		03: I_L1_1	1 —	w18		03: I_L1_1			
RWr19	İ	02: D_L1_1	RW	w19		02: D_L1_1			
Wr20		02: L.PID	RW	w20		02: L.PID			
RWr21		02: A1_L1_1	RW	w21		02: A1_L1_1			
RWr22		02: A2_L1_1	RW	w22		02: A2_L1_1			
Wr23		02: A3_L1_1	RW	w23		02: A3_L1_1			
Wr24		02: A4_L1_1	RW	w24		02: A4_L1_1			
RWr25		02: P_L2_1	RW	w25		02: P_L2_1			
RWr26		02: I_L2_1	RW	w26		02: I_L2_1			
RWr27		02: D_L2_1	RW	w27		02: D_L2_1			
RWr28		02: A1_L2_1	RW	w28		02: A1_L2_1			
Wr29		02: A2_L2_1	RW	w29		02: A2_L2_1			
RW30		02: A3_L2_1	RW	w30		02: A3_L2_1			
RWr31		02: A4_L2_1	RW	w31		02: A4_L2_1			

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Page 3

		IN area		OUT area					
C	C-Link sl	ave (UTAdvanced) → CC-Link master	С	C-Link m	aster → CC-Link slave (UTAdvanced)				
Word osition	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment				
	RX0	Receive data valid		RY0	Rescan request				
	RX1	During-write		RY1	(Reserved)				
	RX2	Write acknowledgement		RY2	Write request				
	RX3	(Reserved)		RY3	(Reserved)				
	RX4	(Reserved)		RY4	(Reserved)				
	RX5	(Reserved)		RY5	(Reserved)				
	RX6	(Reserved)		RY6	(Reserved)				
	RX7	(Reserved)		RY7	(Reserved)				
		The fixed-part is omitted		:	The fixed-part is omitted				
		(See profile number 0 on page 1)			(See profile number 0 on page 1)				
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)				
	RX48	(Unused)	1	RY48	(Unused)				
	•	\ \ - \ - \ - \ \ \ \ \ \ \ \ \ \ \ \ \		•	()				
	:		↓	:					
		(Unused)	↓		(Unused)				
	RX176	(Reserved)		RY176	(Reserved)				
	:								
	RX187	Remote Ready		RY187	(Reserved)				
	:	,	1	:					
	• DV404	(Reserved)	-	PV404	(Reserved)				
	KAISI	((Keserved)		Kilai	(Reserved)				
RWr0		Current page	RWw0		Page change request				
RWr1		01: L.TY1	RWw1		01: L.TY1				
RWr2		01: L.EV1	RWw2		01: L.EV1				
RWr3		01: L.TY2	RWw3		01: L.TY2				
RWr4		01: L.EV2	RWw4		01: L.EV2				
RWr5		01: L.TY3	RWw5		01: L.TY3				
RWr6		01: L.EV3	RWw6		01: L.EV3				
RWr7		01: L.TY4	RWw7		01: L.TY4				
RWr8		01: L.EV4	RWw8		01: L.EV4				
RWr9			RWw9		01: L.TY5				
		01: L.TY5	1						
RWr10		01: L.EV5.	RWw10		01: L.EV5.				
RWr11		01: L.TY6	RWw11		01: L.TY6				
RWr12		01: L.EV6	RWw12		01: L.EV6				
RWr13		01: L.TY7	RWw13		01: L.TY7				
RWr14		01: L.EV7	RWw14	-	01: L.EV7				
RWr15		01: L.TY8	RWw15		01: L.TY8				
RWr16		01: L.EV8	RWw16	-	01: L.EV8				
RWr17		(Unused)	RWw17	-	(Unused)				
RWr18		(Unused)	RWw18	-	(Unused)				
RWr19		(Unused)	RWw19		(Unused)				
RWr20		(Unused)	RWw20		(Unused)				
RWr21		(Unused)	RWw21		(Unused)				
RWr22		(Unused)	RWw22	1	(Unused)				
RWr23		(Unused)	RWw23		(Unused)				
RWr24		(Unused)	RWw24		(Unused)				
RWr25		(Unused)	RWw25		(Unused)				
RWr26		(Unused)	RWw26		(Unused)				
RWr27		(Unused)	RWw27		(Unused)				
RWr28		(Unused)	RWw28		(Unused)				
RWr29		(Unused)	RWw29		(Unused)				
RW30		(Unused)	RWw30		(Unused)				
RWr31	 	(Unused)	RWw31		(Unused)				

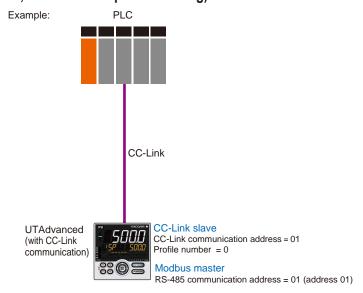
		IN area			OUT area
С	C-Link sl	ave (UTAdvanced) → CC-Link master	C	C-Link m	aster → CC-Link slave (UTAdvanced)
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)	1 -	RY48	(Unused)
		(3.13333)	1	:	
			ļ		
		(Unused)			(Unused)
	RX176	(Reserved)		RY176	(Reserved)
	:			:	
	RX187	Remote Ready		RY187	(Reserved)
	:			:	
	RX191	(Reserved)		RY191	(Reserved)
	100101	(1.1000.100)			(1.000.100)
RWr0		Current page	RWw0		Page change request
RWr1		02: L.TY1	RWw1		02: L.TY1
RWr2		02: L.EV1	RWw2		02: L.EV1
RWr3		02: L.TY2	RWw3		02: L.TY2
RWr4		02: L.EV2	RWw4		02: L.EV2
RWr5		02: L.TY3	RWw5		02: L.TY3
RWr6		02: L.EV3	RWw6		02: L.EV3
RWr7		02: L.TY4	RWw7		02: L.TY4
RWr8		02: L.EV4	RWw8		02: L.EV4
RWr9		02: L.TY5	RWw9		02: L.TY5
RWr10		02: L.EV5.	RWw10		02: L.EV5.
RWr11		02: L.TY6	RWw11		02: L.TY6
RWr12		02: L.EV6	RWw12		02: L.EV6
RWr13		02: L.TY7	RWw13		02: L.TY7
RWr14		02: L.EV7	RWw14		02: L.EV7
RWr15		02: L.TY8	RWw15		02: L.TY8
RWr16		02: L.EV8	RWw16		02: L.EV8
RWr17		(Unused)	RWw17		(Unused)
RWr18		(Unused)	RWw18		(Unused)
RWr19		(Unused)	RWw19		(Unused)
RWr20		(Unused)	RWw20		(Unused)
RWr21		(Unused)	RWw21		(Unused)
RWr22		(Unused)	RWw22		(Unused)
RWr23		(Unused)	RWw23		(Unused)
RWr24		(Unused)	RWw24		(Unused)
RWr25		(Unused)	RWw25		(Unused)
RWr26		(Unused)	RWw26		(Unused)
RWr27		(Unused)	RWw27		(Unused)
RWr28		(Unused)	RWw28		(Unused)
RWr29		(Unused)	RWw29		(Unused)
	-	(Unused)	RWw29 RWw30		(Unused)
RW30					

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Profile number 15 (Cascade control with program pattern setting for 1 connected controller) (Ver.2.00, 3-station occupied x8 setting)





Page 1

	C-Link sl	IN area ave (UTAdvanced) → CC-Link master	С	OUT area CC-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
	RX0	Receive data valid		RY0	Rescan request			
	RX1	During-write		RY1	(Unused)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	01: RST_ON		RY48	01: RST_ON			
	RX49	01: PRG_ON		RY49	01: PRG_ON			
	RX50	01: LOC_ON		RY50	01: LOC_ON			
	RX51	01: HOLD		RY51	01: HOLD			
	RX52	(Unused)		RY52	01: ADV			
	RX53	01: A.M_L2		RY53	01: A.M_L2			
	RX54	(Unused)		RY54	(Unused)			
	RX55	(Unused)		RY55	(Unused)			
	RX56	01: L.C		RY56	01: L.C			
	RX57	(Unused)		RY57	(Unused)			
	RX58	(Unused)		RY58	(Unused)			
	RX59	(Unused)		RY59	(Unused)			
	RX60	(Unused)		RY60	(Unused)			
	RX61	(Unused)		RY61	(Unused)			
	RX62	(Unused)		RY62	(Unused)			
	RX63	(Unused)		RY63	(Unused)			
	RX64	01: PV_EV1		RY64	(Unused)			
	RX65	01: PV_EV2		RY65	(Unused)			
	RX66	01: PV_EV3		RY66	(Unused)			
	RX67	01: PV_EV4		RY67	(Unused)			
	RX68	01: PV_EV5		RY68	(Unused)			
	RX69	01: PV_EV6		RY69	(Unused)			
	RX70	01: PV_EV7		RY70	(Unused)			
	RX71	01: PV_EV8		RY71	(Unused)			
	RX72	01: ALM1 L1		RY72	(Unused)			

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Profile n	umber 15 (Cascade control with program pattern setting for 1 c	onne	cted contro	ller) on pa	ge 1 (Ver.2.00, 3-station occupied x8 setting)
		IN area				OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master			C-Link m	aster → CC-Link slave (UTAdvanced)
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment
position	position RX73	01: ALM2_L1		position	Position RY73	(Unused)
		01: ALM3_L1	1		RY74	(Unused)
		01: ALM4 L1	1		RY75	(Unused)
		(Unused)	1		RY76	(Unused)
	RX77	(Unused)	1		RY77	(Unused)
	RX78	(Unused)	1		RY78	(Unused)
	_	(Unused)	1		RY79	(Unused)
		01: TIME_EV1	1		RY80	(Unused)
	1	01: TIME EV2	1		RY81	(Unused)
	RX82	01: TIME_EV3	1		RY82	(Unused)
	RX83	01: TIME_EV4			RY83	(Unused)
	RX84	01: TIME_EV5	1		RY84	(Unused)
	RX85	01: TIME_EV6	1		RY85	(Unused)
	RX86	01: TIME_EV7	1		RY86	(Unused)
	RX87	01: TIME_EV8	1		RY87	(Unused)
	RX88	01: TIME_EV9	1		RY88	(Unused)
	RX89	01: TIME_EV10			RY89	(Unused)
	RX90	01: TIME_EV11			RY90	(Unused)
	RX91	01: TIME_EV12			RY91	(Unused)
		01: TIME_EV13			RY92	(Unused)
		01: TIME_EV14			RY93	(Unused)
	RX94	01: TIME_EV15			RY94	(Unused)
	RX95	01: TIME_EV16			RY95	(Unused)
	RX96	(Unused)			RY96	(Unused)
	:				:	
	RX623	(Unused)	1		RY623	(Unused)
		(Reserved)				(Reserved)
	:		i		:	
	PY635	Remote Ready	1		PV635	(Reserved)
	10000	Nemote Ready	1		•	(ineserved)
	•	100			: D)(000	160
	RX639	(Reserved)			RY639	(Reserved)
DIA/=0		0	1	DIM		Danie shanna na masat
RWr0 RWr1		Current page	1	RWw0 RWw1		Page change request
RWr2		01: PV_L1 01: CSP_L1	1	RWw2		01: H.TSP_L1 01: H.SP_L1
RWr3		01: SEG_RTIME	┨	RWw3		01: H.TM_L1
RWr4		01: LSP_L1		RWw4		01: LSP_L1
RWr5		01: OUT_L2	1	RWw5		01: MOUT L2
RWr6		(Unused)	1	RWw6		(Unused)
RWr7		(Unused)	1	RWw7		(Unused)
RWr8		01: C.PTNO.	1	RWw8		01: PTNO.
RWr9		01: SEG.N	1	RWw9		01: SST
RWr10		(Unused)	1	RWw10		(Unused)
RWr11		01: PV L2	1	RWw11		(Unused)
RWr12		01: CSP_L2	1	RWw12		01: LSP_L2
RWr13		01: OUT_L2	1	RWw13	1	01: MOUT_L2
RWr14		(Unused)	1	RWw14		(Unused)
:			1	:		,
			-			
RWr95		(Unused)		RWw95		(Unused)

Profile n	umber 15 (Cascade control with program pattern setting for 1 con	nected contro	oller) on pa	ge 2 (Ver.2.00, 3-station occupied x8 setting)			
	IN area			OUT area				
		ave (UTAdvanced) → CC-Link master			aster → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment			
	RX0	Receive data valid		RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	(Unused)		RY48	(Unused)			
	:			:				
	RX623	(Unused)		RY623	(Unused)			
	RX624	(Reserved)		RY624	(Reserved)			
	:			:				
	RX635	Remote Ready		RY635	(Reserved)			
	:			:				
	RX639	(Reserved)		RY639	(Reserved)			

RWr0	Current page
RWr1	01: P_L1_1
RWr2	01: I_L1_1
RWr3	01: D_L1_1
RWr4	01: L.PID
RWr5	01: A1_L1_1
RWr6	01: A2_L1_1
RWr7	01: A3_L1_1
RWr8	01: A4_L1_1
RWr9	01: P_L2_1
RWr10	01: I_L2_1
RWr11	01: D_L2_1
RWr12	01: A1_L2_1
RWr13	01: A2_L2_1
RWr14	01: A3_L2_1
RWr15	01: A4_L2_1
RWr16	(Unused)
RWr17	(Unused)
RWr18	(Unused)
RWr19	(Unused)
RWr20	(Unused)
RWr21	01: L.TY1
RWr22	01: L.EV1
RWr23	01: L.TY2
RWr24	01: L.EV2
RWr25	01: L.TY3
RWr26	01: L.EV3
RWr27	01: L.TY4
RWr28	01: L.EV4
RWr29	01: L.TY5
RWr30	01: L.EV5.
RWr31	01: L.TY6
RWr32	01: L.EV6
RWr33	01: L.TY7
RWr34	01: L.EV7
RWr35	01: L.TY8
RWr36	01: L.EV8
RWr37	(Unused)
:	
RWr70	(Unused)

RWw0	Page change request
RWw1	01: P L1 1
RWw2	01: I L1 1
RWw3	01: D_L1_1
RWw4	01: L.PID
RWw5	01: A1 L1 1
RWw6	01: A2 L1 1
RWw7	01: A3 L1 1
RWw8	01: A4 L1 1
RWw9	01: P_L2_1
RWw10	01: I L2 1
RWw11	01: D L2 1
RWw12	01: A1 L2 1
RWw13	01: A2 L2 1
RWw14	01: A3_L2_1
RWw15	01: A4_L2_1
RWw16	(Unused)
RWw17	(Unused)
RWw18	(Unused)
RWw19	(Unused)
RWw20	(Unused)
RWw21	01: L.TY1
RWw22	01: L.EV1
RWw23	01: L.TY2
RWw24	01: L.EV2
RWw25	01: L.TY3
RWw26	01: L.EV3
RWw27	01: L.TY4
RWw28	01: L.EV4
RWw29	01: L.TY5
RWw30	01: L.EV5.
RWw31	01: L.TY6
RWw32	01: L.EV6
RWw33	01: L.TY7
RWw34	01: L.EV7
RWw35	01: L.TY8
RWw36	01: L.EV8
RWw37	(Unused)
:	
RWw70	(Unused)

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Profile no	umber 15 (Cascade control with program pattern setting for 1 co	onnec	cted contro	ller) on pa	ge 2 (Ver.2.00, 3-station occupied x8 setting)		
		IN area		OUT area				
C	CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
RWr71		01: CLR.P		RWw71		01: CLR.P		
RWr72		01: CLR.TRG		RWw72		01: CLR.TRG		
RWr73		01: PTN.ERR		RWw73		(Unused)		
RWr74		(Unused)		RWw74		(Unused)		
:				:				
RWr95		(Unused)]	RWw95		(Unused)		

		IN area		OUT area					
		ave (UTAdvanced) → CC-Link master			aster → CC-Link slave (UTAdvanced)				
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment				
OSILIOII	RX0	Receive data valid	position	RY0	Rescan request				
	RX1	During-write		RY1	(Reserved)				
	RX2	Write acknowledgement		RY2	Write request				
	RX3	(Reserved)		RY3	(Reserved)				
	RX4	(Reserved)		RY4	(Reserved)				
	RX5	(Reserved)		RY5	(Reserved)				
	RX6	(Reserved)	+	RY6	(Reserved)				
	RX7	(Reserved)	+	RY7	(Reserved)				
		(Reserved)	+	K17	(Keserveu)				
		The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)				
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)				
	RX48	(Unused)		RY48	(Unused)				
	•	(Chasea)		•	(Onuseu)				
	:			:					
	RX623	(Unused)		RY623	(Unused)				
	RX624	(Reserved)		RY624	(Reserved)				
	:			:					
	RX635	Remote Ready		RY635	(Reserved)				
	•	Tromoto Troday		•	(1.0001700)				
	:			:	(=				
	RX639	(Reserved)		RY639	(Reserved)				
RWr0		Current page	RWw0		Page change request				
RWr1		01: PTNOC	RWw1		01: PTNOC				
RWr2		01: SEGNOC	RWw2		01: SEGNOC				
RWr3		01: SSP_L1	RWw3		01: SSP_L1				
RWr4		(Unused)	RWw4		(Unused)				
RWr5		01: STC	RWw5		01: STC				
RWr6		01: WT.SW1	RWw6		01: WT.SW1				
RWr7		01: WZ.UP1	RWw7		01: WZ.UP1				
RWr8		01: WZ.LO1	RWw8		01: WZ.LO1				
RWr9		01: WT.TM1	RWw9		01: WT.TM1				
RWr10		01: WT.SW2	RWw10		01: WT.SW2				
RWr11			RWw10	-					
		01: WZ.UP2	+ 		01: WZ.UP2				
RWr12		01: WZ.LO2	RWw12	-	01: WZ.LO2				
RWr13		01: WT.TM2	RWw13		01: WT.TM2				
RWr14		01: WT.SW3	RWw14		01: WT.SW3				
RWr15		01: WZ.UP3	RWw15		01: WZ.UP3				
RWr16		01: WZ.LO3	RWw16		01: WZ.LO3				
RWr17		01: WT.TM3	RWw17		01: WT.TM3				
RWr18		01: WT.SW4	RWw18		01: WT.SW4				
RWr19		01: WZ.UP4	RWw19		01: WZ.UP4				
RWr20		01: WZ.LO4	RWw20		01: WZ.LO4				
RWr21		01: WT.TM4	RWw21		01: WT.TM4				
RWr22		01: WT.SW5	RWw22		01: WT.SW5				
RWr23		01: WZ.UP5	RWw23		01: WZ.UP5				
RWr24		01: WZ.LO5	RWw24		01: WZ.LO5				
RWr25		01: WT.TM5	RWw25		01: WT.TM5				
RWr26		01: R.CYCL	RWw26		01: R.CYCL				
RWr27		01: R.STRT	RWw26		01: R.STRT				
			 	-					
RWr28		01: R.END	RWw28	-	01: R.END				
RWr29		(Unused)	RWw29	-	(Unused)				
RWr30		(Unused)	RWw30		(Unused)				
RWr31		01: P.NAME	RWw31	-	01: P.NAME				
RWr32		01: P.NAME	RWw32	_	01: P.NAME				
RWr33		01: P.NAME	RWw33		01: P.NAME				
RWr34		01: P.NAME	RWw34		01: P.NAME				
RWr35		01: P.NAME	RWw35		01: P.NAME				
RWr36		01: P.NAME	RWw36		01: P.NAME				
RWr37		01: P.NAME	RWw37		01: P.NAME				
RWr38		01: P.NAME	RWw38		01: P.NAME				
RWr39		01: P.NAME	RWw39		01: P.NAME				
	I	O 1 . 1 .1 W WINE	1 11000039	1	O 1. 1 .1 W WINE				

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Profile no	Profile number 15 (Cascade control with program pattern setting for 1 connected controller) on page 3 (Ver.2.00, 3-station occupied x8 setting)						
	IN area			OUT area			
C	CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
RWr41		01: P.NAME		RWw41		01: P.NAME	
RWr42		01: PTN.ERR		RWw42		(Unused)	
RWr43		(Unused)		RWw43		(Unused)	
:				:			
RWr95		(Unused)		RWw95		(Unused)	

		IN area			IN area		
		ave (UTAdvanced) → CC-Link master		CC-Link slave (UTAdvanced) → CC-Link mast			
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment		
OSILIOII	RX0	Receive data valid	position	RY0	Rescan request		
	RX1	During-write	1	RY1	(Reserved)		
	RX2	Write acknowledgement		RY2	Write request		
	RX3	(Reserved)		RY3	(Reserved)		
	RX4	(Reserved)		RY4	(Reserved)		
	RX5	(Reserved)		RY5	(Reserved)		
	RX6	(Reserved)	-	RY6	(Reserved)		
	RX7	(Reserved)	+	RY7	(Reserved)		
		(Reserved)	+	K17	(Reserved)		
		The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)		
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)		
	RX48	(Unused)	1	RY48	(Unused)		
	•	(Ondsed)	1 -	11140	(Onuseu)		
	:			:			
	RX623	(Unused)		RY623	(Unused)		
	RX624	(Reserved)		RY624	(Reserved)		
	:			:			
	BX635	Remote Ready		BV63F	(Reserved)		
	•	Internote incauy	+	•	(Neserveu)		
	:			:			
	RX639	(Reserved)		RY639	(Reserved)		
RWr0		Current page	RWw0		Page change request		
RWr1		01: PTNOC	RWw1		01: PTNOC		
RWr2		01: SEGNOC	RWw2		01: SEGNOC		
RWr3		01: TSP L1	RWw3		01: TSP L1		
RWr4		(Unused)	RWw4		(Unused)		
RWr5		01: TIME	RWw5		01: TIME		
RWr6		01: TM.RT	RWw6		01: TM.RT		
RWr7		01: S.PID	RWw7		01: S.PID		
RWr8		01: JC	RWw8		01: JC		
RWr9		01: PV.TY1	RWw9		01: PV.TY1		
RWr10		01: PV.EV1	RWw10		01: PV.EV1		
RWr11		01: PV.TY2	RWw11		01: PV.TY2		
RWr12		01: PV.EV2	RWw12		01: PV.EV2		
RWr13		01: PV.TY3	RWw13		01: PV.TY3		
RWr14		01: PV.EV3	RWw14		01: PV.EV3		
RWr15		01: PV.TY4	RWw15		01: PV.TY4		
RWr16		01: PV.EV4	RWw16		01: PV.EV4		
RWr17		01: PV.TY5	RWw17		01: PV.TY5		
RWr18		01: PV.EV5	RWw18		01: PV.EV5		
RWr19		01: PV.TY6	RWw19		01: PV.TY6		
RWr20		01: PV.EV6	RWw20		01: PV.EV6		
RWr21		01: PV.TY7	RWw21		01: PV.TY7		
RWr22		01: PV.EV7	RWw22		01: PV.EV7		
RWr23		01: PV.TY8	RWw23	-	01: PV.TY8		
RWr24		01: PV.EV8	RWw24		01: PV.EV8		
RWr25		01: F V.EV8	RWw25		01: TME1		
			RWw25		01: T.ON1		
RWr26		01: T.ON1		-			
RWr27		01: T.OF1	RWw27		01: T.OF1		
RWr28		01: TME2	RWw28	-	01: TME2		
RWr29		01: T.ON2	RWw29	-	01: T.ON2		
RWr30		01: T.OF2	RWw30		01: T.OF2		
RWr31		01: TME3	RWw31		01: TME3		
RWr32		01: T.ON3	RWw32	_	01: T.ON3		
RWr33		01: T.OF3	RWw33		01: T.OF3		
RWr34		01: TME4	RWw34		01: TME4		
RWr35		01: T.ON4	RWw35		01: T.ON4		
RWr36		01: T.OF4	RWw36		01: T.OF4		
RWr37		01: TME5	RWw37		01: TME5		
RWr38		01: T.ON5	RWw38		01: T.ON5		
RWr39		01: T.OF5	RWw39		01: T.OF5		
	1	Jo 1. 1. 0 1 0	1 11000039		01. 1.01 0		

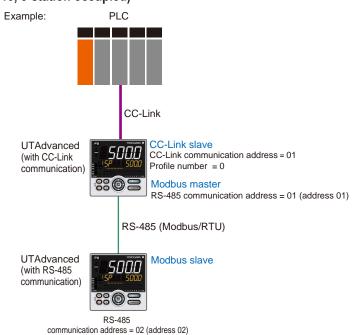
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IN area CC-Link slave (UTAdvanced) → CC-Link master				IN area CC-Link slave (UTAdvanced) → CC-Link master			
Word	Bit	,	Word	Bit	Rit		
osition	position	Contents of assignment	position	position	Contents of assignment		
RWr41		01: T.ON6	RWw41		01: T.ON6		
RWr42		01: T.OF6	RWw42		01: T.OF6		
RWr43		01: TME7	RWw43		01: TME7		
RWr44		01: T.ON7	RWw44		01: T.ON7		
RWr45		01: T.OF7	RWw45		01: T.OF7		
RWr46		01: TME8	RWw46		01: TME8		
RWr47		01: T.ON8	RWw47		01: T.ON8		
RWr48		01: T.OF8	RWw48		01: T.OF8		
RWr49		01: TME9	RWw49		01: TME9		
RWr50		01: T.ON9	RWw50		01: T.ON9		
RWr51		01: T.OF9	RWw51		01: T.OF9		
RWr52		01: TME10	RWw52		01: TME10		
RWr53		01: T.ON10	RWw53		01: T.ON10		
RWr54		01: T.OF10	RWw54		01: T.OF10		
RWr55		01: TME11	RWw55		01: TME11		
RWr56		01: T.ON11	RWw56		01: T.ON11		
RWr57		01: T.OF11	RWw57		01: T.OF11		
RWr58		01: TME12	RWw58		01: TME12		
RWr59		01: T.ON12	RWw59		01: T.ON12		
RWr60		01: T.OF12	RWw60		01: T.OF12		
RWr61		01: TME13	RWw61		01: TME13		
RWr62		01: T.ON13	RWw62		01: T.ON13		
RWr63		01: T.OF13	RWw63		01: T.OF13		
RWr64		01: TME14	RWw64		01: TME14		
RWr65		01: T.ON14	RWw65		01: T.ON14		
RWr66		01: T.OF14	RWw66		01: T.OF14		
RWr67		01: TME15	RWw67		01: TME15		
RWr68		01: T.ON15	RWw68		01: T.ON15		
RWr69		01: T.OF15	RWw69		01: T.OF15		
RWr70		01: TME16	RWw70		01: TME16		
RWr71		01: T.ON16	RWw71		01: T.ON16		
RWr72		01: T.OF16	RWw72		01: T.OF16		
RWr73		01: PTN.ERR	RWw73		(Unused)		
RWr74		(Unused)	RWw74		(Unused)		
:					,		
RWr95		(Unused)	RWw95		(Unused)		

4.9.3 Profile List for UT75A

Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) (Ver.1.10, 3-station occupied)





Page 1

		IN area			OUT area
CC-Link slave (UTAdvanced) → CC-Link master					naster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	RX8	(Reserved)		RY8	(Reserved)
	RX9	(Reserved)		RY9	(Reserved)
	RX10	(Reserved)		RY10	(Reserved)
	RX11	(Reserved)		RY11	(Reserved)
	RX12	(Reserved)		RY12	(Reserved)
	RX13	(Reserved)		RY13	(Reserved)
	RX14	(Reserved)		RY14	(Reserved)
	RX15	(Reserved)		RY15	(Reserved)
	RX16	Normal connection slave (address 01)		RY16	Batch write request (address 01)
	RX17	Normal connection slave (address 02)		RY17	Batch write request (address 02)
	RX18	Normal connection slave (address 03)		RY18	Batch write request (address 03)
	RX19	Normal connection slave (address 04)		RY19	Batch write request (address 04)
	RX20	Normal connection slave (address 05)		RY20	Batch write request (address 05)
	RX21	Normal connection slave (address 06)		RY21	Batch write request (address 06)
	RX22	Normal connection slave (address 07)		RY22	Batch write request (address 07)
	RX23	Normal connection slave (address 08)		RY23	Batch write request (address 08)
	RX24	Normal connection slave (address 09)		RY24	Batch write request (address 09)
	RX25	Normal connection slave (address 10)		RY25	Batch write request (address 10)
	RX26	Normal connection slave (address 11)		RY26	Batch write request (address 11)
	RX27	Normal connection slave (address 12)		RY27	Batch write request (address 12)
	RX28	Normal connection slave (address 13)		RY28	Batch write request (address 13)
	RX29	Normal connection slave (address 14)		RY29	Batch write request (address 14)
		Normal connection slave (address 15)		RY30	Batch write request (address 15)
	RX31	Normal connection slave (address 16)		RY31	Batch write request (address 16)
	RX32	Normal connection slave (address 17)		RY32	Batch write request (address 17)

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Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1 (Ver.1.10, 3-station occupied) IN area CC-Link slave (UTAdvanced) → CC-Link master Word Bit Contents of assignment position position RX33 Normal connection slave (address 18) RX34 Normal connection slave (address 19) RX35 Normal connection slave (address 20) RX36 Normal connection slave (address 21) RX37 Normal connection slave (address 22) RX38 Normal connection slave (address 23) RX39 Normal connection slave (address 24) RX40 Normal connection slave (address 25) RX41 Normal connection slave (address 26) RX42 Normal connection slave (address 27) RX43 Normal connection slave (address 28) RX44 Normal connection slave (address 29) RX45 Normal connection slave (address 30) RX46 Normal connection slave (address 31) RX47 Normal connection slave (address 32) RX48 01: A.M_L1 RX49 01: R.L_L1 RX50 01: S.R_L1 RX51 (Unused) RX52 (Unused) RX53 (Unused) RX54 (Unused) RX55 (Unused) RX56 01: ALM1_L1 RX57 01: ALM2_L1 RX58 01: ALM3_L1 RX59 01: ALM4_L1 RX60 01: ALM5_L1 RX61 01: ALM6_L1 RX62 01: ALM7_L1 RX63 01: ALM8_L1 RX64 02: A.M_L1 RX65 02: R.L_L1 RX66 02: S.R_L1 RX67 (Unused) RX68 (Unused) RX69 (Unused) RX70 (Unused) RX71 (Unused) RX72 02: ALM1_L1 RX73 02: ALM2_L1 RX74 02: ALM3_L1 RX75 02: ALM4_L1 RX76 02: ALM5_L1 RX77 02: ALM6_L1 RX78 02: ALM7_L1

RWr0	Current page
RWr1	01: PV_L1
RWr2	02: PV_L1
RWr3	01: CSP_L1
RWr4	02: CSP_L1
RWr5	01: OUT_L1
RWr6	02: OUT_L1
RWr7	(Unused)
RWr8	(Unused)
RWr9	(Unused)

RX79 02: ALM8 L1 RX80 (Reserved)

RX95 (Reserved)

RX91 :

Remote Ready

_	COMMICCIO	u control	OUT area
	C	C-I ink m	aster → CC-Link slave (UTAdvanced)
ŀ	Word	Bit	
	position	position	Contents of assignment
		RY33	Batch write request (address 18)
		RY34	Batch write request (address 19)
		RY35	Batch write request (address 20)
		RY36	Batch write request (address 21)
ļ		RY37	Batch write request (address 22)
ļ		RY38	Batch write request (address 23)
		RY39	Batch write request (address 24)
		RY40	Batch write request (address 25)
ļ		RY41	Batch write request (address 26)
		RY42	Batch write request (address 27)
		RY43	Batch write request (address 28)
ĺ		RY44	Batch write request (address 29)
ĺ		RY45	Batch write request (address 30)
Ì		RY46	Batch write request (address 31)
Ì		RY47	Batch write request (address 32)
		RY48	01: A.M_L1
İ		RY49	01: R.L_L1
		RY50	01: S.R L1
ł		RY51	(Unused)
Ì		RY52	(Unused)
l		RY53	(Unused)
Ì		RY54	(Unused)
ł		RY55	(Unused)
l		RY56	(Unused)
l		RY57	(Unused)
l		RY58	(Unused)
ł		RY59	(Unused)
l		RY60	(Unused)
ł		RY61	(Unused)
ł		RY62	(Unused)
ł		RY63	(Unused)
ł		RY64	02: A.M_L1
l		RY65	02: R.L_L1
l		RY66	02: S.R_L1
l		RY67	(Unused)
l		RY68	(Unused)
		RY69	(Unused)
İ		RY70	(Unused)
		RY71	(Unused)
		RY72	(Unused)
		RY73	(Unused)
		RY74	(Unused)
		RY75	(Unused)
ł		RY76	(Unused)
		RY77	(Unused)
		RY78	(Unused)
		RY79	(Unused)
		RY80	(Reserved)
			(110001VBU)
		: :	(5)
		RY91	(Reserved)
		RY95	(Reserved)
•			

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	01: SP_L1_1
RWw4	02: SP_L1_1
RWw5	01: MOUT_L1
RWw6	02: MOUT_L1
RWw7	(Unused)
RWw8	(Unused)
RWw9	(Unused)

Profile r	Profile number 0 (User profile [initial value: simple PID control with 2 connected controllers]) on page 1 (Ver.1.10, 3-station occupied						
	IN area			OUT area			
С	CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)			k slave (UTAdvanced)
Word position	Bit position	Contents of assignment		Word position	Bit position	Conte	ents of assignment
RWr10		(Unused)		RWw10		(Unused)	
RWr11		(Unused)		RWw11		(Unused)	

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Profile r	number 0	(User profile [initial value: simple PID control w	ith 2 connecte	d control	lers]) on page 2 (Ver.1.10, 3-station occupied		
		IN area			OUT area		
		ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
	RX0	Receive data valid		RY0	Rescan request		
	RX1	During-write		RY1	(Reserved)		
	RX2	Write acknowledgement		RY2	Write request		
	RX3	(Reserved)		RY3	(Reserved)		
	RX4	(Reserved)		RY4	(Reserved)		
	RX5	(Reserved)		RY5	(Reserved)		
	RX6	(Reserved)		RY6	(Reserved)		
	RX7	(Reserved)		RY7	(Reserved)		
	•	The fixed-part is omitted		•	The fixed-part is omitted		
	•	(See profile number 0 on page 1)		•	(See profile number 0 on page 1)		
	٠	, ,		•			
		Normal connection slave (address 32)		RY47	Batch write request (address 32)		
	RX48	(Unused)			(Unused)		
	:			:			
	RX79	(Unused)			(Unused)		
	RX80	(Reserved)		RY80	(Reserved)		
	:			:			
	RX91	Remote Ready		RY91	(Reserved)		
	:			:			
	RX95	(Reserved)		RY95	(Reserved)		
RWr0	Ì	Current no co	RWw0		Dome change request		
RWr1		Current page 01: P L1 1	RWw1		Page change request 01: P L1 1		
RWr2		02: P_L1_1	RWw2		02: P_L1_1		
RWr3		02. F_L1_1 01: I L1 1	RWw3		01: I L1 1		
RWr4		02: I_L1_1	RWw4		02: I_L1_1		
RWr5		01: D L1 1	RWw5		01: D_L1_1		
RWr6		02: D L1 1	RWw5		02: D L1 1		
RWr7		02: D_L1_1 01: SPNO.	RWw7		01: SPNO.		
RWr8		02: SPNO.	RWw8		02: SPNO.		
RWr9		(Unused)	RWw9		(Unused)		
RWr10	-	(Unused)	RWw10		(Unused)		
RWr11		(Unused)	RWw10		(Unused)		

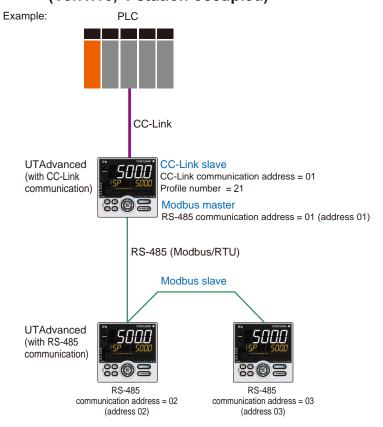
C	C-I ink sl	IN area ave (UTAdvanced) → CC-Link master	C	C-I ink m	OUT area aster → CC-Link slave (UTAdvanced)
Word Bit		Contents of assignment	Word	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)			(Unused)
	:			:	
	RX79	(Unused)			(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	:			:	
	RX91	Remote Ready		RY91	(Reserved)
	:			:	
	RX95	(Reserved)		RY95	(Reserved)
DIM 0			DW 0		
RWr0		Current page	RWw0		Page change request
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1
RWr3		01: lc_L1_1	RWw3		01: lc_L1_1
RWr4		02: lc_L1_1	RWw4		02: lc_L1_1
RWr5		01: Dc_L1_1	RWw5		01: Dc_L1_1
RWr6		02: Dc_L1_1	RWw6		02: Dc_L1_1
RWr7		01: SPNO.	RWw7		01: SPNO.
RWr8		02: SPNO.	RWw8		02: SPNO.
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

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Profile r	number 0	(User profile [initial value: simple PID control w	ith 2 connecte	d control	lers]) on page 4 (Ver.1.10, 3-station occupie
		IN area			OUT area
		ave (UTAdvanced) → CC-Link master			aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)			(Unused)
	:			:	
	RX79	(Unused)			(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	:			:	
	RX91	Remote Ready		RY91	(Reserved)
	:			:	
	RX95	(Reserved)		RY95	(Reserved)
D)//-0		C	RWw0		Danie sharing areas
RWr0		Current page			Page change request
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1
RWr2		02: A1_L1_1	RWw2		02: A1_L1_1
RWr3 RWr4		01: A2_L1_1	RWw3		01: A2_L1_1
		02: A2_L1_1	RWw4		02: A2_L1_1
RWr5		01: A3_L1_1	RWw5		01: A3_L1_1
RWr6		02: A3_L1_1	RWw6		02: A3_L1_1
RWr7		01: A4_L1_1	RWw7		01: A4_L1_1
RWr8		02: A4_L1_1	RWw8		02: A4_L1_1
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

Profile number 21 (Simple PID control with 3 connected controllers) (Ver.1.10, 4-station occupied)





Page 1

Pro	file num	ber 21 (Simple Pl	D control with 3 connected	d controller	s) on pa	ge 1 (Ver.1.10, 4-station occupied		
		IN area			OUT area			
	CC-Link slave (UTAdvanced) → CC-Link master					aster → CC-Link slave (UTAdvanced)		
Word osition	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
	RX0	Receive data valid			RY0	Rescan request		
	RX1	During-write			RY1	(Reserved)		
	RX2	Write acknowledg	ement		RY2	Write request		
	RX3	(Reserved)			RY3	(Reserved)		
	RX4	(Reserved)			RY4	(Reserved)		
	RX5	(Reserved)			RY5	(Reserved)		
	RX6	(Reserved)			RY6	(Reserved)		
	RX7	(Reserved)			RY7	(Reserved)		
	•	The five	d-part is omitted		•	The fixed-part is omitted		
	•		number 0 on page 1)		•	(See profile number 0 on page 1)		
	•	(See profile	number of on page 1)		•	(See profile flumber of our page 1)		
	RX47	Normal connection	n slave (address 32)		RY47	Batch write request (address 32)		
	RX48	01: A.M_L1			RY48	01: A.M_L1		
	RX49	01: R.L_L1			RY49	01: R.L_L1		
	RX50	01: S.R_L1			RY50	01: S.R_L1		
	RX51	(Unused)			RY51	(Unused)		
	RX52	(Unused)			RY52	(Unused)		
	RX53	(Unused)			RY53	(Unused)		
	RX54	(Unused)			RY54	(Unused)		
	RX55	(Unused)			RY55	(Unused)		
	RX56	01: ALM1_L1			RY56	(Unused)		
	RX57	01: ALM2_L1			RY57	(Unused)		
	RX58	01: ALM3_L1			RY58	(Unused)		
	RX59	01: ALM4_L1			RY59	(Unused)		
	RX60	01: ALM5_L1			RY60	(Unused)		
	RX61	01: ALM6_L1			RY61	(Unused)		
	RX62	01: ALM7_L1			RY62	(Unused)		
	RX63	01: ALM8_L1			RY63	(Unused)		

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(Ver.1.10, 4-station occupied)

IN area CC-Link slave (UTAdvanced) $\rightarrow CC$ -Link master Word Bit Contents of assignment position position RX64 02: A.M_L1 RX65 02: R.L_L1 RX66 02: S.R_L1 RX67 (Unused) RX68 (Unused) RX69 (Unused) RX70 (Unused) RX71 (Unused) RX72 02: ALM1_L1 RX73 02: ALM2_L1 RX74 02: ALM3_L1 RX75 02: ALM4_L1 RX76 02: ALM5_L1 RX77 02: ALM6_L1 RX78 02: ALM7_L1 RX79 02: ALM8_L1 RX80 03: A.M_L1 RX81 03: R.L_L1 RX82 03: S.R_L1 RX83 (Unused) RX84 (Unused) RX85 (Unused) RX86 (Unused) RX87 (Unused) RX88 03: ALM1_L1 RX89 03: ALM2_L1 RX90 03: ALM3 L1 RX91 03: ALM4_L1 RX92 03: ALM5_L1 RX93 03: ALM6_L1 RX94 03: ALM7_L1 RX95 03: ALM8_L1 RX96 (Unused) RX111 (Unused) RX112 (Reserved) : RX123 Remote Ready : RX127 (Reserved)

Profile number 21 (Simple PID control with 3 connected controllers) on page 1

Ontroller	3) on pa	OUT area				
	CC-Link master → CC-Link slave (UTAdvanced)					
Word	Bit					
	position	Contents of assignment				
	RY64	02: A.M_L1				
	RY65	02: R.L_L1				
	RY66	02: S.R_L1				
	RY67	(Unused)				
	RY68	(Unused)				
	RY69	(Unused)				
	RY70	(Unused)				
	RY71	(Unused)				
	RY72	(Unused)				
	RY73	(Unused)				
	RY74	(Unused)				
	RY75	(Unused)				
	RY76	(Unused)				
	RY77	(Unused)				
	RY78	(Unused)				
	RY79	(Unused)				
	RY80	03: A.M_L1				
	RY81	03: R.L_L1				
	RY82	03: S.R_L1				
	RY83	(Unused)				
	RY84	(Unused)				
	RY85	(Unused)				
	RY86	(Unused)				
	RY87	(Unused)				
	RY88	(Unused)				
	RY89	(Unused)				
	RY90	(Unused)				
	RY91	(Unused)				
	RY92	(Unused)				
	RY93	(Unused)				
	R94	(Unused)				
	RY95	(Unused)				
	RY96	(Unused)				
	:					
	RY111	(Unused)				
	RY112	(Reserved)				
	:					
	RY123	(Reserved)				
	:					
	RY127	(Reserved)				

RWr0	Current page
RWr1	01: PV_L1
RWr2	02: PV_L1
RWr3	03: PV_L1
RWr4	01: CSP_L1
RWr5	02: CSP_L1
RWr6	03: CSP_L1
RWr7	01: OUT_L1
RWr8	02: OUT_L1
RWr9	03: OUT_L1
RWr10	01: H.OUT_L1
RWr11	02: H.OUT_L1
RWr12	03: H.OUT_L1
RWr13	01: C.OUT_L1
RWr14	02: C.OUT_L1
RWr15	03: C.OUT_L1

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	01: SP_L1_1
RWw5	02: SP_L1_1
RWw6	03: SP_L1_1
RWw7	01: MOUT_L1
RWw8	02: MOUT_L1
RWw9	03: MOUT_L1
RWw10	01: MOUT_L1
RWw11	02: MOUT_L1
RWw12	03: MOUT_L1
RWw13	01: MOUTc_L1
RWw14	02: MOUTc_L1
RWw15	03: MOUTc_L1

IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
	RX0	Receive data valid			RY0	Rescan request	
	RX1	During-write	ī i		RY1	(Reserved)	
	RX2	Write acknowledgement	Ī		RY2	Write request	
	RX3	(Reserved)			RY3	(Reserved)	
	RX4	(Reserved)			RY4	(Reserved)	
	RX5	(Reserved)	1 i		RY5	(Reserved)	
	RX6	(Reserved)	1 1		RY6	(Reserved)	
	RX7	(Reserved)			RY7	(Reserved)	
	:	The fixed-part is omitted			:	The fixed-part is omitted	
		(See profile number 0 on page 1)				(See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)	
	RX48	(Unused)	1 1		RY48	(Unused)	
	:				:	,	
	RX111	(Unused)	1		RY111	(Unused)	
	RX112	(Reserved)			RY112	(Reserved)	
	:		i i		:		
	RX123	Remote Ready	1			(Reserved)	
	:		1		:	(**************************************	
	PV127	(Reserved)	-		• DV127	(Reserved)	
	RA121	(Keserveu)			KIIZI	(Keserved)	
RWr0		Current page		RWw0		Page change request	
RWr1		01: P_L1_1		RWw1		01: P_L1_1	
RWr2		02: P_L1_1		RWw2		02: P_L1_1	
RWr3		03: P_L1_1		RWw3		03: P_L1_1	
RWr4		01: I_L1_1]	RWw4		01: I_L1_1	
RWr5		02: I_L1_1		RWw5		02: I_L1_1	
RWr6		03: I_L1_1		RWw6		03: I_L1_1	
RWr7		01: D_L1_1]	RWw7		01: D_L1_1	
RWr8		02: D_L1_1]	RWw8		02: D_L1_1	
RWr9		03: D_L1_1	J	RWw9		03: D_L1_1	
RWr10		01: SPNO.	J l	RWw10		01: SPNO.	
RWr11		02: SPNO.		RWw11		02: SPNO.	
RWr12		03: SPNO.] [RWw12		03: SPNO.	
RWr13		(Unused)]	RWw13		(Unused)	
RWr14		(Unused)]	RWw14		(Unused)	
RWr15		(Unused)	7	RWw15		(Unused)	

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IN area CC-Link slave (UTAdvanced) → CC-Link master				OUT area CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
	RX0	Receive data valid		RY0	Rescan request	
	RX1	During-write		RY1	(Reserved)	
	RX2	Write acknowledgement		RY2	Write request	
	RX3	(Reserved)		RY3	(Reserved)	
	RX4	(Reserved)		RY4	(Reserved)	
	RX5	(Reserved)		RY5	(Reserved)	
	RX6	(Reserved)		RY6	(Reserved)	
	RX7	(Reserved)		RY7	(Reserved)	
	•	The fixed-part is omitted		•	The fixed-part is omitted	
	•	(See profile number 0 on page 1)			(See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)	
	RX48	(Unused)		RY48	(Unused)	
	:			÷		
	RX111	(Unused)		RY111	(Unused)	
	RX112	(Reserved)		RY112	(Reserved)	
	:			:		
	RX123	Remote Ready		RY123	(Reserved)	
	:			:		
	RX127	(Reserved)		RY127	(Reserved)	
RWr0		Current page	RWw0		Page change request	
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1	
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1	
RWr3		03: Pc_L1_1	RWw3		03: Pc_L1_1	
RWr4		01: lc_L1_1	RWw4		01: lc_L1_1	
RWr5		02: lc_L1_1	RWw5		02: lc_L1_1	
RWr6		03: lc_L1_1	RWw6		03: lc_L1_1	
RWr7		01: Dc_L1_1	RWw7		01: Dc_L1_1	
RWr8		02: Dc_L1_1	RWw8		02: Dc_L1_1	
RWr9		03: Dc_L1_1	RWw9		03: Dc_L1_1	
RWr10		01: SPNO.	RWw10		01: SPNO.	
RWr11		02: SPNO.	RWw11		02: SPNO.	
RWr12		03: SPNO.	RWw12		03: SPNO.	
RWr13		(Unused)	RWw13		(Unused)	
RWr14		(Unused)	RWw14		(Unused)	
RWr15		(Unused)	RWw15		(Unused)	

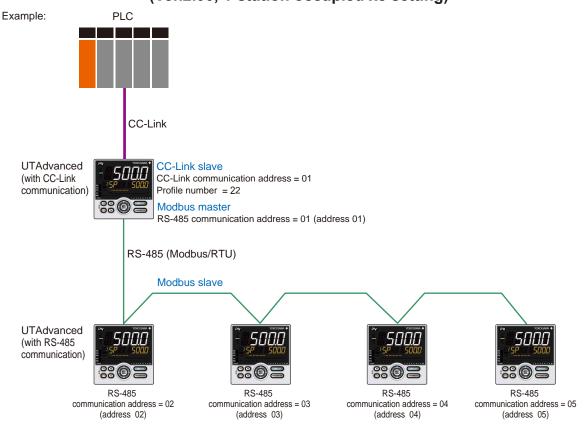
Pro	file num	ber 21 (Simple PID control with 3 connect	ed controller	s) on pa	*		
IN area				OUT area			
	CC-Link slave (UTAdvanced) → CC-Link master				aster → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment		
	RX0	Receive data valid		RY0	Rescan request		
	RX1	During-write		RY1	(Reserved)		
	RX2	Write acknowledgement		RY2	Write request		
	RX3	(Reserved)		RY3	(Reserved)		
	RX4	(Reserved)		RY4	(Reserved)		
	RX5	(Reserved)		RY5	(Reserved)		
	RX6	(Reserved)		RY6	(Reserved)		
	RX7	(Reserved)		RY7	(Reserved)		
	•	The fixed-part is omitted		•	The fixed-part is omitted		
	•	(See profile number 0 on page 1)		•	(See profile number 0 on page 1)		
	•	(occ prome number of on page 1)		•	(Occ prome number of on page 1)		
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)		
	RX48	(Unused)		RY48	(Unused)		
	:			:			
	RX111	(Unused)		RY111	(Unused)		
	RX112	(Reserved)		RY112	(Reserved)		
	:			:			
	RX123	Remote Ready		RY123	(Reserved)		
	:			:			
	RX127	(Reserved)		RY127	(Reserved)		
RWr0		Current page	RWw0		Page change request		
RWr1		01: A1_L1_1	RWw1		01: A1_L1_1		
RWr2		02: A1_L1_1	RWw2		01: A1_L1_1		
RWr3		03: A1_L1_1	RWw3		03: A1_L1_1		
RWr4		01: A2_L1_1	RWw4		01: A2_L1_1		
RWr5		02: A2_L1_1	RWw5		02: A2_L1_1		
RWr6		03: A2_L1_1	RWw6		03: A2_L1_1		
RWr7		01: A3_L1_1	RWw7		01: A3_L1_1		
RWr8		02: A3_L1_1	RWw8		02: A3_L1_1		
RWr9		03: A3_L1_1	RWw9		03: A3_L1_1		
RWr10		01: A4_L1_1	RWw10		01: A4_L1_1		
RWr11		02: A4_L1_1	RWw11		02: A4_L1_1		
RWr12		03: A4_L1_1	RWw12		03: A4_L1_1		
RWr13		01: A5_L1_1	RWw13		01: A5_L1_1		
RWr14		02: A5_L1_1	RWw14		02: A5_L1_1		
RWr15		03: A5_L1_1	RWw15		03: A5_L1_1		

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Profile number 22 (Simple PID control with 5 connected controllers) (Ver.2.00, 1-station occupied x8 setting)





Page 1

1 TOTAL	number 2	22 (Simple PID control with 5 connected con	ii Oll	oraj on p	uge i	(Ver.2.00, 1-station occupied x8 setting)
•				001 4.04		
CC-Link slave (UTAdvanced) → CC-Link master Word Bit			CC-Link master → CC-Link slave (UTAdvanced) Word Bit			
	position	Contents of assignment			Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
		The fixed-part is omitted				The fixed-part is omitted
	•	(See profile number 0 on page 1)			•	(See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	01: A.M_L1			RY48	01: A.M_L1
	RX49	01: R.L_L1			RY49	01: R.L_L1
	RX50	01: S.R_L1			RY50	01: S.R_L1
	RX51	01: ALM1_L1			RY51	(Unused)
	RX52	01: ALM2_L1			RY52	(Unused)
	RX53	01: ALM3_L1			RY53	(Unused)
	RX54	01: ALM4_L1			RY54	(Unused)
	RX55	01: ALM5_L1			RY55	(Unused)
	RX56	01: ALM6_L1			RY56	(Unused)
	RX57	01: ALM7_L1			RY57	(Unused)
	RX58	01: ALM8_L1			RY58	(Unused)
	RX59	02: A.M_L1			RY59	02: A.M_L1
	RX60	02: R.L_L1			RY60	02: R.L_L1
	RX61	02: S.R_L1			RY61	02: S.R_L1
	RX62	02: ALM1_L1			RY62	(Unused)
	RX63	02: ALM2_L1			RY63	(Unused)

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C	IN area CC-Link slave (UTAdvanced) → CC-Link master						
Word	Bit	· · · · · ·					
position	position	Contents of assignment					
	RX64	02: ALM3_L1					
	RX65	02: ALM4_L1					
	RX66	02: ALM5_L1					
	RX67	02: ALM6_L1					
	RX68	02: ALM7_L1					
	RX69	02: ALM8_L1					
	RX70	03: A.M_L1					
	RX71	03: R.L_L1					
	RX72	03: S.R_L1					
	RX73	03: ALM1_L1					
	RX74	03: ALM2_L1					
	RX75	03: ALM3_L1					
	RX76	03: ALM4_L1					
	RX77	03: ALM5_L1					
	RX78	03: ALM6_L1					
	RX79	03: ALM7_L1					
	RX80	03: ALM8_L1					
	RX81	04: A.M_L1					
	RX82	04: R.L_L1					
	RX83	04: S.R_L1					
	RX84	04: ALM1_L1					
	RX85	04: ALM2_L1					
	RX86	04: ALM3_L1					
	RX87	04: ALM4_L1					
	RX88	04: ALM5_L1					
	RX89	04: ALM6_L1					
	RX90	04: ALM7_L1					
	RX91	04: ALM8_L1					
	RX92	05: A.M_L1					
	RX93	05: R.L_L1					
	RX94	05: S.R_L1					
	RX95	05: ALM1_L1					
	RX96	05: ALM2_L1					
	RX97	05: ALM3_L1					
	RX98	05: ALM4_L1					
	RX99 RX100	05: ALM5_L1 05: ALM6_L1					
		· · · · · ·					
	RX101 RX102	05: ALM7_L1 05: ALM8 L1					
		(Unused)					
		(Unused)					
		(Unused)					
		(Unused)					
	RX100	(Unused)					
		(Unused)					
	RX100	(Unused)					
	RX109	(Unused)					
	RX110	(Unused)					
	RX1112	(Reserved)					
	•	(I/G3GIVGU)					
	:						
	RX123	Remote Ready					
	:						
	RX127	(Reserved)					

Profile number 22 (Simple PID control with 5 connected controllers) on page 1 (Ver.2.00, 1-station occupied x8 setting)

lers) on p	age 1	(Ver.2.00, 1-station occupied x8 setting)
		OUT area
		aster → CC-Link slave (UTAdvanced)
Word	Bit	Contents of assignment
position	Position RY64	(Unused)
	RY65	(Unused)
	RY66	(Unused)
	RY67	(Unused)
	RY68	(Unused)
	RY69	(Unused)
	RY70	03: A.M_L1
	RY71	03: R.L_L1
	RY72	03: S.R_L1
	RY73	(Unused)
	RY74	(Unused)
	RY75	(Unused)
	RY76	(Unused)
	RY77	(Unused)
	RY78	(Unused)
	RY79	(Unused)
	RY80	(Unused)
	RY81	04: A.M_L1
	RY82	04: R.L_L1
	RY83	04: S.R_L1
	RY84	(Unused)
	RY85	(Unused)
	RY86	(Unused)
	RY87	(Unused)
	RY88	(Unused)
	RY89	(Unused)
	RY90	(Unused)
	RY91	(Unused)
	RY92	05: A.M_L1
	RY93	05: R.L_L1
	RY94	05: S.R_L1
	RY95	(Unused)
	RY96	(Unused)
	RY97	(Unused)
	RY98	(Unused)
	RY99	(Unused)
	RY100	(Unused)
	RY101	(Unused)
	RY102	(Unused)
	RY103	(Unused)
	RY104	(Unused)
	RY105	(Unused)
	RY106	(Unused)
	RY107	(Unused)
	RY108	(Unused)
	RY109	(Unused)
	RY110	(Unused)
	RY111	(Unused)
	RY112	(Reserved)
	:	
	RY123	(Reserved)
	•	(Incodived)
	:	(0)
	RY127	(Reserved)

RWr0	Current page
RWr1	01: PV_L1
RWr2	02: PV_L1
RWr3	03: PV_L1
RWr4	04: PV_L1
RWr5	05: PV_L1
RWr6	01: CSP_L1
RWr7	02: CSP_L1
RWr8	03: CSP_L1

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	(Unused)
RWw5	(Unused)
RWw6	01: SP_L1_1
RWw7	02: SP_L1_1
RWw8	03: SP_L1_1

Profile	Profile number 22 (Simple PID control with 5 connected control		troll	ers) on p	age 1	(Ver.2.00, 1-station occupied x8 setting)		
		IN area			OUT area			
	CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
RWr9		04: CSP_L1		RWw9		04: SP_L1_1		
RWr10		05: CSP_L1		RWw10		05: SP_L1_1		
RWr11		01: OUT_L1		RWw11		01: MOUT_L1		
RWr12		02: OUT_L1		RWw12		02: MOUT_L1		
RWr13		03: OUT_L1		RWw13		03: MOUT_L1		
RWr14		04: OUT_L1		RWw14		04: MOUT_L1		
RWr15		05: OUT_L1		RWw15		05: MOUT_L1		
RWr16		01: H.OUT_L1		RWw16		01: MOUT_L1		
RWr17		02: H.OUT_L1		RWw17		02: MOUT_L1		
RWr18		03: H.OUT_L1		RWw18		03: MOUT_L1		
RWr19		04: H.OUT_L1		RWw19		04: MOUT_L1		
RWr20		05: H.OUT_L1		RWw20		05: MOUT_L1		
RWr21		01: C.OUT_L1		RWw21		01: MOUTc_L1		
RWr22		02: C.OUT_L1		RWw22		02: MOUTc_L1		
RWr23		03: C.OUT_L1		RWw23		03: MOUTc_L1		
RWr24		04: C.OUT_L1		RWw24		04: MOUTc_L1		
RWr25		05: C.OUT_L1		RWw25		05: MOUTc_L1		
RWr26		(Unused)		RWw26		(Unused)		
RWr27		(Unused)		RWw27		(Unused)		
RWr28		(Unused)		RWw28		(Unused)		
RWr29		(Unused)		RWw29		(Unused)		
RW30		(Unused)		RWw30		(Unused)		
RWr31		(Unused)		RWw31		(Unused)		

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RWr29

RW30

RWr31

(Unused)

(Unused)

(Unused)

Profile number 22 (Simple PID control with 5 connected control IN area		,	J -	(Ver.2.00, 1-station occupied x8 sett		
C	C₋l ink el	ave (UTAdvanced) → CC-Link master		C-l ink m	aster → CC-Link slave (UTAdvanced)	
Word	Bit		Word	Bit	,	
	position	Contents of assignment		position	Contents of assignment	
	RX0	Receive data valid		RY0	Rescan request	
	RX1	During-write		RY1	(Reserved)	
	RX2	Write acknowledgement		RY2	Write request	
	RX3	(Reserved)		RY3	(Reserved)	
	RX4	(Reserved)		RY4	(Reserved)	
	RX5	(Reserved)		RY5	(Reserved)	
	RX6	(Reserved)		RY6	(Reserved)	
	RX7	(Reserved)		RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)		:	The fixed-part is omitted (See profile number 0 on page 1	
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)	
	RX48	(Unused)		RY48	(Unused)	
	:			:		
		(1 laura d)		PV444	((lave a d)	
		(Unused)		RY111	(Unused)	
	RX112	(Reserved)		RY112	(Reserved)	
	RX123	Remote Ready		RY123	(Reserved)	
	:			:		
	RX127	(Reserved)		RY127	(Reserved)	
					,	
RWr0		Current page	RWw0		Page change request	
RWr1		01: P_L1_1	RWw1		01: P_L1_1	
RWr2		02: P_L1_1	RWw2		02: P_L1_1	
RWr3		03: P_L1_1	RWw3		03: P_L1_1	
RWr4		04: P_L1_1	RWw4		04: P_L1_1	
RWr5		05: P_L1_1	RWw5		05: P_L1_1	
RWr6		01: I_L1_1	RWw6		01: I_L1_1	
RWr7		02: I_L1_1	RWw7		02: I_L1_1	
RWr8		03: I_L1_1	RWw8		03: I_L1_1	
RWr9		04: I_L1_1	RWw9		04: I_L1_1	
RWr10		05: I_L1_1	RWw10		05: I_L1_1	
RWr11		01: D_L1_1	RWw11		01: D_L1_1	
RWr12		02: D_L1_1	RWw12		02: D_L1_1	
RWr13		03: D_L1_1	RWw13	+	03: D_L1_1	
RWr14		04: D_L1_1	RWw14		04: D_L1_1	
RWr15		05: D_L1_1	RWw15	+	05: D_L1_1	
RWr16		01: SPNO.	RWw16	+	01: SPNO.	
RWr17		02: SPNO.	RWw17	+	02: SPNO.	
RWr18		03: SPNO.	RWw18	_	03: SPNO.	
RWr19		04: SPNO.	RWw19		04: SPNO.	
RWr20		05: SPNO.	RWw20	+	05: SPNO.	
		(Unused)	RWw20	+	(Unused)	
くハハトンコ		(Unused)	RWw21	_	(Unused)	
	1	(Unused)	RWw23	+	(Unused)	
RWr22					,	
RWr22 RWr23			DIAMOA		(Unused)	
RWr21 RWr22 RWr23 RWr24		(Unused)	RWw24	+	(Unused)	
RWr22 RWr23 RWr24 RWr25		(Unused)	RWw25		(Unused)	
RWr22 RWr23 RWr24 RWr25 RWr26		(Unused) (Unused) (Unused)	RWw25 RWw26		(Unused) (Unused)	
RWr22 RWr23		(Unused)	RWw25		(Unused)	

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RWw29

RWw30

RWw31

(Unused)

(Unused)

(Unused)

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		22 (Simple PID control with 5 connected cor IN area			(Ver.2.00, 1-station occupied x8 sett			
C	C-Link sl	ave (UTAdvanced) → CC-Link master	С	CC-Link master → CC-Link slave (UTAdvanced)				
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment			
osition	position	_	position	position	_			
	RX0	Receive data valid	-	RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement	-	RY2	Write request			
	RX3	(Reserved)	-	RY3	(Reserved)			
	RX4	(Reserved)	ł	RY4	(Reserved)			
	RX5	(Reserved)	1	RY5	(Reserved)			
	RX6 RX7	(Reserved)	+	RY6 RY7	(Reserved)			
	•	(Reserved)	+	•	(Keserveu)			
	•	The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1			
	•			•	1 1			
		Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	(Unused)	l	RY48	(Unused)			
	:			:				
		(Unused)		RY111	(Unused)			
		(Reserved)		RY112	(Reserved)			
	:			:				
	•	Remote Ready	+	RY123	(Reserved)			
	•	Remote Ready	+		(Reserved)			
	:			:				
	RX127	(Reserved)		RY127	(Reserved)			
DIMA			DIM 0					
RWr0		Current page	RWw0		Page change request			
RWr1		01: Pc_L1_1	RWw1		01: Pc_L1_1			
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1			
RWr3		03: Pc_L1_1	RWw3		03: Pc_L1_1			
RWr4		04: Pc_L1_1	RWw4		04: Pc_L1_1			
RWr5		05: Pc_L1_1	RWw5		05: Pc_L1_1			
RWr6		01: lc_L1_1	RWw6		01: lc_L1_1			
RWr7		02: lc_L1_1	RWw7		02: lc_L1_1			
RWr8		03: lc_L1_1	RWw8		03: lc_L1_1			
RWr9		04: lc_L1_1	RWw9		04: lc_L1_1			
RWr10		05: lc_L1_1	RWw10		05: lc_L1_1			
RWr11		01: Dc_L1_1	RWw11		01: Dc_L1_1			
RWr12 RWr13		02: Dc_L1_1	RWw12 RWw13		02: Dc_L1_1			
RWr14		03: Dc_L1_1	RWw13		03: Dc_L1_1			
RWr15		04: Dc_L1_1 05: Dc_L1_1	RWw15		04: Dc_L1_1 05: Dc_L1_1			
RWr16		01: SPNO.	RWw16		01: SPNO.			
RWr17		02: SPNO.	RWw17		02: SPNO.			
RWr18		03: SPNO.	RWw17		03: SPNO.			
RWr19		04: SPNO.	RWw18		04: SPNO.			
RWr20		05: SPNO.	RWw20		05: SPNO.			
RWr21		(Unused)	RWw20		(Unused)			
RWr22		(Unused)	RWw21		(Unused)			
RWr23		(Unused)	RWw22		(Unused)			
RWr24		(Unused)	RWw24		(Unused)			
RWr25		(Unused)	RWw24		(Unused)			
RWr26		(Unused)	RWw25		(Unused)			
					,			
RWr27		(Unused)	RWw27		(Unused)			
RWr28		(Unused)	RWw28		(Unused)			
RWr29		(Unused)	RWw29 RWw30		(Unused)			
RW30								

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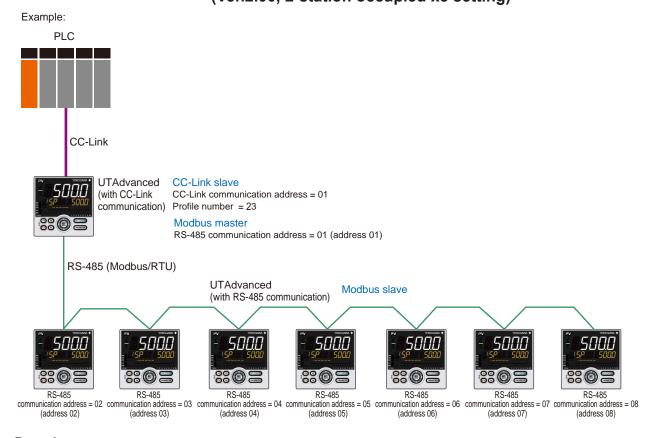
Profile	number 2	22 (Simple PID control with 5 connected cont	rolle	ers) on p	age 4	(Ver.2.00, 1-station occupied x8 setting)
	IN area			OUT area		
	CC-Link slave (UTAdvanced) → CC-Link master					aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write	[RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)	[RY4	(Reserved)
	RX5	(Reserved)	[RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	:				:	
	RX111	(Unused)	ĺ		RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	:				:	
	RX123	Remote Ready			RY123	(Reserved)
	:				:	
	RX127	(Reserved)			RY127	(Reserved)
RWr0		Current page	ı	RWw0		Page change request

RWr0	Current page
RWr1	01: A1_L1_1
RWr2	02: A1_L1_1
RWr3	03: A1_L1_1
RWr4	04: A1_L1_1
RWr5	05: A1_L1_1
RWr6	01: A2_L1_1
RWr7	02: A2_L1_1
RWr8	03: A2_L1_1
RWr9	04: A2_L1_1
RWr10	05: A2_L1_1
RWr11	01: A3_L1_1
RWr12	02: A3_L1_1
RWr13	03: A3_L1_1
RWr14	04: A3_L1_1
RWr15	05: A3_L1_1
RWr16	01: A4_L1_1
RWr17	02: A4_L1_1
RWr18	03: A4_L1_1
RWr19	04: A4_L1_1
RWr20	05: A4_L1_1
RWr21	01: A5_L1_1
RWr22	02: A5_L1_1
RWr23	03: A5_L1_1
RWr24	04: A5_L1_1
RWr25	05: A5_L1_1
RWr26	(Unused)
RWr27	(Unused)
RWr28	(Unused)
RWr29	(Unused)
RW30	(Unused)
RWr31	(Unused)

RWw0	Page change request
RWw1	01: A1_L1_1
RWw2	02: A1_L1_1
RWw3	03: A1_L1_1
RWw4	04: A1_L1_1
RWw5	05: A1_L1_1
RWw6	01: A2_L1_1
RWw7	02: A2_L1_1
RWw8	03: A2_L1_1
RWw9	04: A2_L1_1
RWw10	05: A2_L1_1
RWw11	01: A3_L1_1
RWw12	02: A3_L1_1
RWw13	03: A3_L1_1
RWw14	04: A3_L1_1
RWw15	05: A3_L1_1
RWw16	01: A4_L1_1
RWw17	02: A4_L1_1
RWw18	03: A4_L1_1
RWw19	04: A4_L1_1
RWw20	05: A4_L1_1
RWw21	01: A5_L1_1
RWw22	02: A5_L1_1
RWw23	03: A5_L1_1
RWw24	04: A5_L1_1
RWw25	05: A5_L1_1
RWw26	(Unused)
RWw27	(Unused)
RWw28	(Unused)
RWw29	(Unused)
RWw30	(Unused)
RWw31	(Unused)

Profile number 23 (Simple PID control with 8 connected controllers) (Ver.2.00, 2-station occupied x8 setting)





Page 1

Profile	Profile number 23 (Simple PID control with 8 connected con			ers) on p	age 1	(Ver.2.00, 2-station occupied x8 setting)	
		IN area		OUT area			
	CC-Link slave (UTAdvanced) → CC-Link master				C-Link m	aster → CC-Link slave (UTAdvanced)	
Word position	Bit position	Contents of assignment		Word position	Contents of assignment		
	RX0	Receive data valid			RY0	Rescan request	
	RX1	During-write			RY1	(Reserved)	
	RX2	Write acknowledgement			RY2	Write request	
	RX3	(Reserved)			RY3	(Reserved)	
	RX4	(Reserved)			RY4	(Reserved)	
	RX5	(Reserved)			RY5	(Reserved)	
	RX6	(Reserved)			RY6	(Reserved)	
	RX7	(Reserved)			RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)	
	RX48	01: A.M_L1			RY48	01: A.M_L1	
	RX49	01: R.L_L1			RY49	01: R.L_L1	
	RX50	01: S.R_L1			RY50	01: S.R_L1	
	RX51	(Unused)			RY51	(Unused)	
	RX52	(Unused)			RY52	(Unused)	
	RX53	(Unused)			RY53	(Unused)	
	RX54	(Unused)			RY54	(Unused)	
	RX55	(Unused)			RY55	(Unused)	
	RX56	01: ALM1_L1			RY56	(Unused)	
	RX57	01: ALM2_L1			RY57	(Unused)	
	RX58	01: ALM3_L1			RY58	(Unused)	
	RX59	01: ALM4_L1			RY59	(Unused)	
	RX60	01: ALM5_L1			RY60	(Unused)	
	RX61	01: ALM6_L1			RY61	(Unused)	
	RX62	01: ALM7_L1			RY62	(Unused)	

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Profile	Profile number 23 (Simple PID control with 8 connected co			age 1	(Ver.2.00, 2-station occupied x8 setting)
0	C Limbral	IN area		C I inle m	OUT area
Word	Bit	ave (UTAdvanced) → CC-Link master	Word	Bit	aster → CC-Link slave (UTAdvanced)
position	position	Contents of assignment	position	position	
		01: ALM8_L1 02: A.M_L1		RY63 RY64	(Unused) 02: A.M_L1
	_	02: R.L_L1			02: R.L_L1
		02: S.R_L1		RY66	02: S.R_L1
	RX67	(Unused)		RY67	(Unused)
	RX68	(Unused)		RY68	(Unused)
	RX69 RX70	(Unused)		RY69 RY70	(Unused)
	RX71	(Unused)		RY71	(Unused)
	RX72	02: ALM1_L1		RY72	(Unused)
		02: ALM2_L1		RY73	(Unused)
		02: ALM3_L1		RY74	(Unused)
	_	02: ALM4_L1 02: ALM5_L1		RY75 RY76	(Unused)
		02: ALM6 L1		RY77	(Unused)
		02: ALM7_L1		RY78	(Unused)
	RX79	02: ALM8_L1		RY79	(Unused)
		03: A.M_L1		RY80	03: A.M_L1
	RX81	03: R.L_L1		RY81	03: R.L_L1
	RX82 RX83	03: S.R_L1 (Unused)		RY82 RY83	03: S.R_L1 (Unused)
	RX84	(Unused)		RY84	(Unused)
	RX85	(Unused)		RY85	(Unused)
		(Unused)		RY86	(Unused)
	RX87	(Unused)		RY87	(Unused)
		03: ALM1_L1		RY88	(Unused)
	_	03: ALM2_L1		RY89	(Unused)
		03: ALM3_L1		RY90	(Unused)
	_	03: ALM4_L1 03: ALM5_L1		RY91 RY92	(Unused)
		03: ALM3_L1		RY93	(Unused)
	-	03: ALM7_L1		RY94	(Unused)
	RX95	03: ALM8_L1		RY95	(Unused)
		04: A.M_L1		RY96	04: A.M_L1
		04: R.L_L1		RY97	04: R.L_L1
		04: S.R_L1		RY98 RY99	04: S.R_L1
		(Unused)			(Unused)
	_	(Unused)			(Unused)
		(Unused)		+	(Unused)
	RX103	(Unused)		RY103	(Unused)
	_	04: ALM1_L1			(Unused)
		04: ALM2_L1		+	(Unused)
		04: ALM3_L1			(Unused)
		04: ALM4_L1 04: ALM5_L1		+	(Unused)
		04: ALM6_L1		+	(Unused)
	i 	04: ALM7_L1			(Unused)
		04: ALM8_L1		+	(Unused)
	RX112	05: A.M_L1		RY112	05: A.M_L1
		05: R.L_L1			05: R.L_L1
		05: S.R_L1		+	05: S.R_L1
-		(Unused)		_	(Unused)
		(Unused)		+	(Unused)
	_	(Unused)		_	(Unused)
		(Unused)		+	(Unused)
		05: ALM1_L1		RY120	(Unused)
		05: ALM2_L1		+	(Unused)
		05: ALM3_L1		_	(Unused)
		05: ALM4_L1		+	(Unused)
		05: ALM5_L1 05: ALM6_L1		_	(Unused)
-		05: ALM6_L1 05: ALM7_L1		+	(Unused)
	11/1/120	00.7.EIVI7_E1	· L	111120	(Chasca)

Profile	number 2	23 (Simple PID control with 8 connected con	trollers)	on p	age 1	(Ver.2.00, 2-station occupied x8 setting)
		IN area		Ť		OUT area
		ave (UTAdvanced) → CC-Link master				aster → CC-Link slave (UTAdvanced)
Word	Bit position	Contents of assignment	Wo		Bit position	Contents of assignment
position		05: ALM8_L1	posi	LIOII		(Unused)
	RX128	06: A.M_L1				06: A.M_L1
	RX129	06: R.L_L1			RY129	06: R.L_L1
		06: S.R_L1			RY130	06: S.R_L1
	-	(Unused)				(Unused)
	 	(Unused)				(Unused)
	-	(Unused)				(Unused)
		(Unused)				(Unused)
		(Unused) 06: ALM1_L1				(Unused)
		06: ALM1_L1 06: ALM2_L1				(Unused)
	-	06: ALM3_L1				(Unused)
	-	06: ALM4_L1				(Unused)
	-	06: ALM5_L1				(Unused)
	-	06: ALM6_L1			RY141	(Unused)
	RX142	06: ALM7_L1			RY142	(Unused)
		06: ALM8_L1			RY143	(Unused)
	RX144	07: A.M_L1			RY144	07: A.M_L1
		07: R.L_L1]		07: R.L_L1
	 	07: S.R_L1				07: S.R_L1
		(Unused)				(Unused)
	-	(Unused)				(Unused)
		(Unused)				(Unused)
	-	(Unused)	-			(Unused)
		(Unused) 07: ALM1_L1				(Unused)
		07: ALM2_L1				(Unused)
		07: ALM3_L1				(Unused)
	-	07: ALM4_L1				(Unused)
	-	07: ALM5_L1				(Unused)
	RX157	07: ALM6_L1			RY157	(Unused)
		07: ALM7_L1				(Unused)
		07: ALM8_L1				(Unused)
	 	08: A.M_L1				08: A.M_L1
	-	08: R.L_L1				08: R.L_L1
	 	08: S.R_L1				08: S.R_L1
		(Unused)				(Unused)
		(Unused)				(Unused)
		(Unused)				(Unused)
		(Unused)				(Unused)
		08: ALM1_L1				(Unused)
	RX169	08: ALM2_L1			RY169	(Unused)
	RX170	08: ALM3_L1			RY170	(Unused)
		08: ALM4_L1				(Unused)
	i	08: ALM5_L1				(Unused)
		08: ALM6_L1				(Unused)
	+	08: ALM7_L1				(Unused)
		08: ALM8_L1	<u> </u>			(Unused)
		(Unused)				(Unused)
	:				:	
		(Unused)				(Unused)
	-	(Reserved)				(Reserved)
	:				:	
		Remote Ready				(Reserved)
	:				:	
	RX383	(Reserved)			RY383	(Reserved)
RWr0		Current page	RW			Page change request
RWr1		01: PV_L1	RW	_		(Unused)
RWr2		02: PV_L1		/w2		(Unused)
RWr3 RWr4		03: PV_L1 04: PV_L1	RW	_		(Unused)
120014		· · · · · · · · · · · · · · · · · · ·		***	<u> </u>	

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Profile	Profile number 23 (Simple PID control with 8 connected controllers) on page 1 (Ver.2.00, 2-station occupied x8 setting)					
		IN area				OUT area
С	C-Link sl	ave (UTAdvanced) → CC-Link master		C	C-Link m	aster → CC-Link slave (UTAdvanced)
Word	Bit	Contents of assignment		Word	Bit	Contents of assignment
	position	•		-	position	ŭ .
RWr5		05: PV_L1	_	RWw5		(Unused)
RWr6		06: PV_L1		RWw6		,
RWr7		07: PV_L1	_	RWw7		(Unused)
RWr8		08: PV_L1		RWw8		,
RWr9		01: CSP_L1	_	RWw9		01: SP_L1_1
RWr10		02: CSP_L1		RWw10		02: SP_L1_1
RWr11		03: CSP_L1	_	RWw11		03: SP_L1_1
RWr12		04: CSP_L1	_	RWw12		04: SP_L1_1
RWr13		05: CSP_L1	_	RWw13		05: SP_L1_1
RWr14		06: CSP_L1	_	RWw14		06: SP_L1_1
RWr15		07: CSP_L1	_	RWw15		07: SP_L1_1
RWr16		08: CSP_L1		RWw16		08: SP_L1_1
RWr17		01: OUT_L1	_	RWw17		01: MOUT_L1
RWr18		02: OUT_L1		RWw18		02: MOUT_L1
RWr19		03: OUT_L1	_	RWw19		03: MOUT_L1
RWr20		04: OUT_L1		RWw20		04: MOUT_L1
RWr21		05: OUT_L1		RWw21		05: MOUT_L1
RWr22		06: OUT_L1		RWw22		06: MOUT_L1
RWr23		07: OUT_L1		RWw23		07: MOUT_L1
RWr24		08: OUT_L1	_	RWw24		08: MOUT_L1
RWr25		01: H.OUT_L1		RWw25		01: MOUT_L1
RWr26		02: H.OUT_L1	_	RWw26		02: MOUT_L1
RWr27		03: H.OUT_L1		RWw27		03: MOUT_L1
RWr28		04: H.OUT_L1		RWw28		04: MOUT_L1
RWr29		05: H.OUT_L1		RWw29		05: MOUT_L1
RWr30		06: H.OUT_L1	_	RWw30		06: MOUT_L1
RWr31		07: H.OUT_L1		RWw31		07: MOUT_L1
RWr32		08: H.OUT_L1		RWw32		08: MOUT_L1
RWr33		01: C.OUT_L1		RWw33		01: MOUTc_L1
RWr34		02: C.OUT_L1		RWw34		02: MOUTc_L1
RWr35		03: C.OUT_L1		RWw35		03: MOUTc_L1
RWr36		04: C.OUT_L1		RWw36		04: MOUTc_L1
RWr37		05: C.OUT_L1		RWw37		05: MOUTc_L1
RWr38		06: C.OUT_L1		RWw38		06: MOUTc_L1
RWr39		07: C.OUT_L1		RWw39		07: MOUTc_L1
RWr40		08: C.OUT_L1	_	RWw40		08: MOUTc_L1
RWr41		(Unused)	_	RWw41		(Unused)
:				:		
RWr63		(Unused)		RWw63		(Unused)

_	0.15-1	IN area	_	C 1 !!	OUT area
Word	C-Link sl	ave (UTAdvanced) → CC-Link master	Word	C-Link m	aster → CC-Link slave (UTAdvanced)
	position	Contents of assignment		position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed part is emitted		•	The fixed-part is omitted
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	(See profile number 0 on page 1)
	•	(See profile fluffiber of off page 1)		•	(See profile flumber of our page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	PX367	(Unused)	1 —	RY367	(Unused)
		(Reserved)		RY368	(Reserved)
	•	(Incocived)		•	(iveseiveu)
	:			:	(-
	RX379	Remote Ready		RY379	(Reserved)
				:	
	RX383	(Reserved)		RY383	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: P_L1_1	RWw1		01: P_L1_1
RWr2		02: P_L1_1	RWw2		02: P_L1_1
RWr3		03: P_L1_1	RWw3		03: P_L1_1
RWr4		04: P_L1_1	RWw4		04: P_L1_1
RWr5		05: P_L1_1	RWw5		05: P_L1_1
RWr6		06: P_L1_1	RWw6		06: P_L1_1
RWr7		07: P_L1_1	RWw7		07: P_L1_1
RWr8		08: P_L1_1	RWw8		08: P_L1_1
RWr9		01: I_L1_1	RWw9		01: I_L1_1
RWr10		02: I_L1_1	RWw10		02: I_L1_1
RWr11		03: I_L1_1	RWw11		03: I_L1_1
RWr12		04: I_L1_1	RWw12		04: I_L1_1
RWr13		05: I_L1_1	RWw13		05: I_L1_1
RWr14		06: I_L1_1	RWw14		06: I_L1_1
RWr15		07: I_L1_1	RWw15		07: I_L1_1
RWr16		08: I_L1_1	RWw16		08: I_L1_1
RWr17		01: D_L1_1	RWw17		01: D_L1_1
RWr18		02: D_L1_1	RWw18		02: D_L1_1
RWr19		03: D_L1_1	RWw19		03: D_L1_1
RWr20		04: D_L1_1	RWw20		04: D_L1_1
RWr21		05: D_L1_1	RWw21		05: D_L1_1
RWr22		06: D_L1_1	RWw22		06: D_L1_1
RWr23		07: D_L1_1	RWw23		07: D_L1_1
RWr24		08: D_L1_1	RWw24		08: D_L1_1
RWr25		01: SPNO.	RWw25		01: SPNO.
RWr26		02: SPNO.	RWw26		02: SPNO.
RWr27		03: SPNO.	RWw27		03: SPNO.
RWr28		04: SPNO.	RWw28		04: SPNO.
RWr29		05: SPNO.	RWw29		05: SPNO.
RWr30		06: SPNO.	RWw30		06: SPNO.
RWr31		07: SPNO.	RWw31		07: SPNO.
RWr32		08: SPNO.	RWw32		08: SPNO.
RWr33		(Unused)	RWw33		(Unused)
:			:		
RWr63		(Unused)	RWw63	-	(Unused)

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Page 3

Profile	number :	23 (Simple PID control with 8 connected con	ntrollers) on	page 3	(Ver.2.00, 2-station occupied x8 setting)
		IN area			OUT area
Word	C-Link sl	ave (UTAdvanced) → CC-Link master	Word	C-Link m	aster → CC-Link slave (UTAdvanced)
	position	Contents of assignment		position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5 RX6	(Reserved)		RY5 RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	,		•	
		The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX367	(Unused)			(Unused)
		(Reserved)		RY368	(Reserved)
	:	,		:	
	RX379	Remote Ready		RY379	(Reserved)
	:	- Tomoto Houdy		:	(
	BX383	(Reserved)		RY383	(Reserved)
	IXXXXX	(INESEIVEU)		1000	(INeserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: Pc_L1_1	RWw1		01: Pc L1 1
RWr2		02: Pc_L1_1	RWw2		02: Pc_L1_1
RWr3		03: Pc_L1_1	RWw3		03: Pc_L1_1
RWr4		04: Pc_L1_1	RWw4		04: Pc_L1_1
RWr5		05: Pc_L1_1	RWw5		05: Pc_L1_1
RWr6		06: Pc_L1_1	RWw6		06: Pc_L1_1
RWr7		07: Pc_L1_1	RWw7		07: Pc_L1_1
RWr8		08: Pc_L1_1	RWw8		08: Pc_L1_1
RWr9		01: lc_L1_1	RWw9		01: lc_L1_1
RWr10		02: lc_L1_1	RWw10		02: lc_L1_1
RWr11		03: lc_L1_1	RWw11		03: lc_L1_1
RWr12		04: lc_L1_1	RWw12	+	04: lc_L1_1
RWr13		05: lc_L1_1	RWw13	_	05: lc_L1_1
RWr14		06: lc_L1_1	RWw14	_	06: lc_L1_1
RWr15		07: lc_L1_1	RWw15	_	07: lc_L1_1
RWr16 RWr17		08: lc_L1_1 01: Dc_L1_1	RWw16 RWw17	_	08: lc_L1_1 01: Dc_L1_1
RWr18		02: Dc_L1_1	RWw18	_	02: Dc_L1_1
RWr19		03: Dc_L1_1	RWw19	+	03: Dc_L1_1
RWr20		04: Dc_L1_1	RWw20	† 	04: Dc_L1_1
RWr21		05: Dc_L1_1	RWw21	+	05: Dc_L1_1
RWr22		06: Dc_L1_1	RWw22	+	06: Dc_L1_1
RWr23		07: Dc_L1_1	RWw23	+	07: Dc_L1_1
RWr24		08: Dc_L1_1	RWw24	_	08: Dc_L1_1
RWr25		01: SPNO.	RWw25		01: SPNO.
RWr26	1	02: SPNO.	RWw26		02: SPNO.
RWr27		03: SPNO.	RWw27	+	03: SPNO.
RWr28		04: SPNO.	RWw28		04: SPNO.
RWr29		05: SPNO.	RWw29		05: SPNO.
RWr30		06: SPNO.	RWw30		06: SPNO.
RWr31		07: SPNO.	RWw31		07: SPNO.
RWr32		08: SPNO.	RWw32		08: SPNO.
RWr33		(Unused)	RWw33		(Unused)
:			:		
RWr63	 	(Unused)	RWw63		(Unused)
1111100		[(0114004)	11.00000		1(0114004)

Profile	number 2	23 (Simple PID control with 8 connected control	lers) on p	age 4	(Ver.2.00, 2-station occupied x8 setting)
		IN area		-	OUT area
		ave (UTAdvanced) → CC-Link master		C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX367	(Unused)		RY367	(Unused)
	RX368	(Reserved)		RY368	(Reserved)
	:			:	
	RX379	Remote Ready		RY379	(Reserved)
	:			:	
	RX383	(Reserved)		RY383	(Reserved)

RWr0	Current page
RWr1	01: A1_L1_1
RWr2	02: A1_L1_1
RWr3	03: A1_L1_1
RWr4	04: A1_L1_1
RWr5	05: A1_L1_1
RWr6	06: A1_L1_1
RWr7	07: A1_L1_1
RWr8	08: A1_L1_1
RWr9	01: A2_L1_1
RWr10	02: A2_L1_1
RWr11	03: A2_L1_1
RWr12	04: A2_L1_1
RWr13	05: A2_L1_1
RWr14	06: A2_L1_1
RWr15	07: A2_L1_1
RWr16	08: A2_L1_1
RWr17	01: A3_L1_1
RWr18	02: A3_L1_1
RWr19	03: A3_L1_1
RWr20	04: A3_L1_1
RWr21	05: A3_L1_1
RWr22	06: A3_L1_1
RWr23	07: A3_L1_1
RWr24	08: A3_L1_1
RWr25	01: A4_L1_1
RWr26	02: A4_L1_1
RWr27	03: A4_L1_1
RWr28	04: A4_L1_1
RWr29	05: A4_L1_1
RWr30	06: A4_L1_1
RWr31	07: A4_L1_1
RWr32	08: A4_L1_1
RWr33	01: A5_L1_1
RWr34	02: A5_L1_1
RWr35	03: A5_L1_1
RWr36	04: A5_L1_1
RWr37	05: A5_L1_1
RWr38	06: A5_L1_1
RWr39	07: A5_L1_1

08: A5_L1_1

RWr40

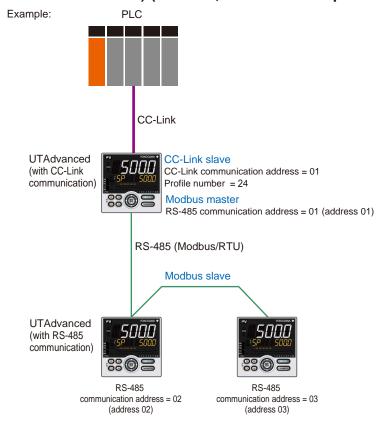
RWw0	Page change request
RWw1	01: A1_L1_1
RWw2	02: A1_L1_1
RWw3	03: A1_L1_1
RWw4	04: A1_L1_1
RWw5	05: A1_L1_1
RWw6	06: A1_L1_1
RWw7	07: A1_L1_1
RWw8	08: A1_L1_1
RWw9	01: A2_L1_1
RWw10	02: A2_L1_1
RWw11	03: A2_L1_1
RWw12	04: A2_L1_1
RWw13	05: A2_L1_1
RWw14	06: A2_L1_1
RWw15	07: A2_L1_1
RWw16	08: A2_L1_1
RWw17	01: A3_L1_1
RWw18	02: A3_L1_1
RWw19	03: A3_L1_1
RWw20	04: A3_L1_1
RWw21	05: A3_L1_1
RWw22	06: A3_L1_1
RWw23	07: A3_L1_1
RWw24	08: A3_L1_1
RWw25	01: A4_L1_1
RWw26	02: A4_L1_1
RWw27	03: A4_L1_1
RWw28	04: A4_L1_1
RWw29	05: A4_L1_1
RWw30	06: A4_L1_1
RWw31	07: A4_L1_1
RWw32	08: A4_L1_1
RWw33	01: A5_L1_1
RWw34	02: A5_L1_1
RWw35	03: A5_L1_1
RWw36	04: A5_L1_1
RWw37	05: A5_L1_1
RWw38	06: A5_L1_1
RWw39	07: A5_L1_1
RWw40	08: A5_L1_1

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Profile	Profile number 23 (Simple PID control with 8 connected co		troll	lers) on page 4 (Ver.2.00, 2-station occupied x8		(Ver.2.00, 2-station occupied x8 setting)
	IN area			OUT area		
CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr41		(Unused)		RWw41		(Unused)
:				:		
RWr63		(Unused)		RWw63		(Unused)

Profile number 24 (Cascade control / Dual-loop control with 3 connected controllers) (Ver.2.00, 1-station occupied x8 setting)





Page 1

		24 (Cascade control / dual-loop control v	rith 3 con	nected c	ontrollers) on page 1 (Ver.2.00, 1-station	
occupi	ed x8 set	IN area		_	OUT area	
С	C-Link sl	ave (UTAdvanced) → CC-Link master		CC-Link m	aster → CC-Link slave (UTAdvanced)	
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment	
	RX0	Receive data valid		RY0	Rescan request	
	RX1	During-write		RY1	(Unused)	
	RX2	Write acknowledgement		RY2	Write request	
	RX3	(Reserved)		RY3	(Reserved)	
	RX4	(Reserved)		RY4	(Reserved)	
	RX5	(Reserved)		RY5	(Reserved)	
	RX6	(Reserved)		RY6	(Reserved)	
	RX7	(Reserved)		RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)	
	RX48	01: R.L_L1		RY48	01: R.L_L1	
	RX49	01: S.R_L1		RY49	01: S.R_L1	
	RX50	01: ALM1_L1		RY50	(Unused)	
	RX51	01: ALM2_L1		RY51	(Unused)	
	RX52	01: ALM3_L1		RY52	(Unused)	
	RX53	01: ALM4_L1		RY53	(Unused)	
	RX54	(Unused)		RY54	(Unused)	
	RX55	(Unused)		RY55	(Unused)	
	RX56	02: R.L_L1		RY56	02: R.L_L1	
	RX57	02: S.R_L1		RY57	02: S.R_L1	
	RX58	02: ALM1_L1		RY58	(Unused)	
	RX59	02: ALM2_L1		RY59	(Unused)	
	RX60	02: ALM3_L1		RY60	(Unused)	
	RX61	02: ALM4_L1		RY61	(Unused)	
	RX62	(Unused)		RY62	(Unused)	

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		ave (UTAdvanced) → CC-Link master
Word	Bit	Contents of assignment
position	position RX63	(Unused)
	RX64	03: R.L L1
	RX65	03: S.R_L1
	RX66	03: ALM1_L1
	RX67	03: ALM2 L1
	RX68	03: ALM3 L1
		03: ALM4_L1
	RX69	_
	RX70	(Unused)
	RX71	(Unused)
	RX72	(Unused)
	RX73	(Unused)
	RX74	(Unused)
	RX75	(Unused)
	RX76	(Unused)
	RX77	(Unused)
	RX78	(Unused)
	RX79	(Unused)
	RX80	01: R.L_L2
	RX81	01: S.R_L2
	RX82	01: ALM1_L2
	RX83	01: ALM2_L2
	RX84	01: ALM3_L2
	RX85	01: ALM4_L2
	RX86	(Unused)
	RX87	(Unused)
	RX88	02: R.L L2
	RX89	02: S.R_L2
	RX90	02: ALM1 L2
	RX91	02: ALM2_L2
	RX92	02: ALM3_L2
	RX93	02: ALM4 L2
	RX94	(Unused)
	RX95	(Unused)
	RX96	03: R.L_L2
	RX97	03: S.R_L2
	RX98	03: ALM1 L2
	RX99	03: ALM1_L2 03: ALM2_L2
	RX100	03: ALM2_L2 03: ALM3_L2
	RX100	
		03: ALM4_L2
	_	(Unused)
	RX103	(Unused)
		(Unused)
		(Unused)
	RX106	(Unused)
	RX107	(Unused)
	RX108	(Unused)
	RX109	(Unused)
	RX110	(Unused)
	RX111	(Unused)
	RX112	(Reserved)
	:	
	RX123	Remote Ready
	:	

Current page 01: PV_L1

02: PV_L1 03: PV_L1 01: PV_L2 02: PV_L2

03: PV_L2

RWr0 RWr1 RWr2

RWr3 RWr4 RWr5

RWr6

l	
	_
	R'
	R'
	R'
	R'
	R'
	R
	R'

C	OUT area $ \textbf{CC-Link master} \rightarrow \textbf{CC-Link slave (UTAdvanced)} $		
Word	Bit	i i	
	position	Contents of assignment	
poomon	RY63	(Unused)	
	RY64	03: R.L L1	
	RY65	03: S.R L1	
	RY66	(Unused)	
	RY67	(Unused)	
	RY68	(Unused)	
	RY69	(Unused)	
	RY70	(Unused)	
	RY71	(Unused)	
	RY72	(Unused)	
	RY73	(Unused)	
	RY74	(Unused)	
	RY75	(Unused)	
	RY76	(Unused)	
	RY77	(Unused)	
	RY78	(Unused)	
	RY79	(Unused)	
	RY80	01: R.L_L2	
	RY81	01: S.R_L2	
	RY82	(Unused)	
	RY83	(Unused)	
	RY84	(Unused)	
	RY85	(Unused)	
		(Unused)	
	RY86	(Unused)	
	RY87	,	
	RY88	02: R.L_L2 02: S.R L2	
	RY89		
	RY90 RY91	(Unused)	
	RY92	(Unused)	
	RY93	(Unused)	
	R94	(Unused)	
	RY95	(Unused)	
	RY96	03: R.L_L2	
	RY97	03: S.R_L2	
	RY98	(Unused)	
	RY99	(Unused)	
	RY100	(Unused)	
	RY101	(Unused)	
	RY102	(Unused)	
	RY103	(Unused)	
	RY104	(Unused)	
	RY105	(Unused)	
	RY106	(Unused)	
	RY107	(Unused)	
	RY108	(Unused)	
	RY109	(Unused)	
	RY110	(Unused)	
	RY111	(Unused)	
	RY112	(Reserved)	
	:		
	RY123	(Reserved)	
	:		
	RY127	(Reserved)	

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	(Unused)
RWw5	(Unused)
RWw6	(Unused)

Profile number 24 (Cascade control / dual-loop control with 3 connected controllers) on page 1 (Ver.2.00, 1-station occupied x8 setting)

		IN area		OUT area			
		ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
RWr7		01: CSP_L1		RWw7		01: SP_L1_1	
RWr8		02: CSP_L1		RWw8		02: SP_L1_1	
RWr9		03: CSP_L1		RWw9		03: SP_L1_1	
RWr10		01: CSP_L2		RWw10		01: SP_L2_1	
RWr11		02: CSP_L2		RWw11		02: SP_L2_1	
RWr12		03: CSP_L2		RWw12		03: SP_L2_1	
RWr13		01: C.A.M		RWw13		01: C.A.M	
RWr14		02: C.A.M		RWw14		02: C.A.M	
RWr15		03: C.A.M		RWw15		03: C.A.M	
RWr16		01: OUT_L1		RWw16		01: MOUT_L1	
RWr17		02: OUT_L1		RWw17		02: MOUT_L1	
RWr18		03: OUT_L1		RWw18		03: MOUT_L1	
RWr19		01: OUT_L2		RWw19		01: MOUT_L2	
RWr20		02: OUT_L2		RWw20		02: MOUT_L2	
RWr21		03: OUT_L2		RWw21		03: MOUT_L2	
RWr22		(Unused)]	RWw22		(Unused)	
RWr23		(Unused)		RWw23		(Unused)	
RWr24		(Unused)		RWw24		(Unused)	
RWr25		01: A.M_L1]	RWw25		01: A.M_L1	
RWr26		02: A.M_L1]	RWw26		02: A.M_L1	
RWr27		03: A.M_L1]	RWw27		03: A.M_L1	
RWr28		01: A.M_L2]	RWw28		01: A.M_L2	
RWr29		02: A.M_L2		RWw29		02: A.M_L2	
RW30		03: A.M_L2		RWw30		03: A.M_L2	
RWr31		(Unused)		RWw31		(Unused)	

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Page 2

Profile number 24 (Cascade control / dual-loop control with 3 connected controllers) on page 2 (Ver.2.00, 1-station occupied x8 setting)

	IN area				
	CC-Link slave (UTAdvanced) → CC-Link master				
Word position	Bit position	Contents of assignment			
	RX0	Receive data valid			
	RX1	During-write			
	RX2	Write acknowledgement			
	RX3	(Reserved)			
	RX4	(Reserved)			
	RX5	(Reserved)			
	RX6	(Reserved)			
	RX7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)			
	RX48	(Unused)			
	:				
	RX111	(Unused)			
	RX112	(Reserved)			
	:				
	RX123	Remote Ready			
	:				
	RX127	(Reserved)			

C	C-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	
	RY0	Rescan request	
	RY1	(Reserved)	
	RY2	Write request	
	RY3	(Reserved)	
	RY4	(Reserved)	
	RY5	(Reserved)	
	RY6	(Reserved)	
	RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)	
	RY47	Batch write request (address 32)	
	RY48	(Unused)	
	:		
	RY111	(Unused)	
	RY112	(Reserved)	
	:		
	RY123	(Reserved)	
	RY127	(Reserved)	

OUT area

RWr0	Current page
RWr1	01: P_L1_1
RWr2	02: P_L1_1
RWr3	03: P_L1_1
RWr4	01: I_L1_1
RWr5	02: I_L1_1
RWr6	03: I_L1_1
RWr7	01: D_L1_1
RWr8	02: D_L1_1
RWr9	03: D_L1_1
RWr10	01: SPNO.
RWr11	02: SPNO.
RWr12	03: SPNO.
RWr13	01: P_L2_1
RWr14	02: P_L2_1
RWr15	03: P_L2_1
RWr16	01: I_L2_1
RWr17	02: I_L2_1
RWr18	03: I_L2_1
RWr19	01: D_L2_1
RWr20	02: D_L2_1
RWr21	03: D_L2_1
RWr22	(Unused)
RWr23	(Unused)
RWr24	(Unused)
RWr25	(Unused)
RWr26	(Unused)
RWr27	(Unused)
RWr28	(Unused)
RWr29	(Unused)
RW30	(Unused)
RWr31	(Unused)

RWw15	RWw0	Page change request
RWw3	RWw1	01: P_L1_1
RWw4	RWw2	02: P_L1_1
RWw5 02: I_L1_1 RWw6 03: I_L1_1 RWw7 01: D_L1_1 RWw8 02: D_L1_1 RWw9 03: D_L1_1 RWw9 03: D_L1_1 RWw10 01: SPNO. RWw11 02: SPNO. RWw12 03: SPNO. RWw12 03: SPNO. RWw13 01: P_L2_1 RWw14 02: P_L2_1 RWw15 03: P_L2_1 RWw16 01: I_L2_1 RWw16 01: I_L2_1 RWw17 02: I_L2_1 RWw18 03: I_L2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw29 (Unused) RWw29 (Unused) RWw29 (Unused) RWw29 (Unused) RWw29 (Unused) RWw29 (Unused)	RWw3	03: P_L1_1
RWw6 03: LL1_1 RWw7 01: D_L1_1 RWw8 02: D_L1_1 RWw9 03: D_L1_1 RWw10 01: SPNO. RWw11 02: SPNO. RWw12 03: SPNO. RWw13 01: P_L2_1 RWw14 02: P_L2_1 RWw15 03: P_L2_1 RWw16 01: LL2_1 RWw17 02: LL2_1 RWw18 03: LL2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw4	01: I_L1_1
RWw7 01: D_L1_1 RWw8 02: D_L1_1 RWw9 03: D_L1_1 RWw10 01: SPNO. RWw11 02: SPNO. RWw12 03: SPNO. RWw13 01: P_L2_1 RWw14 02: P_L2_1 RWw15 03: P_L2_1 RWw16 01: L_2_1 RWw17 02: L_L2_1 RWw18 03: L_2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw5	02: I_L1_1
RWw8 02: D_L1_1 RWw9 03: D_L1_1 RWw10 01: SPNO. RWw11 02: SPNO. RWw12 03: SPNO. RWw13 01: P_L2_1 RWw14 02: P_L2_1 RWw15 03: P_L2_1 RWw16 01: L_L2_1 RWw17 02: L_L2_1 RWw18 03: L_L2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused)	RWw6	03: I_L1_1
RWw9	RWw7	01: D_L1_1
RWw10 01: SPNO. RWw11 02: SPNO. RWw12 03: SPNO. RWw13 01: P_L2_1 RWw14 02: P_L2_1 RWw15 03: P_L2_1 RWw16 01: L_L2_1 RWw17 02: L_L2_1 RWw18 03: L_L2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw8	02: D_L1_1
RWw11 02: SPNO. RWw12 03: SPNO. RWw13 01: P_L2_1 RWw14 02: P_L2_1 RWw15 03: P_L2_1 RWw16 01: L_2_1 RWw17 02: L_2_1 RWw18 03: L_2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw9	03: D_L1_1
RWw12 03: SPNO. RWw13 01: P_L2_1 RWw14 02: P_L2_1 RWw15 03: P_L2_1 RWw16 01: L_L2_1 RWw17 02: L_L2_1 RWw18 03: L_L2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw10	01: SPNO.
RWw13	RWw11	02: SPNO.
RWw14 02: P_L2_1 RWw15 03: P_L2_1 RWw16 01: L_L2_1 RWw17 02: L_L2_1 RWw18 03: L_L2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw12	03: SPNO.
RWw15	RWw13	01: P_L2_1
RWw16 01: I_L2_1 RWw17 02: I_L2_1 RWw18 03: I_L2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw21 (Unused) RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw29 (Unused) RWw29 (Unused)	RWw14	02: P_L2_1
RWw17 02: I_L2_1 RWw18 03: I_L2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw15	03: P_L2_1
RWw18 03: I_L2_1 RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw16	01: I_L2_1
RWw19 01: D_L2_1 RWw20 02: D_L2_1 RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw27 (Unused) RWw28 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw17	02: I_L2_1
RWw20	RWw18	03: I_L2_1
RWw21 03: D_L2_1 RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw19	01: D_L2_1
RWw22 (Unused) RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw20	02: D_L2_1
RWw23 (Unused) RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw21	03: D_L2_1
RWw24 (Unused) RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw22	(Unused)
RWw25 (Unused) RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw23	(Unused)
RWw26 (Unused) RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw24	(Unused)
RWw27 (Unused) RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw25	(Unused)
RWw28 (Unused) RWw29 (Unused) RWw30 (Unused)	RWw26	(Unused)
RWw29 (Unused) RWw30 (Unused)	RWw27	(Unused)
RWw30 (Unused)	RWw28	(Unused)
()	RWw29	(Unused)
RWw31 (Unused)	RWw30	(Unused)
	RWw31	(Unused)

Profile number 24 (Cascade control / dual-loop control with 3 connected controllers) on page 3 (Ver.2.00, 1-station occupied/ x8 setting)

occupied/ x8 setting)					
		IN area			
С	C-Link sl	ave (UTAdvanced) → CC-Link master			
Word position	Bit position	Contents of assignment	Word		
	RX0	Receive data valid			
	RX1	During-write			
	RX2	Write acknowledgement			
	RX3	(Reserved)			
	RX4	(Reserved)			
	RX5	(Reserved)			
	RX6	(Reserved)			
	RX7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)			
	RX48	(Unused)			
	:				
	RX111	(Unused)			
	RX112	(Reserved)			
	RX123	Remote Ready			
	:				
	RX127	(Reserved)			
RWr0		Current page	RWw		
RWr1		(Unused)	RWw		
RWr2		(Unused)	RWw		
RWr3		(Unused)	RWw		
RWr4		(Unused)	RWw		
RWr5		(Unused)	RWw		
RWr6		(Unused)	RWw		
D) 4/ -	1	Lat. 10	Dia:		

	OUT area				
		aster → CC-Link slave (UTAdvanced)			
Word	Bit	Contents of assignment			
position	-	_			
	RY0	Rescan request			
	RY1	(Reserved)			
	RY2	Write request			
	RY3	(Reserved)			
	RY4	(Reserved)			
	RY5	(Reserved)			
	RY6	(Reserved)			
	RY7	(Reserved)			
	•	The fixed-part is omitted			
	•	·			
	•	(See profile number 0 on page 1)			
	RY47	Batch write request (address 32)			
	RY48	(Unused)			
	:				
	RY111	(Unused)			
	RY112	(Reserved)			
	:				
	RY123	(Reserved)			
	:				
	RY127	(Reserved)			

RWr0	Current page
RWr1	(Unused)
RWr2	(Unused)
RWr3	(Unused)
RWr4	(Unused)
RWr5	(Unused)
RWr6	(Unused)
RWr7	(Unused)
RWr8	(Unused)
RWr9	(Unused)
RWr10	(Unused)
RWr11	(Unused)
RWr12	(Unused)
RWr13	(Unused)
RWr14	(Unused)
RWr15	(Unused)
RWr16	(Unused)
RWr17	(Unused)
RWr18	(Unused)
RWr19	(Unused)
RWr20	(Unused)
RWr21	(Unused)
RWr22	(Unused)
RWr23	(Unused)
RWr24	(Unused)
RWr25	(Unused)
RWr26	(Unused)
RWr27	(Unused)
RWr28	(Unused)
RWr29	(Unused)
RW30	(Unused)
RWr31	(Unused)

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	(Unused)
RWw5	(Unused)
RWw6	(Unused)
RWw7	(Unused)
RWw8	(Unused)
RWw9	(Unused)
RWw10	(Unused)
RWw11	(Unused)
RWw12	(Unused)
RWw13	(Unused)
RWw14	(Unused)
RWw15	(Unused)
RWw16	(Unused)
RWw17	(Unused)
RWw18	(Unused)
RWw19	(Unused)
RWw20	(Unused)
RWw21	(Unused)
RWw22	(Unused)
RWw23	(Unused)
RWw24	(Unused)
RWw25	(Unused)
RWw26	(Unused)
RWw27	(Unused)
RWw28	(Unused)
RWw29	(Unused)
RWw30	(Unused)
RWw31	(Unused)

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Page 4

Profile number 24 (Cascade control / dual-loop with 3 connected controllers) on page 4 (Ver.2.00, 1-station occupied x8 setting)

IN area

OUT area

IN area		
CC-Link slave (UTAdvanced) → CC-Link master		
Word position	Bit position	Contents of assignment
	RX0	Receive data valid
	RX1	During-write
	RX2	Write acknowledgement
	RX3	(Reserved)
	RX4	(Reserved)
	RX5	(Reserved)
	RX6	(Reserved)
	RX7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)
	RX48	(Unused)
	:	
	RX111	(Unused)
	RX112	(Reserved)
	:	
	RX123	Remote Ready
	:	
	RX127	(Reserved)

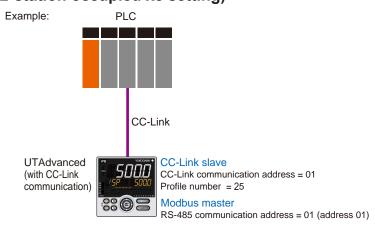
C	C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment
	RY0	Rescan request
	RY1	(Reserved)
	RY2	Write request
	RY3	(Reserved)
	RY4	(Reserved)
	RY5	(Reserved)
	RY6	(Reserved)
	RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)
	RY47	Batch write request (address 32)
	RY48	(Unused)
	:	
	RY111	(Unused)
	RY112	(Reserved)
	:	
	RY123	(Reserved)
	:	
	RY127	(Reserved)

RWr0	Current page
RWr1	01: A1_L1_1
RWr2	02: A1_L1_1
RWr3	03: A1_L1_1
RWr4	01: A2_L1_1
RWr5	02: A2_L1_1
RWr6	03: A2_L1_1
RWr7	01: A3_L1_1
RWr8	02: A3_L1_1
RWr9	03: A3_L1_1
RWr10	01: A4_L1_1
RWr11	02: A4_L1_1
RWr12	03: A4_L1_1
RWr13	(Unused)
RWr14	(Unused)
RWr15	(Unused)
RWr16	01: A1_L2_1
RWr17	02: A1_L2_1
RWr18	03: A1_L2_1
RWr19	01: A2_L2_1
RWr20	02: A2_L2_1
RWr21	03: A2_L2_1
RWr22	01: A3_L2_1
RWr23	02: A3_L2_1
RWr24	03: A3_L2_1
RWr25	01: A4_L2_1
RWr26	03: A5_L2_1
RWr27	03: A4_L2_1
RWr28	(Unused)
RWr29	(Unused)
RW30	(Unused)
RWr31	(Reserved)

RWw0	F	Page change request
RWw1	(01: A1_L1_1
RWw2	(02: A1_L1_1
RWw3	(03: A1_L1_1
RWw4	(01: A2_L1_1
RWw5	(02: A2_L1_1
RWw6	(03: A2_L1_1
RWw7	(01: A3_L1_1
RWw8	(02: A3_L1_1
RWw9	(03: A3_L1_1
RWw10	(01: A4_L1_1
RWw11	(02: A4_L1_1
RWw12	(03: A4_L1_1
RWw13	(Unused)
RWw14	(Unused)
RWw15	(Unused)
RWw16	(01: A1_L2_1
RWw17	(02: A1_L2_1
RWw18	(03: A1_L2_1
RWw19	()1: A2_L2_1
RWw20	(02: A2_L2_1
RWw21	(03: A2_L2_1
RWw22	(01: A3_L2_1
RWw23	(02: A3_L2_1
RWw24	(03: A3_L2_1
RWw25	(01: A4_L2_1
RWw26	(02: A4_L2_1
RWw27	(03: A4_L2_1
RWw28	(Unused)
RWw29	(Unused)
RWw30	(Unused)
RWw31	(Reserved)

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) (Ver.2.00, 2-station occupied x8 setting)





Page 1

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area

OUT area

	IN area		
CC-Link slave (UTAdvanced) → CC-Link master			
Word position	Bit position	Contents of assignment	
	RX0	Receive data valid	
	RX1	During-write	
	RX2	Write acknowledgement	
	RX3	(Reserved)	
	RX4	(Reserved)	
	RX5	(Reserved)	
	RX6	(Reserved)	
	RX7	(Reserved)	
	•	The fixed-part is omitted	
		(See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)	
	RX48	01: A.M_L1	
	RX49	01: R.L_L1	
	RX50	01: S.R_L1	
	RX51	01: HOLD	
	RX52	(Unused)	
	RX53	01: WAITF	
	RX54	01: PROG	
	RX55	(Unused)	
	RX56	01: ALM1_L1	
	RX57	01: ALM2_L1	
	RX58	01: ALM3_L1	
	RX59	01: ALM4_L1	
	RX60	01: ALM5_L1	
	RX61	01: ALM6_L1	
	RX62	01: ALM7_L1	
	RX63	01: ALM8_L1	
	RX64	01: A.M_L2	
	RX65	01: R.L_L2	
	RX66	01: S.R_L2	
	RX67	(Unused)	
	RX68	(Unused)	
	RX69	(Unused)	
	RX70	(Unused)	
	RX71	(Unused)	
	RX72	01: ALM1_L2	
	RX73	01: ALM2_L2	
	RX74	01: ALM3_L2	

0.	OUT area			
CC-Link master → CC-Link slave (UTAdvanced) Word Bit Control Bit Bit Control Bit Bit Bit Control Bit B				
	position	Contents of assignment		
	RY0	Rescan request		
	RY1	(Unused)		
	RY2	Write request		
	RY3	(Reserved)		
	RY4	(Reserved)		
	RY5	(Reserved)		
	RY6	(Reserved)		
	RY7	(Reserved)		
	•	The fixed-part is omitted		
	•	(See profile number 0 on page 1)		
	•	· · ·		
	RY47	Batch write request (address 32)		
	RY48	01: A.M_L1		
	RY49	01: R.L_L1		
	RY50	01: S.R_L1		
	RY51	01: HOLD		
	RY52	01: ADV		
	RY53	(Unused)		
	RY54	01: PROG		
	RY55 RY56	(Unused)		
	RY57	(Unused)		
	RY58	(Unused)		
	RY59	(Unused)		
	RY60	(Unused)		
	RY61	(Unused)		
	RY62	(Unused)		
	RY63	(Unused)		
	RY64	01: A.M_L2		
	RY65	01: R.L L2		
	RY66	01: S.R_L2		
	RY67	(Unused)		
	RY68	(Unused)		
	RY69	(Unused)		
	RY70	(Unused)		
	RY71	(Unused)		
	RY72	(Unused)		
	RY73	(Unused)		
	RY74	(Unused)		

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Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 1 (Ver.2.00, 2-station occupied x8 setting)

		IN area	
Word	CC-Link slave (UTAdvanced) → CC-Link master Word Bit		
position		Contents of assignment	
		01: ALM4_L2	
	RX76	01: ALM5_L2	
	RX77 RX78	01: ALM6_L2 01: ALM7_L2	
	RX79	01: ALM8 L2	
	RX80	(Unused)	
	RX81	(Unused)	
	RX82	(Unused)	
	RX83	(Unused)	
	RX84	(Unused)	
	RX85 RX86	(Unused)	
	RX87	(Unused)	
	RX88	(Unused)	
	RX89	(Unused)	
	RX90	(Unused)	
	RX91	(Unused)	
	RX92	(Unused)	
	RX93	(Unused)	
	RX94	(Unused)	
	RX95	(Unused)	
	RX96 RX97	(Unused)	
	RX98	(Unused)	
	RX99	(Unused)	
	RX100	(Unused)	
	RX101	(Unused)	
		(Unused)	
	RX100	(Unused)	
		(Unused)	
		(Unused)	
	RX110	(Unused)	
	RX111	(Unused)	
	RX112	(Unused)	
	RX113	(Unused)	
	RX114	(Unused)	
	RX115 RX116	(Unused)	
	RX116	(Unused)	
	RX117	(Unused)	
		(Unused)	
	RX120	(Unused)	
	RX121	(Unused)	
		(Unused)	
	RX123	(Unused)	
		(Unused)	
	RX125 RX126	(Unused)	
	RX120	(Unused)	
		(Unused)	
	RX129	(Unused)	
	RX130	(Unused)	
	RX131	(Unused)	
	RX132	(Unused)	
	RX133	(Unused)	
	RX134	(Unused)	
	RX135	(Unused)	
	RX136	(Unused)	

0.	0.1.51	OUT area
Word	C-Link m	aster → CC-Link slave (UTAdvanced)
	position	Contents of assignment
promon	RY75	(Unused)
	RY76	(Unused)
	RY77	(Unused)
	RY78	(Unused)
	RY79	(Unused)
	RY80	(Unused)
	RY81	(Unused)
	RY82	(Unused)
	RY83	(Unused)
	RY84	(Unused)
	RY85	(Unused)
	RY86	(Unused)
	RY87	(Unused)
	RY88	(Unused)
	RY89	(Unused)
	RY90	
	RY91	(Unused)
		(Unused)
	RY92	(Unused)
	RY93	(Unused)
	RY94	(Unused)
	RY95	(Unused)
	RY96	(Unused)
	RY97	(Unused)
	RY98	(Unused)
	RY99	(Unused)
	RY100	(Unused)
	RY101	(Unused)
	RY102	(Unused)
	RY103	(Unused)
	RY104	(Unused)
	RY105	(Unused)
	RY106	(Unused)
	RY107	(Unused)
	RY108	(Unused)
	RY109	(Unused)
	RY110	(Unused)
	RY111	(Unused)
	RY112	(Unused)
	RY113	(Unused)
	RY114	(Unused)
	RY115	(Unused)
	RY116	(Unused)
	RY117	(Unused)
	RY118	(Unused)
	RY119	(Unused)
	RY120	(Unused)
	RY121	(Unused)
	RY122	(Unused)
	RY123	(Unused)
	RY124	(Unused)
	RY125	(Unused)
	RY126	(Unused)
<u> </u>	RY127	(Unused)
	RY127	(Unused)
		,
	RY129	(Unused)
	RY130	(Unused)
	RY131	(Unused)
	RY132	(Unused)
	RY133	(Unused)
	RY134	(Unused)
	RY135	(Unused)
	RY136	(Unused)

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 1 (Ver.2.00, 2-station occupied x8 setting)

	IN area			OUT area		
	CC-Link slave (UTAdvanced) → CC-Link master				aster → CC-Link slave (UTAdvanced)	
Word	Bit position	Contents of assignment	Word	Bit position	Contents of assignment	
OSILIOII		(Unused)	position		(Unused)	
	RX138	(Unused)	1	RY138	(Unused)	
	RX139	(Unused)		RY139	(Unused)	
	RX140	(Unused)		RY140	(Unused)	
	RX141	(Unused)]	RY141	(Unused)	
	_	(Unused)		_	(Unused)	
		(Unused)	┦ ├──	_	(Unused)	
	_	(Unused)	┩	_	(Unused)	
		(Unused)	┥ ├──	+	(Unused)	
	-	(Unused)	┨	-	(Unused)	
	-	(Unused)	-	RY147 RY148	(Unused)	
	-	(Unused)	┨ ├──		(Unused)	
	RX149	(Unused)	1	+	(Unused)	
	-	(Unused)	1	RY151	(Unused)	
	-	(Unused)	1	_	(Unused)	
	RX153	(Unused)	1	RY153	(Unused)	
		(Unused)	1	RY154	(Unused)	
	RX155	(Unused)		RY155	(Unused)	
	RX156	(Unused)		RY156	(Unused)	
	RX157	(Unused)		RY157	(Unused)	
	RX158	(Unused)		RY158	(Unused)	
	RX159	(Unused)		RY159	(Unused)	
		(Unused)		+	(Unused)	
	-	(Unused)	┦		(Unused)	
	-	(Unused)		+	(Unused)	
		(Unused)	┨ ├──	-	(Unused)	
	_	(Unused)	-	_	(Unused)	
		(Unused)	┨	+	(Unused)	
	RX167	(Unused)	┨ ├──	RY167	(Unused)	
		(Unused)	1		(Unused)	
		(Unused)	1	RY169	(Unused)	
	-	(Unused)	1	RY170	(Unused)	
	RX171	(Unused)	1	RY171	(Unused)	
	RX172	(Unused)	1	RY172	(Unused)	
	RX173	(Unused)		RY173	(Unused)	
	RX174	(Unused)		RY174	(Unused)	
	RX175	(Unused)		RY175	(Unused)	
		(Unused)			(Unused)	
		(Unused)		1	(Unused)	
	_	(Unused)		+	(Unused)	
	_	(Unused)		+	(Unused)	
		(Unused)	┦ ├──	+	(Unused)	
		(Unused)	┨	1	(Unused)	
	-	(Unused)		+	(Unused)	
		(Unused)	-		(Unused)	
		(Unused)	┥ ├──	+	(Unused)	
		(Unused)	1 -	+	(Unused)	
		(Unused)	1	+	(Unused)	
		(Unused)	1	_	(Unused)	
	-	(Unused)	1	+	(Unused)	
		(Unused)	1	+	(Unused)	
		(Unused)	1	+	(Unused)	
		(Unused)	1		(Unused)	
		(Unused)	1	+	(Unused)	
		(Unused)	1	+	(Unused)	
	-	(Unused)	1	+	(Unused)	
		(Unused)	1	+	(Unused)	
		(Unused)	1	+	(Unused)	
		(Unused)	1	+	(Unused)	

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Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area			
C	CC-Link slave (UTAdvanced) → CC-Link master		
Word	Word Bit Contents of assignment		
poolition	-	(Unused)	
	RX200	(Unused)	
	RX201	(Unused)	
	RX202	(Unused)	
	RX203	(Unused)	
	RX204	(Unused)	
	RX205	(Unused)	
	RX206	(Unused)	
	RX207	(Unused)	
	RX208	(Unused)	
	:		
	RX367	(Unused)	
	RX368	(Reserved)	
	:		
	RX379	Remote Ready	
	:		
	RX383	(Reserved)	

OUT area			
C	CC-Link master → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment	
	RY199	(Unused)	
	RY200	(Unused)	
	RY201	(Unused)	
	RY202	(Unused)	
	RY203	(Unused)	
	RY204	(Unused)	
	RY205	(Unused)	
	RY206	(Unused)	
	RY207	(Unused)	
	RY208	(Unused)	
	:		
	RY367	(Unused)	
	RY368	(Reserved)	
	:		
	RY379	(Reserved)	
	:		
	RY383	(Reserved)	

RWr0	Current page
RWr1	01: PV_L1
RWr2	01: PV_L2
RWr3	01: CSP_L1
RWr4	01: CSP_L2
RWr5	01: C.A.M
RWr6	01: OUT_L1
RWr7	01: OUT_L2
RWr8	01: H.OUT_L1
RWr9	01: H.OUT_L2
RWr10	01: C.OUT_L1
RWr11	01: C.OUT_L2
RWr12	01:SEG.N
RWr13	01:SEG_RTIME
RWr14	01:REM_RCY
RWr15	01:SEG_MAX
RWr16	(Unused)
RWr17	(Unused)
RWr18	(Unused)
RWr19	(Unused)
RWr20	(Unused)
RWr21	(Unused)
RWr22	(Unused)
RWr23	(Unused)
RWr24	(Unused)
RWr25	(Unused)
RWr26	(Unused)
RWr27	(Unused)
RWr28	(Unused)
RWr29	(Unused)
RWr30	(Unused)
RWr31	(Unused)
RWr32	(Unused)
RWr33	(Unused)
RWr34	(Unused)
RWr35	(Unused)
RWr36	(Unused)
RWr37	(Unused)
RWr38	(Unused)
RWr39	(Unused)
RWr40	(Unused)
RWr41	(Unused)
:	

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	01: SP_L1
RWw4	01: SP_L2
RWw5	01: C.A.M
RWw6	01: MOUT_L1
RWw7	01: MOUT_L2
RWw8	01: MOUT_L1
RWw9	01: MOUT_L2
RWw10	01: MOUTc_L1
RWw11	01: MOUTc_L2
RWw12	(Unused)
RWw13	(Unused)
RWw14	(Unused)
RWw15	(Unused)
RWw16	(Unused)
RWw17	(Unused)
RWw18	(Unused)
RWw19	(Unused)
RWw20	(Unused)
RWw21	(Unused)
RWw22	(Unused)
RWw23	(Unused)
RWw24	(Unused)
RWw25	(Unused)
RWw26	(Unused)
RWw27	(Unused)
RWw28	(Unused)
RWw29	(Unused)
RWw30	(Unused)
RWw31	(Unused)
RWw32	(Unused)
RWw33	(Unused)
RWw34	(Unused)
RWw35	(Unused)
RWw36	(Unused)
RWw37	(Unused)
RWw38	(Unused)
RWw39	(Unused)
RWw40	(Unused)
RWw41	(Unused)
:	

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area

CC-Link slave (UTAdvanced) → CC-Link master

Word Bit position position

RWr63 (Unused)

Contents of assignment

RWw63 (Unused)

Control with 1 connected controller) on page 1

CC-Link master → CC-Link slave (UTAdvanced)

Word District Position Position RWw63 (Unused)

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Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 2 (Ver.2.00, 2-station occupied x8 setting)

IN area			
CC-Link slave (UTAdvanced) → CC-Link master			
Word position	Bit position	Contents of assignment	
	RX0	Receive data valid	
	RX1	During-write	
	RX2	Write acknowledgement	
	RX3	(Reserved)	
	RX4	(Reserved)	
	RX5	(Reserved)	
	RX6	(Reserved)	
	RX7	(Reserved)	
	•	The fixed-part is omitted	
		(See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)	
	RX48	(Unused)	
	:		
	RX367	(Unused)	
	RX368	(Reserved)	
	:		
	RX379	Remote Ready	
	:		
	RX383	(Reserved)	

		OUT area		
		aster → CC-Link slave (UTAdvanced)		
Word position	Bit position	Contents of assignment		
	RY0	Rescan request		
	RY1	(Reserved)		
	RY2	Write request		
	RY3	(Reserved)		
	RY4	(Reserved)		
	RY5	(Reserved)		
	RY6	(Reserved)		
	RY7	(Reserved)		
	•	The fixed-part is omitted (See profile number 0 on page 1)		
	RY47	Batch write request (address 32)		
	RY48	(Unused)		
	:			
	RY367	(Unused)		
	RY368	(Reserved)		
	:			
	RY379	(Reserved)		
	:			
	RY383	(Reserved)		

RWr0		Current page
RWr1		01: P_L1_1
RWr2		01: I_L1_1
RWr3		01: D_L1_1
RWr4		01: P_L2_1
RWr5		01: I_L2_1
RWr6		01: D_L2_1
RWr7		01: Pc_L1_1
RWr8		01: lc_L1_1
RWr9		01: Dc_L1_1
RWr10		01: Pc_L2_1
RWr11		01: lc_L2_1
RWr12		01: Dc_L2_1
RWr13		01: SPNO.
RWr14		01: A1_L1_1
RWr15		01: A2_L1_1
RWr16		01: A3_L1_1
RWr17		01: A4_L1_1
RWr18		01: A5_L1_1
RWr19		01: A6_L1_1
RWr20		01: A7_L1_1
RWr21		01: A8_L1_1
RWr22		01: A1_L2_1
RWr23		01: A2_L2_1
RWr24		01: A3_L2_1
RWr25		01: A4_L2_1
RWr26		01: A5_L2_1
RWr27		01: A6_L2_1
RWr28		01: A7_L2_1
RWr29		01: A8_L2_1
RWr30		(Unused)
RWr31		(Unused)
RWr32		(Unused)
RWr33		(Unused)
RWr34		(Unused)
RWr35		(Unused)
RWr36		(Unused)
:		

RWr63

(Unused)

RWw0	Page change request
RWw1	01: P L1 1
RWw2	01: I L1 1
RWw3	01: D L1 1
RWw4	01: P_L2_1
RWw5	01: I L2 1
RWw6	01: D L2 1
RWw7	01: Pc L1 1
RWw8	01: Ic L1 1
RWw9	01: Dc L1 1
RWw10	01: Pc L2 1
RWw10	01: Fc_L2_1
RWw12	01: Dc L2 1
RWw13	01: SPNO.
RWw14	01: A1 L1 1
RWw15	01: A1_L1_1 01: A2 L1 1
RWw16	01: A3 L1 1
RWw17	01: A4 L1 1
RWw18	01: A5_L1_1
RWw19	01: A6 L1 1
RWw20	01: A7_L1_1
RWw21	01: A8 L1 1
RWw22	01: A0_E1_1
RWw23	01: A1_L2_1
RWw24	01: A3 L2 1
RWw25	01: A4_L2_1
RWw26	01: A5 L2 1
RWw27	01: A6 L2 1
RWw28	01: A7_L2_1
RWw29	01: A8_L2_1
RWw30	(Unused)
RWw31	(Unused)
RWw32	(Unused)
RWw33	(Unused)
RWw34	(Unused)
RWw35	(Unused)
RWw36	(Unused)
:	1 7
RWw63	(Unused)

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 3 (Ver.2.00, 2-station occupied x8 setting)

	IN area		
CC-Link slave (UTAdvanced) → CC-Link master			
Word position	Bit position	Contents of assignment	
	RX0	Receive data valid	
	RX1	During-write	
	RX2	Write acknowledgement	
	RX3	(Reserved)	
	RX4	(Reserved)	
	RX5	(Reserved)	
	RX6	(Reserved)	
	RX7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)	
	RX48	(Unused)	
	:		
	RX367	(Unused)	
	RX368	(Reserved)	
	:		
	RX379	Remote Ready	
	:		
	RX383	(Reserved)	
		(1.1000.100)	

OUT area CC-Link master → CC-Link slave (UTAdvanced)			
	aster → CC-Link slave (UTAdvanced)		
Word	Bit	Contents of assignment	
position			
	RY0	Rescan request	
	RY1	(Reserved)	
	RY2	Write request	
	RY3	(Reserved)	
	RY4	(Reserved)	
	RY5	(Reserved)	
	RY6	(Reserved)	
	RY7	(Reserved)	
	•	The fixed-part is omitted	
	•	·	
	•	(See profile number 0 on page 1)	
	RY47	Batch write request (address 32)	
	RY48	(Unused)	
	:		
	RY367	(Unused)	
	RY368	(Reserved)	
	RY379	(Reserved)	
	:		
	RY383	(Reserved)	

RWr0 Current page RWr1 01:STC RWr2 01:SSP_L1 RWr3 01:SSP_L2 RWr4 01:JC RWr5 01:WT.SW1 RWr6 01:WZ.UP1 RWr7 01:WZ.LO1 RWr8 01:WT.SW2 RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr2 01:SSP_L1 RWr3 01:SSP_L2 RWr4 01:JC RWr5 01:WT.SW1 RWr6 01:WZ.UP1 RWr7 01:WZ.LO1 RWr8 01:WT.SW2 RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr3 01:SSP_L2 RWr4 01:JC RWr5 01:WT.SW1 RWr6 01:WZ.UP1 RWr7 01:WZ.LO1 RWr8 01:WT.SW2 RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr4 01:JC RWr5 01:WT.SW1 RWr6 01:WZ.UP1 RWr7 01:WZ.LO1 RWr8 01:WT.SW2 RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr5 01:WT.SW1 RWr6 01:WZ.UP1 RWr7 01:WZ.LO1 RWr8 01:WT.SW2 RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr6 01:WZ.UP1 RWr7 01:WZ.LO1 RWr8 01:WT.SW2 RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr7 01:WZ.LO1 RWr8 01:WT.SW2 RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr8 01:WT.SW2 RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr9 01:WZ.UP2 RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr10 01:WZ.LO2 RWr11 01:R.CYC
RWr11 01:R.CYC
RWr12 01:TSP_L1_1
RWr13 01:TSP_L2_1
RWr14 01:TIME_1
RWr15 01:S.PID_L1_1
RWr16 01:S.PID_L2_1
RWr17 01:TSP_L1_2
RWr18 01:TSP_L2_2
RWr19 01:TIME_2
RWr20 01:S.PID_L1_2
RWr21 01:S.PID_L2_2
RWr22 01:TSP_L1_3
RWr23 01:TSP_L2_3
RWr24 01:TIME_3
RWr25 01:S.PID_L1_3
RWr26 01:S.PID_L2_3
RWr27 01:TSP_L1_4
RWr28 01:TSP_L2_4
RWr29 01:TIME_4
RWr30 01:S.PID_L1_4
RWr31 01:S.PID_L2_4
RWr32 01:TSP_L1_5
RWr33 01:TSP_L2_5
RWr34 01:TIME_5
RWr35 01:S.PID_L1_5
RWr36 01:S.PID_L2_5
RWr37 01:TSP_L1_6
RWr38 01:TSP_L2_6

RWw0	Page change request
RWw1	01:STC
RWw2	01:SSP_L1
RWw3	01:SSP_L2
RWw4	01:JC
RWw5	01:WT.SW1
RWw6	01:WZ.UP1
RWw7	01:WZ.LO1
RWw8	01:WT.SW2
RWw9	01:WZ.UP2
RWw10	01:WZ.LO2
RWw11	01:R.CYC
RWw12	01:TSP_L1_1
RWw13	01:TSP_L2_1
RWw14	01:TIME_1
RWw15	01:S.PID_L1_1
RWw16	01:S.PID_L2_1
RWw17	01:TSP_L1_2
RWw18	01:TSP_L2_2
RWw19	01:TIME_2
RWw20	01:S.PID_L1_2
RWw21	01:S.PID_L2_2
RWw22	01:TSP_L1_3
RWw23	01:TSP_L2_3
RWw24	01:TIME_3
RWw25	01:S.PID_L1_3
RWw26	01:S.PID_L2_3
RWw27	01:TSP_L1_4
RWw28	01:TSP_L2_4
RWw29	01:TIME_4
RWw30	01:S.PID_L1_4
RWw31	01:S.PID_L2_4
RWw32	01:TSP_L1_5
RWw33	01:TSP_L2_5
RWw34	01:TIME_5
RWw35	01:S.PID_L1_5
RWw36	01:S.PID_L2_5
RWw37	01:TSP_L1_6
RWw38	01:TSP_L2_6

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Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 3 (Ver.2.00, 2-station occupied x8 setting)

IN area			OUT area			
CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
RWr39		01:TIME_6		RWw39		01:TIME_6
RWr40		01:S.PID_L1_6		RWw40		01:S.PID_L1_6
RWr41		01:S.PID_L2_6		RWw41		01:S.PID_L2_6
RWr42		01:TSP_L1_7		RWw42		01:TSP_L1_7
RWr43		01:TSP_L2_7		RWw43		01:TSP_L2_7
RWr44		01:TIME_7		RWw44		01:TIME_7
RWr45		01:S.PID_L1_7		RWw45		01:S.PID_L1_7
RWr46		01:S.PID_L2_7		RWw46		01:S.PID_L2_7
RWr47		01:TSP_L1_8		RWw47		01:TSP_L1_8
RWr48		01:TSP_L2_8		RWw48		01:TSP_L2_8
RWr49		01:TIME_8		RWw49		01:TIME_8
RWr50		01:S.PID_L1_8		RWw50		01:S.PID_L1_8
RWr51		01:S.PID_L2_8		RWw51		01:S.PID_L2_8
RWr52		01:TSP_L1_9		RWw52		01:TSP_L1_9
RWr53		01:TSP_L2_9		RWw53		01:TSP_L2_9
RWr54		01:TIME_9		RWw54		01:TIME_9
RWr55		01:S.PID_L1_9		RWw55		01:S.PID_L1_9
RWr56		01:S.PID_L2_9		RWw56		01:S.PID_L2_9
RWr57		01:TSP_L1_10		RWw57		01:TSP_L1_10
RWr58		01:TSP_L2_10		RWw58		01:TSP_L2_10
RWr59		01:TIME_10		RWw59		01:TIME_10
RWr60		01:S.PID_L1_10		RWw60		01:S.PID_L1_10
RWr61		01:S.PID_L2_10		RWw61		01:S.PID_L2_10
RWr62		(Unused)		RWw62		(Unused)
RWr63		(Unused)		RWw63		(Unused)

Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 4 (Ver.2.00, 2-station occupied x8 setting)

IN area			
CC-Link slave (UTAdvanced) → CC-Link master			
Word position	Bit position	Contents of assignment	
	RX0	Receive data valid	
	RX1	During-write	
	RX2	Write acknowledgement	
	RX3	(Reserved)	
	RX4	(Reserved)	
	RX5	(Reserved)	
	RX6	(Reserved)	
	RX7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)	
	RX48	(Unused)	
	:		
	RX367	(Unused)	
	RX368	(Reserved)	
	•		
	RX379	Remote Ready	
	:		
	RX383	(Reserved)	

OUT area				
CC-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		
	RY0	Rescan request		
	RY1	(Reserved)		
	RY2	Write request		
	RY3	(Reserved)		
	RY4	(Reserved)		
	RY5	(Reserved)		
	RY6	(Reserved)		
	RY7	(Reserved)		
	•	The fixed-part is omitted		
	•	(See profile number 0 on page 1)		
	RY47	Batch write request (address 32)		
	RY48	(Unused)		
	:			
	RY367	(Unused)		
	RY368	(Reserved)		
	•••			
	RY379	(Reserved)		
	•••			
	RY383	(Reserved)		

RWr0	Current page
RWr1	01:TSP_L1_11
RWr2	01:TSP_L2_11
RWr3	01:TIME 11
RWr4	01:S.PID_L1_11
RWr5	01:S.PID_L2_11
RWr6	01:TSP_L1_12
RWr7	01:TSP_L2_12
RWr8	01:TIME_12
RWr9	01:S.PID_L1_12
RWr10	01:S.PID_L2_12
RWr11	01:TSP_L1_13
RWr12	01:TSP_L2_13
RWr13	01:TIME_13
RWr14	01:S.PID_L1_13
RWr15	01:S.PID_L2_13
RWr16	01:TSP_L1_14
RWr17	01:TSP_L2_14
RWr18	01:TIME_14
RWr19	01:S.PID_L1_14
RWr20	01:S.PID_L2_14
RWr21	01:TSP_L1_15
RWr22	01:TSP_L2_15
RWr23	01:TIME_15
RWr24	01:S.PID_L1_15
RWr25	01:S.PID_L2_15
RWr26	01:TSP_L1_16
RWr27	01:TSP_L2_16
RWr28	01:TIME_16
RWr29	01:S.PID_L1_16
RWr30	01:S.PID_L2_16
RWr31	01:TSP_L1_17
RWr32	01:TSP_L2_17
RWr33	01:TIME_17
RWr34	01:S.PID_L1_17
RWr35	01:S.PID_L2_17
RWr36	01:TSP_L1_18
RWr37	01:TSP_L2_18
RWr38	01:TIME_18

RWw0	Page change request
RWw1	01:TSP_L1_11
RWw2	01:TSP_L2_11
RWw3	01:TIME_11
RWw4	01:S.PID_L1_11
RWw5	01:S.PID_L2_11
RWw6	01:TSP_L1_12
RWw7	01:TSP_L2_12
RWw8	01:TIME_12
RWw9	01:S.PID_L1_12
RWw10	01:S.PID_L2_12
RWw11	01:TSP_L1_13
RWw12	01:TSP_L2_13
RWw13	01:TIME_13
RWw14	01:S.PID_L1_13
RWw15	01:S.PID_L2_13
RWw16	01:TSP_L1_14
RWw17	01:TSP_L2_14
RWw18	01:TIME_14
RWw19	01:S.PID_L1_14
RWw20	01:S.PID_L2_14
RWw21	01:TSP_L1_15
RWw22	01:TSP_L2_15
RWw23	01:TIME_15
RWw24	01:S.PID_L1_15
RWw25	01:S.PID_L2_15
RWw26	01:TSP_L1_16
RWw27	01:TSP_L2_16
RWw28	01:TIME_16
RWw29	01:S.PID_L1_16
RWw30	01:S.PID_L2_16
RWw31	01:TSP_L1_17
RWw32	01:TSP_L2_17
RWw33	01:TIME_17
RWw34	01:S.PID_L1_17
RWw35	01:S.PID_L2_17
RWw36	01:TSP_L1_18
RWw37	01:TSP_L2_18
RWw38	01:TIME_18

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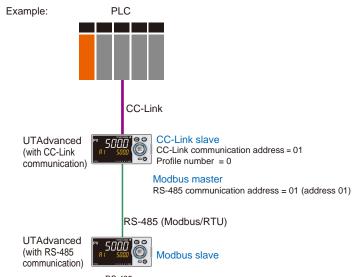
Profile number 25 (Simple PID control / Cascade control / Dual-loop control with 1 connected controller) on page 4 (Ver.2.00, 2-station occupied x8 setting)

IN area		OUT area				
CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
RWr39		01:S.PID_L1_18	RWw39		01:S.PID_L1_18	
RWr40		01:S.PID_L2_18	RWw40		01:S.PID_L2_18	
RWr41		01:TSP_L1_19	RWw41		01:TSP_L1_19	
RWr42		01:TSP_L2_19	RWw42		01:TSP_L2_19	
RWr43		01:TIME_19	RWw43		01:TIME_19	
RWr44		01:S.PID_L1_19	RWw44		01:S.PID_L1_19	
RWr45		01:S.PID_L2_19	RWw45		01:S.PID_L2_19	
RWr46		01:TSP_L1_20	RWw46		01:TSP_L1_20	
RWr47		01:TSP_L2_20	RWw47		01:TSP_L2_20	
RWr48		01:TIME_20	RWw48		01:TIME_20	
RWr49		01:S.PID_L1_20	RWw49		01:S.PID_L1_20	
RWr50		01:S.PID_L2_20	RWw50		01:S.PID_L2_20	
RWr51		(Unused)	RWw51		(Unused)	
:			:			
RWr63		(Unused)	RWw63		(Unused)	

4.9.4 Profile List for UM33A

UM33A

Profile number 0 (User profile [initial value: UM33A, 2 connected]) (Ver.1.10, 3-station occupied)



RS-485 communication address = 02 (address 02)

Page 1

Profile	Profile number 0 (User profile [initial value: UM33A, 2 connected]) on page 1 (Ver.1.10, 3-station occupie						
	IN area					OUT area	
С	CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvanced)			
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment	
	RX0	Receive data valid			RY0	Rescan request	
	RX1	During-write			RY1	(Reserved)	
	RX2	Write acknowledgement			RY2	Write request	
	RX3	(Reserved)			RY3	(Reserved)	
	RX4	(Reserved)			RY4	(Reserved)	
	RX5	(Reserved)			RY5	(Reserved)	
	RX6	(Reserved)			RY6	(Reserved)	
	RX7	(Reserved)			RY7	(Reserved)	
	RX8	(Reserved)			RY8	(Reserved)	
	RX9	(Reserved)			RY9	(Reserved)	
	RX10	(Reserved)			RY10	(Reserved)	
	RX11	(Reserved)			RY11	(Reserved)	
	RX12	(Reserved)			RY12	(Reserved)	
	RX13	(Reserved)			RY13	(Reserved)	
	RX14	(Reserved)			RY14	(Reserved)	
	RX15	(Reserved)			RY15	(Reserved)	
	RX16	Normal connection slave (address 01)			RY16	Batch write request (address 01)	
	RX17	Normal connection slave (address 02)			RY17	Batch write request (address 02)	
	RX18	Normal connection slave (address 03)			RY18	Batch write request (address 03)	
	RX19	Normal connection slave (address 04)			RY19	Batch write request (address 04)	
	RX20	Normal connection slave (address 05)			RY20	Batch write request (address 05)	
	RX21	Normal connection slave (address 06)			RY21	Batch write request (address 06)	
	RX22	Normal connection slave (address 07)			RY22	Batch write request (address 07)	
	RX23	Normal connection slave (address 08)			RY23	Batch write request (address 08)	
	RX24	Normal connection slave (address 09)			RY24	Batch write request (address 09)	
	RX25	Normal connection slave (address 10)			RY25	Batch write request (address 10)	
	RX26	Normal connection slave (address 11)			RY26	Batch write request (address 11)	
	RX27	Normal connection slave (address 12)			RY27	Batch write request (address 12)	
	RX28	Normal connection slave (address 13)			RY28	Batch write request (address 13)	
	RX29	Normal connection slave (address 14)			RY29	Batch write request (address 14)	
	RX30	Normal connection slave (address 15)			RY30	Batch write request (address 15)	
	RX31	Normal connection slave (address 16)			RY31	Batch write request (address 16)	
	RX32	Normal connection slave (address 17)			RY32	Batch write request (address 17)	
	RX33	Normal connection slave (address 18)			RY33	Batch write request (address 18)	
	RX34	Normal connection slave (address 19)			RY34	Batch write request (address 19)	

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(Ver.1.10, 3-station occupied)

Prome	numbe	r U (User profile [initial value: UM33A, 2 (connected	J) on pa	•
		IN area			OUT area
Word		ave (UTAdvanced) → CC-Link master	Word		aster → CC-Link slave (UTAdvanced)
	Bit position	Contents of assignment		Bit position	Contents of assignment
		Normal connection slave (address 20)		RY35	Batch write request (address 20)
		Normal connection slave (address 21)		RY36	Batch write request (address 21)
	RX37	Normal connection slave (address 22)		RY37	Batch write request (address 22)
	RX38	Normal connection slave (address 23)		RY38	Batch write request (address 23)
	RX39	Normal connection slave (address 24)		RY39	Batch write request (address 24)
		Normal connection slave (address 25)		RY40	Batch write request (address 25)
		Normal connection slave (address 26)		RY41	Batch write request (address 26)
		Normal connection slave (address 27)		RY42	Batch write request (address 27)
		Normal connection slave (address 28)		RY43	Batch write request (address 28)
		Normal connection slave (address 29)		RY44	Batch write request (address 29)
	_	Normal connection slave (address 30)		RY45	Batch write request (address 30)
		Normal connection slave (address 31)		RY46	Batch write request (address 31)
		Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	01: ALM1_L1		RY48	01: A.M_L1
	RX49	01: ALM2_L1		RY49	01: R.L_L1
	RX50	01: ALM3_L1		RY50	01: S.R_L1
		01: ALM4_L1		RY51	(Unused)
	-	01: ALM5_L1		RY52	(Unused)
	RX53	01: ALM6_L1		RY53	(Unused)
		01: ALM7_L1		RY54	(Unused)
	RX55	01: ALM8_L1		RY55	(Unused)
	RX56	(Unused)		RY56	(Unused)
	RX57	(Unused)		RY57	(Unused)
	RX58	(Unused)		RY58	(Unused)
	RX59	(Unused)		RY59	(Unused)
	-	(Unused)		RY60	(Unused)
	_	(Unused)		RY61	(Unused)
	RX62	(Unused)		RY62	(Unused)
		(Unused)		RY63	(Unused)
		02: ALM1_L1		RY64	02: A.M_L1
	_	02: ALM2_L1		RY65	02: R.L_L1
	RX66	02: ALM3_L1		RY66	02: S.R_L1
		02: ALM4_L1		RY67	(Unused)
		02: ALM5_L1		RY68	(Unused)
	RX69	02: ALM6_L1		RY69	(Unused)
		02: ALM7_L1		RY70	(Unused)
		02: ALM8_L1		RY71	(Unused)
		(Unused)		RY72	(Unused)
	RX73	(Unused)		RY73	(Unused)
	_	(Unused)		RY74	(Unused)
	RX75	(Unused)		RY75	(Unused)
	RX76	(Unused)		RY76	(Unused)
		(Unused)		RY77	(Unused)
	_	(Unused)		RY78	(Unused)
		(Unused)		RY79	(Unused)
	-	(Reserved)		RY80	(Reserved)
	:			:	
	RX91	Remote Ready		RY91	(Reserved)
	:			:	
	RX95	(Reserved)		RY95	(Reserved)
		(1.1.00	(

Profile number 0 (User profile [initial value: UM33A, 2 connected]) on page 1

RWr0	Current page
RWr1	01: PV_L1
RWr2	02: PV_L1
RWr3	01: PEAK
RWr4	02: PEAK
RWr5	01: BOTM
RWr6	02: BOTM
RWr7	(Unused)
RWr8	(Unused)
RWr9	(Unused)
RWr10	(Unused)
RWr11	(Unused)

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	(Unused)
RWw5	(Unused)
RWw6	(Unused)
RWw7	(Unused)
RWw8	(Unused)
RWw9	(Unused)
RWw10	(Unused)
RWw11	(Unused)

Profile	numbe	r 0 (User profile [initial value: UM33A, 2	connected	J) on pa	• • • • • • • • • • • • • • • • • • • •	
_		IN area			OUT area	
	CC-Link slave (UTAdvanced) → CC-Link master			CC-Link master → CC-Link slave (UTAdvan		
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment	
	RX0	Receive data valid		RY0	Rescan request	
	RX1	During-write		RY1	(Reserved)	
	RX2	Write acknowledgement		RY2	Write request	
	RX3	(Reserved)		RY3	(Reserved)	
	RX4	(Reserved)		RY4	(Reserved)	
	RX5	(Reserved)		RY5	(Reserved)	
	RX6	(Reserved)		RY6	(Reserved)	
	RX7	(Reserved)		RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)	
	RX48	(Unused)			(Unused)	
	:			:		
	RX79	(Unused)			(Unused)	
	RX80	(Reserved)		RY80	(Reserved)	
	:			:		
	RX91	Remote Ready		RY91	(Reserved)	
	:			:		
	RX95	(Reserved)		RY95	(Reserved)	
					•	
RWr0		Current page	RWw0		Page change request	
RWr1		01: A1_L1	RWw1		01: A1_L1	
RWr2		02: A1_L1	RWw2		02: A1_L1	
RWr3		01: A2_L1	RWw3		01: A2_L1	
RWr4		02: A2_L1	RWw4		02: A2_L1	
RWr5		01: A3_L1	RWw5		01: A3_L1	
RWr6		02: A3_L1	RWw6		02: A3_L1	
RWr7		01: A4_L1	RWw7		01: A4_L1	
RWr8		02: A4_L1	RWw8		02: A4_L1	
RWr9		(Unused)	RWw9		(Unused)	
RWr10		(Unused)	RWw10		(Unused)	
RWr11		(Unused)	RWw11		(Unused)	

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Profile	Profile number 0 (User profile [initial value: UM33A, 2 connected]) on page 3 (Ver.1.10, 3-station occupied							
IN area					OUT area			
	CC-Link slave (UTAdvanced) → CC-Link master CC			C-Link master → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
	RX0	Receive data valid	1 [RY0	Rescan request		
	RX1	During-write	1 [RY1	(Reserved)		
	RX2	Write acknowledgement			RY2	Write request		
	RX3	(Reserved)	1 [RY3	(Reserved)		
	RX4	(Reserved)	1 [RY4	(Reserved)		
	RX5	(Reserved)			RY5	(Reserved)		
	RX6	(Reserved)			RY6	(Reserved)		
	RX7	(Reserved)	1 [RY7	(Reserved)		
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)		
	RX47	Normal connection slave (address 32)	1 1		RY47	Batch write request (address 32)		
	RX48	(Unused)	1 [(Unused)		
	:		1		:			
	RX79	(Unused)	1 1			(Unused)		
	RX80	(Reserved)	1 1		RY80	(Reserved)		
	:				:			
	RX91	Remote Ready	1 [RY91	(Reserved)		
	:		1 [:			
	RX95	(Reserved)			RY95	(Reserved)		
RWr0		Current page	1 [RWw0		Page change request		
RWr1		01: A5 L1	1 1	RWw1		01: A5 L1		
RWr2		02: A5 L1	1	RWw2		02: A5 L1		
RWr3		01: A6 L1	1	RWw3		01: A6 L1		
RWr4		02: A6_L1	1	RWw4		02: A6_L1		
RWr5		01: A7_L1	1	RWw5		01: A7_L1		
RWr6		02: A7_L1	1	RWw6		02: A7_L1		
RWr7		01: A8_L1	1	RWw7		01: A8_L1		
RWr8		02: A8_L1	1	RWw8		02: A8_L1		
RWr9		(Unused)	1	RWw9		(Unused)		
RWr10		(Unused)	1	RWw10		(Unused)		
RWr11		(Unused)] [RWw11		(Unused)		

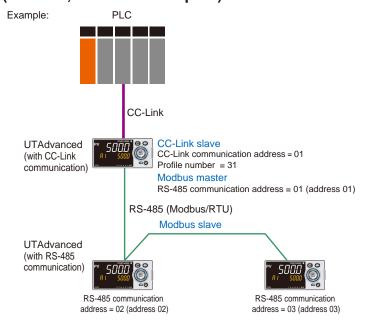
FIOIIIE	Hullibe	r 0 (User profile [initial value: UM33A, 2	Connected	j) on pa	· · · · · · · · · · · · · · · · · · ·	
0	C I interal			C I ink m	OUT area	
Word	Bit	ave (UTAdvanced) → CC-Link master	Word	Bit	k master → CC-Link slave (UTAdvanced)	
	position	Contents of assignment		position	Contents of assignment	
	RX0	Receive data valid		RY0	Rescan request	
	RX1	During-write		RY1	(Reserved)	
	RX2	Write acknowledgement		RY2	Write request	
	RX3	(Reserved)		RY3	(Reserved)	
	RX4	(Reserved)		RY4	(Reserved)	
	RX5	(Reserved)		RY5	(Reserved)	
	RX6	(Reserved)		RY6	(Reserved)	
	RX7	(Reserved)		RY7	(Reserved)	
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)	
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)	
	RX48	(Unused)		RY48	(Unused)	
	:			:		
	RX79	(Unused)		RY79	(Unused)	
	RX80	(Reserved)		RY80	(Reserved)	
	:			:		
	RX91	Remote Ready		RY91	(Reserved)	
	:			:		
	RX95	(Reserved)		RY95	(Reserved)	
					•	
RWr0		Current page	RWw0		Page change request	
RWr1		(Unused)	RWw1		(Unused)	
RWr2		(Unused)	RWw2		(Unused)	
RWr3		(Unused)	RWw3		(Unused)	
RWr4		(Unused)	RWw4		(Unused)	
RWr5		(Unused)	RWw5		(Unused)	
RWr6		(Unused)	RWw6		(Unused)	
RWr7		(Unused)	RWw7		(Unused)	
RWr8		(Unused)	RWw8		(Unused)	
RWr9		(Unused)	RWw9		(Unused)	
RWr10		(Unused)	RWw10		(Unused)	
RWr11		(Unused)	RWw11		(Unused)	

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Profile number 31 (User profile [initial value: UM33A, 3 connected]) (Ver.1.10, 4-station occupied)





Page 1

C	C-l ink el	IN area ave (UTAdvanced) → CC-Link master		OUT area CC-Link master → CC-Link slave (UTAdvanced				
Word	Bit position	Contents of assignment	Word	Bit	Contents of assignment			
OSILIOII	RX0	Receive data valid	position	RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)			
		Normal connection slave (address 32)		RY47	Batch write request (address 32)			
		01: ALM1_L1		RY48	01: A.M_L1			
		01: ALM2_L1		RY49	01: R.L_L1			
	RX50	01: ALM3_L1		RY50	01: S.R_L1			
		01: ALM4_L1		RY51	(Unused)			
		01: ALM5_L1		RY52	(Unused)			
		01: ALM6_L1		RY53	(Unused)			
		01: ALM7_L1		RY54	(Unused)			
		01: ALM8_L1		RY55	(Unused)			
		(Unused)		RY56	(Unused)			
	RX57	(Unused)		RY57	(Unused)			
		(Unused)		RY58	(Unused)			
	RX59	(Unused)		RY59	(Unused)			
	RX60	(Unused)		RY60	(Unused)			
	RX61	(Unused)		RY61	(Unused)			
	RX62	(Unused)		RY62	(Unused)			
		(Unused)		RY63	(Unused)			
		02: ALM1_L1		RY64	02: A.M_L1			
		02: ALM2_L1		RY65	02: R.L_L1			
		02: ALM3_L1		RY66	02: S.R_L1			
	RX67	02: ALM4_L1		RY67	(Unused)			
		02: ALM5_L1		RY68	(Unused)			
		02: ALM6_L1		RY69	(Unused)			
		02: ALM7_L1		RY70	(Unused)			
	RX71	02: ALM8_L1		RY71	(Unused)			

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		IN area		d]) on pa	OUT area				
C	C-I ink si	ave (UTAdvanced) → CC-Link master		CC-Link master → CC-Link slave (UTAdvanced					
Word	Bit	Contents of assignment	Word	Bit	Contents of assignment				
osition	position		position	position					
		(Unused)	_	RY72	(Unused)				
		(Unused)	_	RY73	(Unused)				
		(Unused)	-	RY74	(Unused)				
		(Unused)	_	RY75	(Unused)				
		(Unused)	_	RY76	(Unused)				
		(Unused)		RY77	(Unused)				
	_	(Unused)		RY78	(Unused)				
		(Unused)		RY79	(Unused)				
		03:ALM1_BIT_L1		RY80	(Unused)				
		03:ALM2_BIT_L1		RY81	(Unused)				
		03:ALM3_BIT_L1	-	RY82	(Unused)				
		03:ALM4_BIT_L1		RY83	(Unused)				
		03:ALM5_BIT_L1	+	RY84	(Unused)				
		03:ALM6_BIT_L1	$+$ \vdash	RY85	(Unused)				
		03:ALM7_BIT_L1		RY86	(Unused)				
		03:ALM8_BIT_L1		RY87	(Unused)				
		(Unused)	_	RY88	(Unused)				
		(Unused)		RY89	(Unused)				
		(Unused)		RY90	(Unused)				
		(Unused)	-	RY91	(Unused)				
		(Unused)	_	RY92	(Unused)				
		(Unused)	_	RY93	(Unused)				
		(Unused)	_	RY94	(Unused)				
		(Unused)	-	RY95	(Unused)				
	RX93	(Unused)		RY96	(Unused)				
	:			:					
	RX111	(Unused)		RY111	(Unused)				
	RX112	(Reserved)		RY112	(Reserved)				
	:			:					
	RX123	Remote Ready		RY123	(Reserved)				
	:	Tromoto rroddy		:	(110001100)				
	•	(D		•	100				
	RX127	(Reserved)		RY127	(Reserved)				
RWr0		Current page	RWw0		Page change request				
RWr1		01:PV_L1	RWw1		(Unused)				
RWr2		02:PV L1	RWw2		(Unused)				
RWr3		03:PV L1	RWw3		(Unused)				
RWr4		01:PEAK_L1	RWw4		(Unused)				
RWr5		02:PEAK_L1	RWw5		(Unused)				
RWr6		03:PEAK_L1	RWw6		(Unused)				
RWr7		01:BOTM L1	RWw7	1	(Unused)				
RWr8		02:BOTM_L1	RWw8		(Unused)				
RWr9		03:BOTM_L1	RWw9		(Unused)				
Wr10		(Unused)	RWw10		(Unused)				
RWr11			RWw10	+					
Wr12		(Unused)	RWw11	_	(Unused)				
Wr13		(Unused)	RWw12	+	(Unused)				
Wr14		(Unused)	RWw13	+	(Unused)				
Wr14 Wr15		(Unused)	RWw14	+	(Unused)				

С	C-Link sl	IN area ave (UTAdvanced) → CC-Link master		CC-Link m	OUT area naster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word positio	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
		The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX111	(Unused)		RY111	(Unused)
	RX112	(Reserved)		RY112	(Reserved)
	:			1:	
	RX123	Remote Ready			(Reserved)
	:			:	
	RX127	(Reserved)		-	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01: A1_L1	RWw1		01: A1_L1
RWr2		02: A1_L1	RWw2		02: A1_L1
RWr3		03: A1_L1	RWw3		03: A1_L1
RWr4		01: A2_L1	RWw4		01: A2_L1
RWr5		02: A2_L1	RWw5		02: A2_L1
RWr6		03: A2_L1	RWw6		03: A2_L1
RWr7		01: A3_L1	RWw7		01: A3_L1
RWr8		02: A3_L1	RWw8		02: A3_L1
RWr9		03: A3_L1	RWw9		03: A3_L1
RWr10		01: A4_L1	RWw1)	01: A4_L1
RWr11		02: A4_L1	RWw1		02: A4_L1
RWr12		03: A4_L1	RWw1	2	03: A4_L1
RWr13		(Unused)	RWw1	3	(Unused)
RWr14		(Unused)	RWw1	ļ l	(Unused)
RWr15		(Unused)	RWw1	5	(Unused)

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Profile	numbe	r 31 (User profile [initial value: UM33A	, з connecte	aj) on p	<u> </u>				
		IN area		OUT area					
		ave (UTAdvanced) → CC-Link master			naster → CC-Link slave (UTAdvanced)				
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment				
	RX0	Receive data valid		RY0	Rescan request				
	RX1	During-write		RY1	(Reserved)				
	RX2	Write acknowledgement		RY2	Write request				
	RX3	(Reserved)		RY3	(Reserved)				
	RX4	(Reserved)		RY4	(Reserved)				
	RX5	(Reserved)		RY5	(Reserved)				
	RX6	(Reserved)		RY6	(Reserved)				
	RX7	(Reserved)		RY7	(Reserved)				
	•	The fixed-part is omitted			The fixed-part is omitted				
	•	(See profile number 0 on page 1)		•	(See profile number 0 on page 1)				
	•			•					
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)				
	RX48	(Unused)		RY48	(Unused)				
	:			:					
	RX111	(Unused)		RY111	(Unused)				
	RX112	(Reserved)		RY112	(Reserved)				
	RX123	Remote Ready			(Reserved)				
	:			:					
	RX127	(Reserved)		RY127	(Reserved)				
RWr0		Current page	RWw0		Page change request				
RWr1		01: A5_L1	RWw1		01: A5_L1				
RWr2		02: A5_L1	RWw2		02: A5_L1				
RWr3		03: A5_L1	RWw3		03: A5_L1				
RWr4		01: A6_L1	RWw4		01: A6_L1				
RWr5		02: A6_L1	RWw5		02: A6_L1				
RWr6		03: A6_L1	RWw6		03: A6_L1				
RWr7		01: A7_L1	RWw7		01: A7_L1				
RWr8		02: A7_L1	RWw8		02: A7_L1				
RWr9		03: A7_L1	RWw9		03: A7_L1				
RWr10		01: A8_L1	RWw10		01: A8_L1				
RWr11		02: A8_L1	RWw11		02: A8_L1				
RWr12		03: A8_L1	RWw12		03: A8_L1				
RWr13		(Unused)	RWw13		(Unused)				
RWr14		(Unused)	RWw14		(Unused)				
RWr15		(Unused)	RWw15		(Unused)				

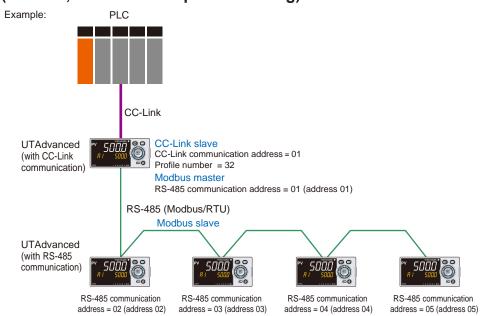
	IN area			OUT area					
Word	C-Link sl	ave (UTAdvanced) → CC-Link master	Word		E-Link master → CC-Link slave (UTAdvanced) Bit				
	position	Contents of assignment		position	Contents of assignment				
	RX0	Receive data valid		RY0	Rescan request				
	RX1	During-write		RY1	(Reserved)				
	RX2	Write acknowledgement		RY2	Write request				
	RX3	(Reserved)		RY3	(Reserved)				
	RX4	(Reserved)		RY4	(Reserved)				
	RX5	(Reserved)		RY5	(Reserved)				
	RX6	(Reserved)		RY6	(Reserved)				
	RX7	(Reserved)		RY7	(Reserved)				
	•	The fixed-part is omitted		•	The fixed-part is omitted				
	•	(See profile number 0 on page 1)		•	(See profile number 0 on page 1)				
	•			•	, , , , , , , , , , , , , , , , , , ,				
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)				
	RX48	(Unused)		RY48	(Unused)				
	:			:					
	RX111	(Unused)	1	RY111	(Unused)				
	RX112	(Reserved)		RY112	(Reserved)				
	:			:					
	RX123	Remote Ready		RY123	(Reserved)				
	:			:					
	RX127	(Reserved)		RY127	(Reserved)				
RWr0		Current page	RWw0		Page change request				
RWr1		(Unused)	RWw1		(Unused)				
RWr2		(Unused)	RWw2		(Unused)				
RWr3		(Unused)	RWw3		(Unused)				
RWr4		(Unused)	RWw4		(Unused)				
RWr5		(Unused)	RWw5		(Unused)				
RWr6		(Unused)	RWw6		(Unused)				
RWr7		(Unused)	RWw7		(Unused)				
RWr8		(Unused)	RWw8		(Unused)				
RWr9		(Unused)	RWw9		(Unused)				
RWr10		(Unused)	RWw10		(Unused)				
RWr11		(Unused)	RWw11		(Unused)				
RWr12		(Unused)	RWw12		(Unused)				
RWr13		(Unused)	RWw13		(Unused)				
RWr14		(Unused)	RWw14		(Unused)				
RWr15		(Unused)	RWw15		(Unused)				

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Intentionally blank

Profile number 32 (User profile [initial value: UM33A, 5 connected]) (Ver.2.00, 1-station occupied x8 setting)





Page 1

Prome	number	32 (User profile [initial value: UM33A, 5 c	onne	ecteal) o	n page 1			
		IN area		OUT area CC-Link master → CC-Link slave (UTAdvanced)				
		ave (UTAdvanced) → CC-Link master						
Word osition	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment		
	RX0	Receive data valid			RY0	Rescan request		
	RX1	During-write			RY1	(Reserved)		
	RX2	Write acknowledgement			RY2	Write request		
	RX3	(Reserved)	1		RY3	(Reserved)		
	RX4	(Reserved)	1		RY4	(Reserved)		
	RX5	(Reserved)	1		RY5	(Reserved)		
	RX6	(Reserved)	1		RY6	(Reserved)		
	RX7	(Reserved)	1		RY7	(Reserved)		
		The fixed-part is omitted (See profile number 0 on page 1)				The fixed-part is omitted (See profile number 0 on page 1)		
	RX47	Normal connection slave (address 32)	1		RY47	Batch write request (address 32)		
	RX48	01: ALM1_BIT_L1	1		RY48	(Unused)		
		01: ALM2 BIT L1	1		RY49	(Unused)		
	RX50	01: ALM3_BIT_L1	1		RY50	(Unused)		
	RX51	01: ALM4_BIT_L1	1		RY51	(Unused)		
		01: ALM5_BIT_L1	1		RY52	(Unused)		
		01: ALM6_BIT_L1	1		RY53	(Unused)		
	RX54	01: ALM7_BIT_L1	1		RY54	(Unused)		
	RX55	01: ALM8 BIT L1	1		RY55	(Unused)		
	RX56	02: ALM1 BIT L1	1		RY56	(Unused)		
		02: ALM2_BIT_L1	1		RY57	(Unused)		
		02: ALM3_BIT_L1	1		RY58	(Unused)		
		02: ALM4_BIT_L1	1		RY59	(Unused)		
		02: ALM5_BIT_L1	1		RY60	(Unused)		
		02: ALM6_BIT_L1	1		RY61	(Unused)		
	RX62	02: ALM7_BIT_L1	1		RY62	(Unused)		
		02: ALM8_BIT_L1	1		RY63	(Unused)		
		03: ALM1_BIT_L1	1		RY64	(Unused)		
		03: ALM2_BIT_L1	1		RY65	(Unused)		
		03: ALM3_BIT_L1	1		RY66	(Unused)		
	RX67	03: ALM4_BIT_L1	1		RY67	(Unused)		
	RX68	03: ALM5_BIT_L1	1		RY68	(Unused)		
		03: ALM6_BIT_L1	1		RY69	(Unused)		
		03: ALM7_BIT_L1	1		RY70	(Unused)		
	RX71	03: ALM8 BIT L1	1		RY71	(Unused)		

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0	0 !	IN area		OUT area CC-Link master → CC-Link slave (UTAdvanced)				
Word	Bit	ave (UTAdvanced) → CC-Link master Contents of assignment	Word	Bit	Contents of assignment			
position	position		position	position	ŭ .			
		04: ALM1_BIT_L1	_	_	(Unused)			
	-	04: ALM2_BIT_L1		RY73	(Unused)			
	RX74	04: ALM3_BIT_L1	_	RY74	(Unused)			
		04: ALM4_BIT_L1		RY75	(Unused)			
		04: ALM5_BIT_L1		RY76	(Unused)			
		04: ALM6_BIT_L1		RY77	(Unused)			
		04: ALM7_BIT_L1		RY78	(Unused)			
	RX79	1		RY79	(Unused)			
	RX80	05: ALM1_BIT_L1		RY80	(Unused)			
	RX81	05: ALM2_BIT_L1	」	RY81	(Unused)			
	RX82	05: ALM3_BIT_L1		RY82	(Unused)			
	RX83	05: ALM4_BIT_L1		RY83	(Unused)			
	RX84	05: ALM5_BIT_L1		RY84	(Unused)			
	RX85	05: ALM6_BIT_L1		RY85	(Unused)			
	RX86	05: ALM7_BIT_L1		RY86	(Unused)			
	RX87	05: ALM8_BIT_L1		RY87	(Unused)			
	RX88	(Unused)		RY88	(Unused)			
	:			:				
	RX111	(Unused)		RY111	(Unused)			
	RX112	(Reserved)		RY112	(Reserved)			
	:			:				
	RX123	Remote Ready		RY123	(Reserved)			
	:			:				
	RX127	(Reserved)		RY127	(Reserved)			
			_					
RWr0		Current page	RWw0		Page change request			
RWr1		01:PV_L1	RWw1		(Unused)			
RWr2		02:PV_L1	RWw2		(Unused)			
RWr3		03:PV_L1	RWw3		(Unused)			
RWr4		01:PEAK_L1	RWw4		(Unused)			
RWr5		02:PEAK_L1	RWw5		(Unused)			
RWr6		03:PEAK_L1	RWw6		(Unused)			
RWr7		01:BOTM_L1	RWw7		(Unused)			
RWr8		02:BOTM_L1	RWw8		(Unused)			
RWr9		03:BOTM_L1	RWw9		(Unused)			
RWr10		(Unused)	RWw10		(Unused)			
RWr11		(Unused)	RWw11		(Unused)			
RWr12		(Unused)	RWw12		(Unused)			
D14/-40		i` '	DW440	 	İ.,			

RWw13

RWw14

RWw15

(Unused)

(Unused)

(Unused)

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(Unused)

(Unused)

(Unused)

RWr13 RWr14

RWr15

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	C-Link sl	IN area ave (UTAdvanced) → CC-Link master			nk m	OUT area aster → CC-Link slave (UTAdvanced)
Word	Bit position	Contents of assignment	Word	n posi		Contents of assignment
OSILIOII	RX0	Receive data valid	positio	R)		Rescan request
	RX1	During-write		R		(Reserved)
	RX2	Write acknowledgement		R۱	Y2	Write request
	RX3	(Reserved)		R۱	Y3	(Reserved)
	RX4	(Reserved)		R۱	Y4	(Reserved)
	RX5	(Reserved)		R۱	Y5	(Reserved)
	RX6	(Reserved)		R۱	Y6	(Reserved)
	RX7	(Reserved)		R۱	Y7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY	′47	Batch write request (address 32)
	RX48	(Unused)		RY	′48	(Unused)
	:				:	
	<u> </u>	(Unused)	1	RY		(Unused)
		(Reserved)				(Reserved)
	:	(1.100.100)				(
	DV100	Pamata Paady				(Reserved)
	RX123	Remote Ready		RY		(INESETVEU)
	•		_			
	RX127	(Reserved)		RY	127	(Reserved)
RWr0		Current page	RWw)		Page change request
RWr1		01: A1_L1	RWw	1		01: A1_L1
RWr2		02: A1_L1	RWw	_		02: A1_L1
RWr3		03: A1_L1	RWw:	_		03: A1_L1
RWr4		04: A1_L1	RWw	_		04: A1_L1
RWr5		05: A1_L1	RWw			05: A1_L1
RWr6		01: A2_L1	RWw	_		01: A2_L1
RWr7	-	02: A2_L1	RWw			02: A2_L1
RWr8		03: A2_L1	RWw			03: A2_L1
RWr9		04: A2_L1	RWw!			04: A2_L1
RWr10		05: A2_L1	RWw1			05: A2_L1
RWr11 RWr12	-	01: A3_L1	RWw1			01: A3_L1
RWr13	-	02: A3_L1	RWw1			02: A3_L1
RWr14		03: A3_L1 04: A3_L1	RWw1	-		03: A3_L1 04: A3 L1
RWr15	 	05: A3_L1	RWw1			05: A3_L1
RWr16		01: A4_L1	RWw1	-		01: A4_L1
RWr17		02: A4_L1	RWw1	-		02: A4_L1
RWr18		03: A4 L1	RWw1			03: A4_L1
RWr19		04: A4_L1	RWw1			04: A4_L1
RWr20	İ	05: A4_L1	RWw2	_		05: A4_L1
RWr21		(Unused)	RWw2			(Unused)
RWr22		(Unused)	RWw2	_		(Unused)
RWr23		(Unused)	RWw2	3		(Unused)
RWr24		(Unused)	RWw2	4		(Unused)
RWr25		(Unused)	RWw2	5		(Unused)
RWr26		(Unused)	RWw2	6		(Unused)
RWr27		(Unused)	RWw2	7		(Unused)
RWr28		(Unused)	RWw2	8		(Unused)
RWr29		(Unused)	RWw2	9		(Unused)
RWr30		(Unused)	RWw3	0		(Unused)
RWr31		(Unused)	RWw3	1		(Unused)

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RWr18

RWr19

RWr20

RWr21

RWr22

RWr23

RWr24

RWr25

RWr26

RWr27

RWr28

RWr29

RWr30

RWr31

03: A8_L1

04: A8_L1 05: A8_L1

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

	C-Link sl	IN area ave (UTAdvanced) → CC-Link master				OUT area $ \textbf{aster} \rightarrow \textbf{CC-Link slave (UTAdvanced)} $
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	•	The fixed-part is omitted			•	The fixed-part is omitted
	•	(See profile number 0 on page 1)			•	(See profile number 0 on page 1
	•	, , , , , , , , , , , , , , , , , , , ,			•	, ,
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	:				:	
	RX111	(Unused)			RY111	(Unused)
	RX112	(Reserved)			RY112	(Reserved)
	:				:	
	RX123	Remote Ready			RY123	(Reserved)
	:				:	
	RX127	(Reserved)			RY127	(Reserved)
RWr0		Current page		RWw0		Page change request
RWr1		01: A5_L1	1	RWw1		01: A5_L1
RWr2		02: A5_L1	1	RWw2		02: A5_L1
RWr3		03: A5 L1	1	RWw3		03: A5 L1
RWr4		04: A5 L1	1	RWw4		04: A5_L1
RWr5		05: A5 L1	1	RWw5		05: A5_L1
RWr6		01: A6 L1	1	RWw6		01: A6_L1
RWr7		02: A6 L1	1	RWw7		02: A6 L1
RWr8		03: A6 L1	1	RWw8		03: A6 L1
RWr9		04: A6_L1	1	RWw9		04: A6_L1
RWr10		05: A6_L1	1	RWw10		05: A6_L1
RWr11		01: A7_L1	1	RWw11		01: A7_L1
RWr12		02: A7_L1	1	RWw12		02: A7_L1
RWr13		03: A7_L1	1	RWw13		03: A7_L1
RWr14		04: A7_L1	1	RWw14		04: A7 L1
RWr15		05: A7 L1	1	RWw15		05: A7_L1
RWr16		01: A8 L1	1	RWw16		01: A8_L1
RWr17		02: A8 L1	\top	RWw17		02: A8 L1
		- -	+			

RWw18

RWw19

RWw20

RWw21

RWw22

RWw23

RWw24

RWw25

RWw26

RWw27

RWw28

RWw29

RWw30

RWw31

03: A8_L1

04: A8_L1 05: A8_L1

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

(Unused)

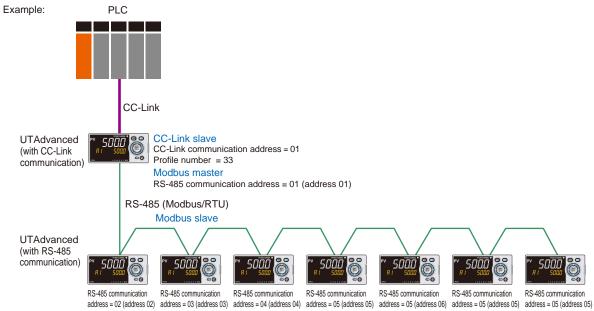
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		IN area		OUT area					
	CC-Link slave (UTAdvanced) → CC-Link master				CC-Link master → CC-Link slave (UTAdvanced)				
Word osition	Bit position	Contents of assignment	Wo posi		Bit position	Contents of assignment			
	RX0	Receive data valid			RY0	Rescan request			
	RX1	During-write			RY1	(Reserved)			
	RX2	Write acknowledgement			RY2	Write request			
	RX3	(Reserved)			RY3	(Reserved)			
	RX4	(Reserved)			RY4	(Reserved)			
	RX5	(Reserved)			RY5	(Reserved)			
	RX6	(Reserved)			RY6	(Reserved)			
	RX7	(Reserved)			RY7	(Reserved)			
		The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)			
	PV47		-		PV47	Datab write request (address 32)			
	RX47	Normal connection slave (address 32)	-		RY47	Batch write request (address 32)			
	RX48	(Unused)	┨		RY48	(Unused)			
	:				:				
	RX111	(Unused)	7		RY111	(Unused)			
	RX112	(Reserved)			RY112	(Reserved)			
	:	,			:				
	-	Remote Ready			PV122	(Reserved)			
	KA123	Remote Ready			•	(Reserved)			
	:				:				
	RX127	(Reserved)			RY127	(Reserved)			
RWr0		Current page	RW	w0		Page change request			
RWr1		(Unused)	RW	w1		(Unused)			
RWr2		(Unused)	RW	w2		(Unused)			
RWr3		(Unused)	RW	w3		(Unused)			
RWr4		(Unused)	RW	w4		(Unused)			
RWr5		(Unused)	RW	w5		(Unused)			
RWr6		(Unused)	RW	w6		(Unused)			
RWr7		(Unused)	RW	w7		(Unused)			
RWr8		(Unused)	RW	w8		(Unused)			
RWr9		(Unused)	RW	_		(Unused)			
RWr10		(Unused)	RW	_		(Unused)			
RWr11		(Unused)	RW			(Unused)			
RWr12		(Unused)	RW			(Unused)			
RWr13			RW						
RWr14	+	(Unused)	RW			(Unused)			
RWr15	-	(Unused)	RW	_		(Unused)			
	-	(Unused)		_		(Unused)			
RWr16	-	(Unused)	RW			(Unused)			
RWr17	-	(Unused)	RW			(Unused)			
RWr18	-	(Unused)	RW	_		(Unused)			
RWr19		(Unused)	RW			(Unused)			
RWr20	-	(Unused)	RW			(Unused)			
RWr21	-	(Unused)	RW			(Unused)			
RWr22	-	(Unused)	RW			(Unused)			
RWr23		(Unused)	RW			(Unused)			
RWr24	ļ	(Unused)	RW			(Unused)			
RWr25		(Unused)	RW			(Unused)			
RWr26		(Unused)	RW	v26		(Unused)			
RWr27		(Unused)	RW	v27		(Unused)			
RWr28		(Unused)	RW	v28		(Unused)			
RWr29		(Unused)	RW	v29		(Unused)			
RWr30		(Unused)	RW			(Unused)			
RWr31	1	(Unused)	RW			(Unused)			

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Profile number 33 (User profile [initial value: UM33A, 8 connected]) (Ver.2.00, 2-station occupied x8 setting)





Page 1

<u></u>	C Link of	IN area ave (UTAdvanced) → CC-Link master		OUT area CC-Link master → CC-Link slave (UTAdvanced)				
Word	Bit position	Contents of assignment	Word		Contents of assignment			
OSILIOII	RX0	Receive data valid	positio	RY0	Rescan request			
	RX1	During-write		RY1	(Reserved)			
	RX2	Write acknowledgement		RY2	Write request			
	RX3	(Reserved)		RY3	(Reserved)			
	RX4	(Reserved)		RY4	(Reserved)			
	RX5	(Reserved)		RY5	(Reserved)			
	RX6	(Reserved)		RY6	(Reserved)			
	RX7	(Reserved)		RY7	(Reserved)			
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)			
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)			
	RX48	01: ALM1_L1		RY48	(Unused)			
		01: ALM2_L1		RY49	(Unused)			
	RX50	01: ALM3_L1		RY50	(Unused)			
	RX51	01: ALM4_L1		RY51	(Unused)			
	RX52	01: ALM5_L1		RY52	(Unused)			
		01: ALM6_L1		RY53	(Unused)			
	RX54	01: ALM7_L1		RY54	(Unused)			
		01: ALM8_L1		RY55	(Unused)			
		02: ALM1_L1		RY56	(Unused)			
	RX57	(Unused)		RY57	(Unused)			
	RX58	(Unused)		RY58	(Unused)			
	RX59	(Unused)		RY59	(Unused)			
		(Unused)		RY60	(Unused)			
	RX61	(Unused)		RY61	(Unused)			
		(Unused)		RY62	(Unused)			
		(Unused)		RY63	(Unused)			
		02: ALM1_L1		RY64	(Unused)			
		02: ALM2_L1		RY65	(Unused)			
		02: ALM3_L1		RY66	(Unused)			
		02: ALM4_L1		RY67	(Unused)			
		02: ALM5_L1		RY68	(Unused)			
	RX69	02: ALM6_L1		RY69	(Unused)			
		02: ALM7_L1		RY70	(Unused)			
	RX71	02: ALM8_L1		RY71	(Unused)			

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Profile number 33 (User profile [initial value: UM33A, 8 connected]) on page 1 (Ver.2.00, 2-station occupied x8 setting)

IN area OUT area

IN area					
Word	C-Link sl	ave (UTAdvanced) → CC-Link master			
	position	Contents of assignment			
	RX72	(Unused)			
	RX73	(Unused)			
	RX74	(Unused)			
	RX75	(Unused)			
	RX76	(Unused)			
	RX77	(Unused)			
	RX78	(Unused)			
	RX79	(Unused)			
	RX80	03: ALM1_L1			
	RX81	03: ALM2_L1			
	RX82	03: ALM3_L1			
	RX83	03: ALM4_L1			
	RX84 RX85	03: ALM5_L1			
	RX86	03: ALM6_L1 03: ALM7 L1			
	RX87	03: ALM7_L1 03: ALM8_L1			
	RX88	(Unused)			
	RX89	(Unused)			
	RX90	(Unused)			
	RX91	(Unused)			
	RX92	(Unused)			
	RX93	(Unused)			
	RX94	(Unused)			
	RX95	(Unused)			
	RX96	04: ALM1_L1			
	RX97	04: ALM2_L1			
	RX98	04: ALM3_L1			
	RX99	04: ALM4_L1			
	RX100	04: ALM5_L1			
	RX101	04: ALM6_L1			
	RX102	04: ALM7_L1			
	RX103	04: ALM8_L1			
	RX104	(Unused)			
	RX105	(Unused)			
	RX106 RX107	(Unused)			
		(Unused)			
		(Unused)			
		(Unused)			
	RX111	(Unused)			
	RX112	05: ALM1_L1			
	i	05: ALM2_L1			
		05: ALM3_L1			
	RX115	05: ALM4_L1			
	RX116	05: ALM5_L1			
	RX117	05: ALM6_L1			
	RX118	05: ALM7_L1			
	RX119	05: ALM8_L1			
	-	(Unused)			
	RX121	(Unused)			
		(Unused)			
		(Unused)			
		(Unused)			
		(Unused)			
		(Unused)			
		(Unused)			
		06:ALM1_L1			
		06:ALM2_L1 06:ALM3_L1			
		06:ALM3_L1 06:ALM4_L1			
		06:ALM4_L1			
		06:ALM6_L1			
		06:ALM7_L1			
	RX135	06:ALM8_L1			

Word	C-Link m	aster → CC-Link slave (UTAdvanced)
vvora position		Contents of assignment
position	RY72	(Unused)
	RY73	(Unused)
		(Unused)
	RY74	(Unused)
	RY75	(Unused)
	RY76	(Unused)
	RY77	(Unused)
	RY78	(Unused)
	RY79	(Unused)
	RY80	(Unused)
	RY81	(Unused)
	RY82	(Unused)
	RY83	(Unused)
	RY84	(Unused)
	RY85	(Unused)
	RY86	(Unused)
	RY87	(Unused)
	RY88	· · · · · · · · · · · · · · · · · · ·
		(Unused)
	RY89	(Unused)
	RY90	(Unused)
	RY91	(Unused)
	RY92	(Unused)
	RY93	(Unused)
	RY94	(Unused)
	RY95	(Unused)
	RY96	(Unused)
	RY97	(Unused)
	RY98	(Unused)
	RY99	(Unused)
	RY100	(Unused)
	RY101	<u> </u>
	RY102	(Unused)
		(Unused)
	RY103	(Unused)
	RY104	(Unused)
	RY105	(Unused)
	RY106	(Unused)
	RY107	(Unused)
	RY108	(Unused)
	RY109	(Unused)
	RY110	(Unused)
	RY111	(Unused)
	RY112	(Unused)
	RY113	(Unused)
	RY114	
		(Unused)
	RY116	(Unused)
		· · · · · · · · · · · · · · · · · · ·
	RY117	(Unused)
	RY118	(Unused)
	RY119	(Unused)
	RY120	(Unused)
	RY121	(Unused)
	RY122	(Unused)
	RY123	(Unused)
	RY124	(Unused)
	RY125	(Unused)
	RY126	(Unused)
	RY127	(Unused)
	RY128	(Unused)
	RY129	(Unused)
		· · ·
	RY130	(Unused)
	RY131	(Unused)
	RY132	(Unused)
	RY133	(Unused)
	RY134	(Unused)
	RY135	(Unused)

Profile number 33 (User profile [initial value: UM33A, 8 connected]) on page 1 (Ver.2.00, 2-station occupied x8 setting) IN area **OUT** area CC-Link slave (UTAdvanced) → CC-Link master CC-Link master → CC-Link slave (UTAdvanced) Word Word Bit Bit Contents of assignment Contents of assignment position position position position RY136 (Unused) RX136 (Unused) RX137 (Unused) RY137 (Unused) RX138 (Unused) RY138 (Unused) RX139 (Unused) RY139 (Unused) RX140 (Unused) RY140 (Unused) RY141 (Unused) RX141 (Unused) RY142 (Unused) RX142 (Unused) RX143 (Unused) RY143 (Unused) RX144 07: ALM1_L1 RY144 (Unused) RY145 (Unused) RX145 07: ALM2_L1 RX146 07: ALM3_L1 RY146 (Unused) RX147 07: ALM4_L1 RY147 (Unused) RX148 07: ALM5_L1 RY148 (Unused) RX149 07: ALM6_L1 RY149 (Unused) RX150 07: ALM7 L1 RY150 (Unused) RX151 07: ALM8_L1 RY151 (Unused) RX152 (Unused) RY152 (Unused) RX153 (Unused) RY153 (Unused) RX154 (Unused) RY154 (Unused) RX155 (Unused) RY155 (Unused) RX156 (Unused) RY156 (Unused) RX157 (Unused) RY157 (Unused) RX158 (Unused) RY158 (Unused) RX159 (Unused) RY159 (Unused) RX160 08: ALM1_L1 RY160 (Unused) RX161 08: ALM2_L1 RY161 (Unused) RX162 08: ALM3 L1 RY162 (Unused) RX163 08: ALM4_L1 RY163 (Unused) RX164 08: ALM5_L1 RY164 (Unused) RX165 08: ALM6_L1 RY165 (Unused) RX166 08: ALM7_L1 RY166 (Unused) RX167 08: ALM8_L1 RY167 (Unused) RX168 (Unused) RY168 (Unused) RX169 (Unused) RY169 (Unused) RX170 (Unused) RY170 (Unused) RX171 (Unused) RY171 (Unused) RX172 (Unused) RY172 (Unused) RX173 (Unused) RY173 (Unused) RX174 (Unused) RY174 (Unused) RX175 (Unused) RY175 (Unused) RX176 (Unused) RY176 (Unused) : : RX367 (Unused) RY367 (Unused) RX368 (Reserved) RY368 (Reserved) RX379 Remote Ready RY379 (Reserved) RX383 (Reserved) RY383 (Reserved)

RWr0	Current page
RWr1	01: PV_L1
RWr2	02: PV_L1
RWr3	03: PV_L1
RWr4	04: PV_L1
RWr5	05: PV_L1
RWr6	06: PV_L1
RWr7	07: PV_L1
RWr8	08: PV_L1
RWr9	01: PEAK_L1
RWr10	02: PEAK_L1
RWr11	03: PEAK_L1
RWr12	04: PEAK_L1
RWr13	05: PEAK_L1

RWw0	Page change request
RWw1	(Unused)
RWw2	(Unused)
RWw3	(Unused)
RWw4	(Unused)
RWw5	(Unused)
RWw6	(Unused)
RWw7	(Unused)
RWw8	(Unused)
RWw9	(Unused)
RWw10	(Unused)
RWw11	(Unused)
RWw12	(Unused)
RWw13	(Unused)

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		33 (User profile [initial value: UM33A, 8 IN area		- P-9-1	OUT area
С	C-Link sl	ave (UTAdvanced) → CC-Link master		C-Link m	aster → CC-Link slave (UTAdvanced)
Vord	Bit	Contents of assignment	Word	Bit	Contents of assignment
sition Wr14	position	5	position RWw14	position	
Vr14 Vr15		(Unused)			(Unused)
Vr15 Vr16		(Unused)	RWw15		(Unused)
Nr17		08: PEAK_L1	RWw17		(Unused)
Nr18		01: BOTM_L1	RWw17		l /
Vr19		02: BOTM_L1 03: BOTM_L1	RWw19		(Unused)
Vr20		04: BOTM_L1	RWw20		(Unused)
Vr21		05: BOTM_L1	RWw21		(Unused)
Vr22		06: BOTM_L1	RWw21		(Unused)
Vr23		07: BOTM_L1	RWw23		(Unused)
Vr24		08: BOTM_L1	RWw24		(Unused)
Vr25		(Unused)	RWw24		(Unused)
Vr26		(Unused)	RWw26		(Unused)
Vr27		(Unused)	RWw27		(Unused)
Vr28		(Unused)	RWw28		(Unused)
/r29		(Unused)	RWw29		(Unused)
Vr30		(Unused)	RWw30		(Unused)
Vr31		(Unused)	RWw31		(Unused)
Nr32		(Unused)	RWw32		(Unused)
Vr33		(Unused)	RWw32		(Unused)
Vr34		(Unused)	RWw34		(Unused)
Vr35		(Unused)	RWw35		(Unused)
Vr36		(Unused)	RWw36		(Unused)
/r37		(Unused)	RWw37		(Unused)
Vr38		(Unused)	RWw38		(Unused)
Vr39		(Unused)	RWw39		(Unused)
/r40		(Unused)	RWw40		(Unused)
Vr41		(Unused)	RWw41		(Unused)
Vr42		(Unused)	RWw42		(Unused)
Vr43		(Unused)	RWw43		(Unused)
Vr44		(Unused)	RWw44		(Unused)
Vr45		(Unused)	RWw45		(Unused)
Vr46		(Unused)	RWw45		(Unused)
Vr47		(Unused)	RWw47		(Unused)
/r48		(Unused)	RWw48	1	(Unused)
r49		(Unused)	RWw49		(Unused)
/r50		(Unused)	RWw50		(Unused)
Vr51		(Unused)	RWw51		(Unused)
Vr52		(Unused)	RWw52		(Unused)
Vr53		(Unused)	RWw53		(Unused)
/r54		(Unused)	RWw54	_	(Unused)
Vr55		(Unused)	RWw55	+	(Unused)
/r56		(Unused)	RWw56	+	(Unused)
Vr57		(Unused)	RWw57	+	(Unused)
Vr58		(Unused)	RWw58		(Unused)
Vr59		(Unused)	RWw59	+	(Unused)
Vr60		(Unused)	RWw60	+	(Unused)
Vr61		(Unused)	RWw61		(Unused)
/r62		(Unused)	RWw62		(Unused)
Vr63	1	(Unused)	RWw63	+	(Unused)

_	C Link of	IN area ave (UTAdvanced) → CC-Link master		CC Link	OUT area naster → CC-Link slave (UTAdvanced)
Nord	Bit	Contents of assignment	Wor	d Bit	Contents of assignment
sition	position RX0	Receive data valid	position	on positio RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)			The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:				
	RX367	(Unused)	1	RY367	(Unused)
		(Reserved)		RY368	/
	:			:	
	RX370	Remote Ready		RY379	(Reserved)
	:	- tomoto rioday		:	(
	•	(Reserved)		DVoc	(Paganyad)
	KASOS	(Reserved)		K1303	(Reserved)
RWr0		Current page	RWw	0	Page change request
RWr1		01: A1_L1	RWw	_	01: A1_L1
RWr2		02: A1_L1	RWw	2	02: A1_L1
RWr3		03: A1_L1	RWw		03: A1_L1
RWr4		04: A1_L1	RWw	4	04: A1_L1
RWr5		05: A1_L1	RWw		05: A1_L1
RWr6		06: A1_L1	RWw	6	06: A1_L1
RWr7		07: A1_L1	RWw	7	07: A1_L1
RWr8		08: A1_L1	RWw	8	08: A1_L1
RWr9		01: A2_L1	RWw	9	01: A2_L1
RWr10		02: A2_L1	RWw	10	02: A2_L1
RWr11		03: A2_L1	RWw	11	03: A2_L1
RWr12		04: A2_L1	RWw	12	04: A2_L1
RWr13		05: A2_L1	RWw	13	05: A2_L1
RWr14		06: A2_L1	RWw	14	06: A2_L1
RWr15		07: A2_L1	RWw	15	07: A2_L1
RWr16		08: A2_L1	RWw		08: A2_L1
RWr17		01: A3_L1	RWw		01: A3_L1
RWr18		02: A3_L1	RWw		02: A3_L1
RWr19		03: A3_L1	RWw		03: A3_L1
RWr20		04: A3_L1	RWw:		04: A3_L1
RWr21		05: A3_L1	RWw:		05: A3_L1
RWr22		06: A3_L1	RWw:	_	06: A3_L1
RWr23		07: A3_L1	RWw:		07: A3_L1
RWr24		08: A3_L1	RWw:		08: A3_L1
RWr25 RWr26		01: A4_L1	RWw:	_	01: A4_L1
RWr27		02: A4_L1	RWw:		02: A4_L1
		03: A4_L1	RWw	_	03: A4_L1
RWr28 RWr29	-	04: A4_L1	RWw		04: A4_L1
		05: A4_L1	RWw:	_	05: A4_L1
RWr30 RWr31	-	06: A4_L1	RWw;		06: A4_L1
RWr32		07: A4_L1 08: A4_L1	RWw:	_	07: A4_L1
RWr33		(Unused)	RWw:		08: A4_L1 (Unused)
		(Chaosa)	-		(Citabou)
:			L:		
Wr63		(Unused)	RWw	63	(Unused)

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Page 3

:

RWr63

(Unused)

		IN area				OUT area
С	C-Link sl	ave (UTAdvanced) → CC-Link master		С	C-Link m	aster → CC-Link slave (UTAdvanced)
Word osition	Bit position	Contents of assignment		Word	Bit position	Contents of assignment
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)			RY48	(Unused)
	•	(\dashv		•	(
	:		_		:	
		(Unused)	_		RY367	(Unused)
	RX368	(Reserved)			RY368	(Reserved)
					:	
	RX379	Remote Ready			RY379	(Reserved)
	:				:	
	RX383	(Reserved)			-	(Reserved)
RWr0		Current page		RWw0		Page change request
RWr1		01: A5_L1		RWw1		01: A5_L1
RWr2		02: A5_L1	7	RWw2		02: A5_L1
RWr3		03: A5_L1	7	RWw3		03: A5_L1
RWr4		04: A5_L1	7	RWw4		04: A5_L1
RWr5		05: A5_L1		RWw5		05: A5_L1
RWr6		06: A5_L1		RWw6		06: A5_L1
RWr7		07: A5_L1		RWw7		07: A5_L1
RWr8		08: A5_L1	7	RWw8		08: A5_L1
RWr9		01: A6_L1	1	RWw9		01: A6_L1
RWr10		02: A6_L1	7	RWw10		02: A6_L1
RWr11		03: A6_L1	-	RWw11		03: A6_L1
RWr12		04: A6_L1	-	RWw12		04: A6_L1
RWr13		05: A6_L1	\dashv	RWw13		05: A6_L1
RWr14		06: A6_L1	-	RWw14		06: A6_L1
RWr15		07: A6_L1	\dashv	RWw15		07: A6_L1
RWr16		08: A6_L1		RWw16		08: A6 L1
RWr17		01: A7_L1		RWw17		01: A7_L1
RWr18		02: A7_L1		RWw18		02: A7_L1
RWr19		03: A7_L1		RWw19	 	03: A7_L1
RWr20		04: A7_L1		RWw20		04: A7_L1
RWr21		05: A7_L1	+	RWw21		05: A7 L1
RWr22		06: A7_L1	+	RWw22		06: A7_L1
RWr23		07: A7_L1		RWw23	-	07: A7_L1
RWr24		08: A7_L1		RWw24		08: A7_L1
RWr25		01: A8_L1	+	RWw25	-	01: A8_L1
RWr26		02: A8_L1		RWw26		02: A8_L1
RWr27		03: A8_L1		RWw27		
RWr28		_		RWw28		03: A8_L1
RWr29		04: A8_L1	+	RWw29		04: A8_L1
		05: A8_L1	+			05: A8_L1
RWr30		06: A8_L1		RWw30		06: A8_L1
RWr31		07: A8_L1	+	RWw31		07: A8_L1
RWr32		08: A8_L1	-	RWw32		08: A8_L1
RWr33	I	(Unused)		RWw33	I	(Unused)

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RWw63

(Unused)

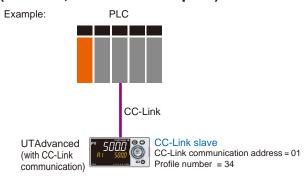
-		IN area		_		OUT area
		ave (UTAdvanced) → CC-Link master				aster → CC-Link slave (UTAdvanced)
Word	Bit position	Contents of assignment		Word	Bit position	Contents of assignment
, cition	RX0	Receive data valid		podition	RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-part is omitted (See profile number 0 on page 1)
	•				•	
	RX47	Normal connection slave (address 32)			RY47	Batch write request (address 32)
	RX48	(Unused)	4		RY48	(Unused)
	:				:	
	RX367	(Unused)			RY367	(Unused)
	RX368	(Reserved)			RY368	(Reserved)
	RX379	Remote Ready			RY379	(Reserved)
	:	,			:	
	PX383	(Reserved)			PV383	(Reserved)
	11/1000	(Neserved)			1000	(Ineserved)
RWr0		Current page		RWw0		Page change request
RWr1		(Unused)	-	RWw1		(Unused)
RWr2		(Unused)	1	RWw2		(Unused)
RWr3		(Unused)	1	RWw3		(Unused)
RWr4		(Unused)	1	RWw4		(Unused)
RWr5		(Unused)		RWw5		(Unused)
RWr6		(Unused)	1	RWw6		(Unused)
RWr7		(Unused)		RWw7		(Unused)
RWr8		(Unused)		RWw8		(Unused)
RWr9		(Unused)		RWw9		(Unused)
RWr10		(Unused)		RWw10		(Unused)
RWr11		(Unused)		RWw11		(Unused)
RWr12		(Unused)		RWw12		(Unused)
RWr13		(Unused)		RWw13		(Unused)
RWr14		(Unused)		RWw14		(Unused)
RWr15		(Unused)		RWw15		(Unused)
RWr16		(Unused)		RWw16		(Unused)
RWr17		(Unused)		RWw17		(Unused)
RWr18		(Unused)		RWw18		(Unused)
RWr19		(Unused)		RWw19		(Unused)
RWr20		(Unused)		RWw20		(Unused)
RWr21		(Unused)		RWw21		(Unused)
RWr22		(Unused)		RWw22		(Unused)
RWr23		(Unused)		RWw23		(Unused)
RWr24		(Unused)		RWw24		(Unused)
RWr25		(Unused)		RWw25		(Unused)
RWr26		(Unused)		RWw26		(Unused)
RWr27		(Unused)		RWw27		(Unused)
RWr28		(Unused)		RWw28		(Unused)
RWr29		(Unused)		RWw29		(Unused)
RWr30		(Unused)		RWw30		(Unused)
RWr31		(Unused)		RWw31		(Unused)
RWr32		(Unused)		RWw32		(Unused)
RWr33		(Unused)		RWw33		(Unused)
:				:		
			-	RWw63		(Unused)

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Intentionally blank

Profile number 34 (User profile [initial value: UM33A, 3 connected]) (Ver.1.10, 4-station occupied)





Page 1

Word position	Bit	IN area ave (UTAdvanced) → CC-Link master			OUT area
Word	Bit	ave (UTAdvanced) → CC-Link master			
		210 (011121211002)			aster → CC-Link slave (UTAdvanced)
	position	Contents of assignment	Word	Bit position	Contents of assignment
		Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•			•	T. 6 1 1: 1: 1: 1:
		The fixed-part is omitted			The fixed-part is omitted
	•	(See profile number 0 on page 1)			(See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
		01: ALM1_L1		RY48	01: A.M_L1
	RX49	01: ALM2_L1		RY49	01: R.L_L1
	RX50	01: ALM3_L1		RY50	01: S.R_L1
		01: ALM4_L1		RY51	(Unused)
	RX52	01: ALM5_L1		RY52	(Unused)
	RX53	01: ALM6_L1		RY53	(Unused)
	RX54	01: ALM7_L1		RY54	(Unused)
		01: ALM8_L1		RY55	(Unused)
		(Unused)		RY56	(Unused)
	RX57	(Unused)		RY57	(Unused)
	RX58	(Unused)		RY58	(Unused)
	RX59	(Unused)		RY59	(Unused)
	RX60	(Unused)		RY60	(Unused)
	RX61	(Unused)		RY61	(Unused)
	RX62	(Unused)		RY62	(Unused)
	RX63	(Unused)		RY63	(Unused)
		(Unused)		RY64	(Unused)
	:			:	
\longrightarrow	•				
		(Unused)		RY79	(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	:			:	
	RX91	Remote Ready		RY91	(Reserved)
	:			:	
	RX95	(Reserved)		RY95	(Reserved)
					,
RWr0		Current page	RWw0		Page change request
RWr1		01:PV_L1	RWw1		(Unused)
RWr2		01:PEAK_L1	RWw2		(Unused)
RWr3		01:BOTM_L1	RWw3		(Unused)
RWr4		(Unused)	RWw4		(Unused)
:		,	:		,
RWr11		(Unused)	RWw11		(Unused)

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Profile	numbe	r 34 (User profile [initial value: UM33A, 1	connected	dl) on pa	ge 2 (Ver.1.10, 3-station occupied)
		IN area		47 - 1	OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master	С	C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX79	(Unused)		RY79	(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	:			:	
	RX91	Remote Ready		RY91	(Reserved)
	:			:	
	RX95	(Reserved)		RY95	(Reserved)
RWr0		Current page	RWw0		Page change request
RWr1		01:A1_L1	RWw1		01:A1_L1
RWr2		01:A2_L1	RWw2		01:A2_L1
RWr3		01:A3_L1	RWw3		01:A3_L1
RWr4		01:A4_L1	RWw4		01:A4_L1
RWr5		01:A5_L1	RWw5		01:A5_L1
RWr6		01:A6_L1	RWw6		01:A6_L1
RWr7		01:A7_L1	RWw7		01:A7_L1
RWr8		01:A8_L1	RWw8		01:A8_L1
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

		IN area			OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master	с	C-Link m	aster → CC-Link slave (UTAdvanced)
Word position	Bit position	Contents of assignment	Word position	Bit position	Contents of assignment
	RX0	Receive data valid		RY0	Rescan request
	RX1	During-write		RY1	(Reserved)
	RX2	Write acknowledgement		RY2	Write request
	RX3	(Reserved)		RY3	(Reserved)
	RX4	(Reserved)		RY4	(Reserved)
	RX5	(Reserved)		RY5	(Reserved)
	RX6	(Reserved)		RY6	(Reserved)
	RX7	(Reserved)		RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)		•	The fixed-part is omitted (See profile number 0 on page 1)
	RX47	Normal connection slave (address 32)		RY47	Batch write request (address 32)
	RX48	(Unused)		RY48	(Unused)
	:			:	
	RX79	(Unused)		RY79	(Unused)
	RX80	(Reserved)		RY80	(Reserved)
	:			:	
	RX91	Remote Ready		RY91	(Reserved)
	:			:	
	RX95	(Reserved)		RY95	(Reserved)
DIA (o			D14/ 0		
RWr0		Current page	RWw0		Page change request
RWr1		(Unused)	RWw1		(Unused)
RWr2		(Unused)	RWw2		(Unused)
RWr3		(Unused)	RWw3	-	(Unused)
RWr4		(Unused)	RWw4		(Unused)
RWr5		(Unused)	RWw5		(Unused)
RWr6		(Unused)	RWw6		(Unused)
RWr7		(Unused)	RWw7	-	(Unused)
RWr8		(Unused)	RWw8		(Unused)
RWr9		(Unused)	RWw9		(Unused)
RWr10		(Unused)	RWw10		(Unused)
RWr11		(Unused)	RWw11		(Unused)

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Profile	numbe	r 34 (User profile [initial value: UM33A	1 co	nnected	l]) on pa	ge 4 (Ver.1.10, 3-st
		IN area				OUT area
C	C-Link sl	ave (UTAdvanced) → CC-Link master			C-Link m	aster \rightarrow CC-Link slave (l
Word position	Bit position	Contents of assignment		Word position	Bit position	Contents of as
	RX0	Receive data valid			RY0	Rescan request
	RX1	During-write			RY1	(Reserved)
	RX2	Write acknowledgement			RY2	Write request
	RX3	(Reserved)			RY3	(Reserved)
	RX4	(Reserved)			RY4	(Reserved)
	RX5	(Reserved)			RY5	(Reserved)
	RX6	(Reserved)			RY6	(Reserved)
	RX7	(Reserved)			RY7	(Reserved)
	•	The fixed-part is omitted (See profile number 0 on page 1)			•	The fixed-par (See profile numb
	RX47	Normal connection slave (address 32)			RY47	Batch write request (add
	RX48	(Unused)	1		RY48	(Unused)
	:				:	
	RX79	(Unused)	1		RY79	(Unused)
	RX80	(Reserved)			RY80	(Reserved)
	:				:	
	RX91	Remote Ready			RY91	(Reserved)
	:					
	RX95	(Reserved)			RY95	(Reserved)
RWr0		Current name	1	RWw0		Page change request
RWr1		Current page	-	RWw1		0 0 1
RWr2		(Unused)	-	RWw2		(Unused)
RWr2 RWr3		(Unused)	-	RWw2		(Unused)
		(Unused)	-	RWw4		(Unused)
RWr4		(Unused)	-			(Unused)
RWr5		(Unused)	-	RWw5		(Unused)
RWr6		(Unused)	-	RWw6		(Unused)
RWr7		(Unused)	-	RWw7		(Unused)
RWr8		(Unused)	-	RWw8		(Unused)
RWr9		(Unused)	4	RWw9		(Unused)
RWr10		(Unused)	-	RWw10		(Unused)
RWr11		(Unused)		RWw11		(Unused)

4.10 Changing Automatic Rescan Time (SCAN in CC-L Menu)

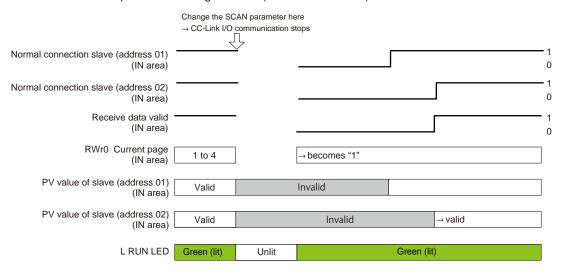
When the automatic rescan time setting is changed, UTAdvanced will perform operation in the following ways.

- (1) Stops the CC-L I/O communication.
- (2) Sets the timer according to the SCAN parameter value.
- (3) Restarts the CC-L I/O communication.
- (4) Sets the current page of the profile to 1 and restarts Modbus communication.

The automatic rescan time is set by the SCAN parameter in the CC-Link Communication Settings menu (CC-L).

- ► Setting SCAN parameters: "2.1.3 Setting CC-Link Communication (CC-Link Slave/Modbus Master)" in this manual
- ▶ 4.1 Overview: "Example: CC-Link Communication Connection" in this manual

Example of connecting 2 slaves (address 01 and 02):



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4.11 Changing Profile Number (FILE in CC-L Menu)

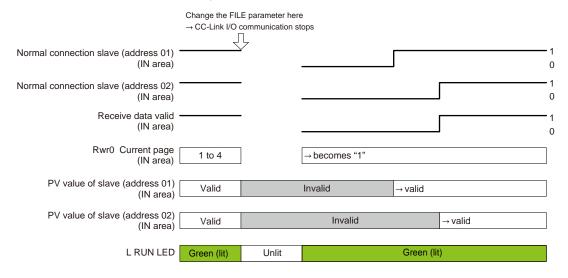
The profile number is set by the FILE parameter in the CC-L Communication Settings menu (CC-L).

- Setting FILE parameters: "2.1.3 Setting CC-L Communication (CC-L Slave/Modbus Master)" in this manual
- ▶ 4.1 Overview: "Example: CC-Link Communication Connection" in this manual

When the profile number is changed, the CC-L I/O size changes. If the profile number (I/O size) matches that set in the CC-Link master, a connection can be established with the CC-Link master after the change is made. If it does not match, a connection cannot be established.

The following figure shows a case where the I/O size does not change after the profile number is changed. Since the I/O size usually changes, the CC-Link communication is disconnected either before or after, or both before and after the change (L ERR LED: blinking red).

Example of connecting 2 slaves (address 01 and 02):



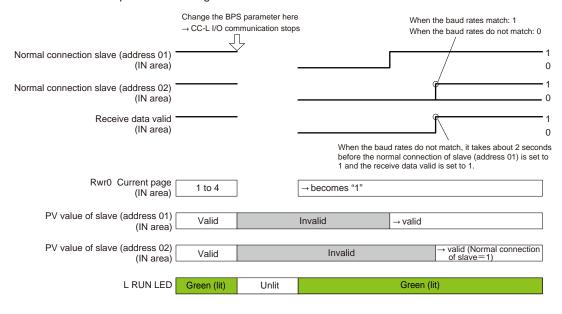
4.12 Changing RS-485 Baud Rate (BPS in CC-L Menu)

Make sure that the RS-485 baud rates of the Modbus master and Modbus slaves are identical.

The RS-485 baud rate of the Modbus master is set by the BPS parameter in the CC-Link Communication Settings menu (CC-L).

- ► Setting BPS parameters: "2.1.3 Setting CC-link Communication (CC-L Slave/Modbus Master)" in this manual
- ▶ 4.1 Overview: "Example: CC-L Communication Connection" in this manual

Example of connecting 2 slaves address 01 and 02:



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4.13 PLC Memory Space

UTAdvanced that serves a CC-link slave occupies area of the memory space of the CC-Link master. The user needs to know from the perspective of a PLC where the data of UTAdvanced is assigned in the memory space.

Be careful because the size of the occupied memory space of the master varies depending on the profile number of UTAdvanced that serves as a CC-Link slave.

Revision Information

Title : UTAdvanced Series Communication Interface (Open Network)

User's Manual

Manual No. : IM 05P07A01-02EN

May 2009/1st Edition

Newly published

Sep. 2009/2nd Edition

Error correction

Jan. 2010/3rd Edition

Addition of UT35A/UT32A

June 2010/4th Edition

Addition of CC-Link communication

Aug. 2010/5th Edition

Addition of UP55A/UP35A and DeviceNet communication

Sep. 2010/6th Edition

Error correction

Jan. 2011/7th Edition

Error correction

Feb. 2013/8th Edition

Addition of UT75A

Apr. 2015/9th Edition

Addition of UT32A/UP32A/UM33A

■ Written by

Yokogawa Electric Corporation

■ Published by Yokogawa Electric Corporation

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Apr. '14